

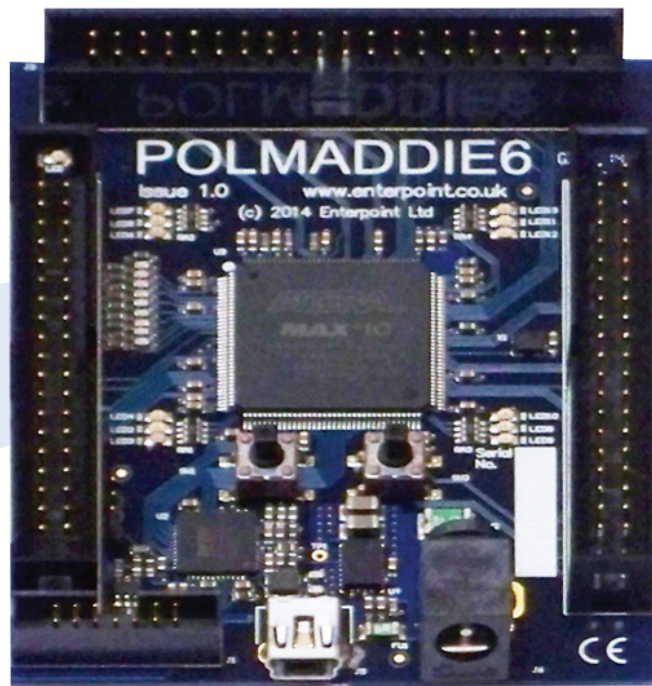
## Learn about HDL Languages and FPGAs with this Low-Cost MAX 10 FPGA Board



Enterpoint specializes in the design and manufacture of advanced FPGA centric boards, IP, systems and the supply of other FPGA related products such training and consultancy. Enterpoint partners with Altera to help you apply their leading technologies to your application.

To design with field programmable gate arrays (FPGAs) engineers typically use High-level Description Languages (HDL) for describing circuit functions within the device.

Take advantage of Enterpoint's free HDL training courses to learn how to design with the new Polmaddie6 dev kit, using a MAX 10 FPGA. Download course material [here](#).

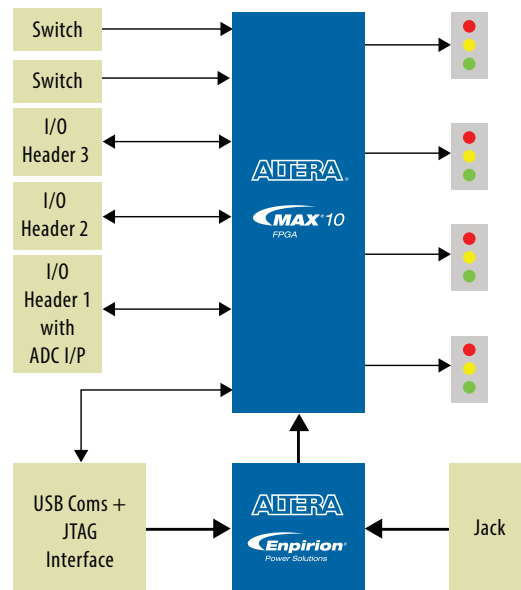


## Technical Details

- Altera MAX 10 FPGA (10M08S, 144 pin EQFP)
- Altera Enpirion Power Regulator
- 1 M/s, 12-bit ADC included in FPGA
- 60 General Purpose User I/O Available
- On board programming interface to FPGA

## Software Details

- Download Altera's Quartus II Web Edition and select "MAX 10 FPGA device support":  
<https://www.altera.com/download/sw/dnl-sw-index.jsp>



## Ordering Details

- p/n – PM6-10M08-C8
- \$40 (Approximate price, subject to change)
- [www.enterpoint.co.uk](http://www.enterpoint.co.uk)
- Business contact – [boardsales@enterpoint.co.uk](mailto:boardsales@enterpoint.co.uk)
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