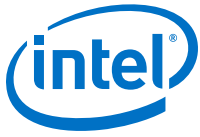




# Serial Lite IV Intel® FPGA IP Release Notes



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## 1. Serial Lite IV Intel® FPGA IP Release Notes

If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

The IP version (X.Y.Z) number may change from one Intel Quartus Prime software version to another. A change in:

- X indicates a major revision of the IP. If you update your Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

### Related Information

- [Intel Quartus Prime Design Suite Update Release Notes](#)
- [Introduction to Intel FPGA IP Cores](#)
- [Serial Lite IV Intel FPGA IP User Guide](#)
- [Serial Lite IV Intel Agilex™ FPGA IP Design Example User Guide](#)
- [Serial Lite IV Intel Stratix® 10 FPGA IP Design Example User Guide](#)
- [Errata for the Serial Lite IV Streaming Intel FPGA IP in the Knowledge Base](#)

### 1.1. Serial Lite IV Intel FPGA IP v1.2.0

**Table 1. v1.2.0 2020.04.13**

Intel Quartus Prime Version	Description	Impact
20.1	<ul style="list-style-type: none"> <li>• Added slave testing mode feature in the design example.</li> </ul>	—

**Table 2. v1.2.0 2019.12.16**

Intel Quartus Prime Version	Description	Impact
19.4	<ul style="list-style-type: none"> <li>• Added NRZ with RS-FEC error detection and correction feature.</li> <li>• Added hardware design example with Serial Lite IV toolkit support for Intel Agilex™ devices.</li> </ul>	—

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\*Other names and brands may be claimed as the property of others.



## 1.2. Serial Lite IV Intel FPGA IP v1.1.0

Table 3. v1.1.0 2019.09.30

Intel Quartus Prime Version	Description	Impact
19.3	<ul style="list-style-type: none"><li>Changed IP name to Serial Lite IV Intel FPGA IP.</li><li>Added support for Intel Agilex devices.</li><li>Added Non-return-to-zero (NRZ) modulation mode that can support up to 16 lanes with 28 Gbps data rate.</li><li>Added Cyclic Redundancy Check 32-bit (CRC32) feature.</li></ul>	—

## 1.3. Serial Lite IV Streaming Intel FPGA IP v1.0.0

Table 4. v1.0.0 2019.07.08

Intel Quartus Prime Version	Description	Impact
19.2	Initial release for Intel Stratix® 10 E-tile devices.	—

## 1.4. Serial Lite IV Intel FPGA IP User Guide Archives

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

If an IP core version is not listed, the user guide for the previous IP core version applies.

IP Core Version	User Guide
1.1.0	<a href="#">Serial Lite IV Streaming Intel FPGA IP User Guide</a>
1.0.0	<a href="#">Serial Lite IV Streaming Intel FPGA IP User Guide</a>

## 1.5. Serial Lite IV Intel Agilex FPGA IP Design Example User Guide Archives

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

If an IP core version is not listed, the user guide for the previous IP core version applies.

IP Core Version	User Guide
1.2.0	<a href="#">Serial Lite IV Intel Agilex FPGA IP Design Example User Guide</a>
1.1.0	<a href="#">Serial Lite IV Intel Agilex FPGA IP Design Example User Guide</a>

## 1.6. Serial Lite IV Intel Stratix 10 FPGA IP Design Example User Guide Archives

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.



If an IP core version is not listed, the user guide for the previous IP core version applies.

<b>IP Core Version</b>	<b>User Guide</b>
1.1.0	<a href="#">Serial Lite IV Intel Stratix 10 FPGA IP Design Example User Guide</a>
1.0.0	<a href="#">Serial Lite IV Streaming Intel Stratix 10 FPGA IP Design Example User Guide</a>