



# SerialLite II IP Core Release Notes

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## 1 SerialLite II Release Notes

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If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

### Related Links

[Intel Quartus Prime Design Suite Update Release Notes](#)

### 1.1 SerialLite II v17.0

**Table 1. v17.0 May 2017**

Description	Impact
Verified in the Quartus Prime software v17.0.	-

### Related Links

- [Introduction to Intel FPGA IP Cores](#)
- [SerialLite II IP Core User Guide](#)
- [Errata for SerialLite II IP Core in the Knowledge Base](#)

### 1.2 SerialLite II v16.1

**Table 2. v16.1 October 2016**

Description	Impact
Verified in the Quartus Prime software v16.1.	-

### Related Links

- [Introduction to Intel FPGA IP Cores](#)
- [SerialLite II IP Core User Guide](#)
- [Errata for SerialLite II IP Core in the Knowledge Base](#)

### 1.3 SerialLite II v16.0

**Table 3. v16.0 May 2016**

Description	Impact
Arria 10 devices are indirectly supported by the SerialLite II IP core version 15.0 and later. If your design needs to implement the SerialLite II interface in Arria 10 devices, contact your local Altera representative or file a Service Request (SR) to obtain a design example, a guideline document, and a special license to enable the Quartus Prime software to generate the FPGA configuration file (.sof).	-

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**Related Links**

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