



Mailbox Client Intel[®] FPGA IP Release Notes



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1. Mailbox Client Intel® FPGA IP Release Notes

IP versions are the same as the Intel® Quartus® Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

The IP version (X.Y.Z) number may change from one Intel Quartus Prime software version to another. A change in:

- X indicates a major revision of the IP. If you update your Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

Related Information

- [Introduction to Altera IP Cores](#)
- [Mailbox Client Intel FPGA IP User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)
- [Intel Quartus Prime Design Suite Version 18.1 Update Release Notes](#)

1.1. Mailbox Client Intel FPGA IP v20.0.0

Table 1. v20.0.0 2020.04.13

Intel Quartus Prime Version	Description	Impact
20.1	Added support for the EOP_TIMEOUT interrupt which indicates that the full command did not include the End of Packet.	You can use these interrupts to handle error detection for incomplete transactions.
	Added support for the BACKPRESSURE_TIMEOUT interrupt which indicates that an error within the SDM occurred.	



1.2. Mailbox Client Intel FPGA IP v19.3

Table 2. v19.3 2019.09.30

Intel Quartus Prime Version	Description	Impact
19.3	Added device support for Intel Agilex™ devices.	You can now use this IP in Intel Agilex devices.
	Added support for an <code>COMMAND_INVALID</code> interrupt which indicates the command length specified the header does not match the actual command sent.	You can use this interrupt to identify incorrectly specified commands.
	Changed the name of this IP from Intel FPGA Stratix 10 Mailbox Client to Mailbox Client Intel FPGA IP.	This IP now supports both Intel Stratix® 10 and Intel Agilex devices. Use the new name to find this IP in the Intel Quartus Prime software or on the web.
	Added new IP version structure.	The IP version number may change from one Intel Quartus Prime software version to another.

1.3. Intel FPGA Stratix 10 Mailbox Client v17.1

Table 3. v17.1 2017.10.30

Intel Quartus Prime Version	Description	Impact
17.1	Initial release.	—