



Native Fixed Point DSP Intel Stratix 10 FPGA IP Core Release Notes



Contents

Native Fixed Point DSP Intel Stratix 10 FPGA IP Core Release Notes.....	3
Native Fixed Point DSP Intel Stratix 10 FPGA IP v19.1.0.....	3
Native Fixed Point DSP Intel Stratix 10 FPGA IP Core v18.0.....	3
Stratix 10 Native Fixed Point DSP IP Core v17.1.....	4
Intel Stratix 10 Variable Precision DSP Blocks User Guide Document Archives.....	4



Native Fixed Point DSP Intel Stratix 10 FPGA IP Core Release Notes

If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

The IP version (X.Y.Z) number may change from one Intel Quartus Prime software version to another. A change in:

- X indicates a major revision of the IP. If you update your Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

Related Information

- [Intel Quartus Prime Design Suite Update Release Notes](#)
- [Variable Precision DSP Blocks User Guide](#)

Native Fixed Point DSP Intel Stratix 10 FPGA IP v19.1.0

Table 1. v19.1.0 2020.09.28

Intel Quartus Prime Version	Description	Impact
20.3	Added "X" propagation support in simulation model.	—

Native Fixed Point DSP Intel Stratix 10 FPGA IP Core v18.0

Table 2. v18.0 May 2018

Description	Impact
Renamed Stratix 10 Fixed Point DSP IP core to Native Fixed Point DSP Intel Stratix 10 FPGA IP as per Intel rebranding.	—

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [Intel Stratix® 10 Variable Precision DSP Blocks User Guide](#)

Intel Corporation. All rights reserved. Agilix, Altera, Arria, Cyclone, Enpirion, Intel, the Intel logo, MAX, Nios, Quartus and Stratix words and logos are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries. Intel warrants performance of its FPGA and semiconductor products to current specifications in accordance with Intel's standard warranty, but reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.

*Other names and brands may be claimed as the property of others.



- [Errata for other IP cores in the Knowledge Base](#)

Stratix 10 Native Fixed Point DSP IP Core v17.1

Table 3. v17.1 November 2017

Description	Impact
Initial release. This IP is available only in Intel Stratix® 10 devices.	—

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [Intel Stratix 10 Variable Precision DSP Blocks User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)

Intel Stratix 10 Variable Precision DSP Blocks User Guide Document Archives

If the table does not list a software version, the user guide for the previous software version applies.

Intel Quartus Prime Version	User Guide
19.3	Intel Stratix 10 Variable Precision DSP Blocks User Guide
18.1	Intel Stratix 10 Variable Precision DSP Blocks User Guide
17.1	Intel Stratix 10 Variable Precision DSP Blocks User Guide