DDR2 and DDR3 SDRAM Controller with UniPHY Intel® FPGA IP Core Release Notes

2018.11.05

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If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

Related Information

Intel Quartus Prime Design Suite Update Release Notes

DDR2 and DDR3 SDRAM Controller with UniPHY Intel® FPGA IP Core v18.1

Table 1: v18.1 September 2018

Description	Impact
Verified in the Intel® Quartus® Prime software v18.1	-

Related Information

- External Memory Interface Handbook
- Errata for DDR3 SDRAM Controller with UniPHY IP core in the Knowledge Base

DDR2 and DDR3 SDRAM Controller with UniPHY Intel FPGA IP Core v18.0

Table 2: v18.0 May 2018

Description	Impact
Verified in the Intel Quartus Prime software v18.0	-

Related Information

- External Memory Interface Handbook
- Errata for DDR3 SDRAM Controller with UniPHY IP core in the Knowledge Base

DDR2 and DDR3 SDRAM Controller with UniPHY IP Core v17.1

Table 3: v17.1 November 2017

Description	Impact
Verified in the Intel Quartus Prime software v17.1.	-

Related Information

- External Memory Interface Handbook
- Errata for DDR3 SDRAM Controller with UniPHY IP core in the Knowledge Base

DDR2 and DDR3 SDRAM Controller with UniPHY IP Core v17.0

Table 4: v17.0 May 2017

Description	Impact
Verified in the Quartus Prime software v17.0	-

Related Information

- External Memory Interface Handbook
- Errata for DDR3 SDRAM Controller with UniPHY IP core in the Knowledge Base

DDR2 and DDR3 SDRAM Controller with UniPHY IP Core v16.1

Table 5: v16.1 November 2016

Description	Impact
Verified in the Quartus Prime software v16.1	-

Related Information

- External Memory Interface Handbook
- Errata for DDR3 SDRAM Controller with UniPHY IP core in the Knowledge Base