



CPRI IP Core Release Notes



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1. CPRI IP Core Release Notes

If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

Related Information

[Intel Quartus Prime Design Suite Update Release Notes](#)

1.1. CPRI v7.0 IP Core v17.1

Table 1. Version 17.1 January 2019

Description	Impact
First release of the CPRI v7.0 IP core.	—
Verified in the Intel Quartus Prime v17.1 software.	—
The Intel Stratix® 10 devices with H- and L- tile transceivers are now available in Intel Quartus Prime Pro Edition v17.1 for all CPRI line bit rates except 0.6144 Gbps.	—
Added new parameter: Data path width .	
Enable line bit rate auto-negotiation is now available for Intel Stratix 10 devices.	—
Added new hybrid clock source input.	—
Added Arria® V GZ device support for the CPRI line bit rate of 8.11008 Gbps.	—
Change the name of the IP in Intel Quartus Prime IP catalog to CPRI .	—

Related Information

- [CPRI v7.0 IP Core User Guide](#)
- [Errata for CPRI IP core in the Knowledge Base](#)



1.2. CPRI v6.0 IP Core v17.0

Table 2. Version 17.0 January 2018

Description	Impact
Verified in the Intel Quartus Prime v17.0 software.	—
The Intel Stratix 10 device support with with H- and L-tile transceivers are now available in Intel Quartus Prime Pro Edition v17.0 for CPRI line bit rates of 1.2288 Gbps and 10.1376 Gbps.	—
Removed Intel Arria 10 device family support for CPRI line bit rate of 0.6144 Gbps.	—
Added new parameter VCCR_GXB and VCCT_GXB supply voltage for the transceiver in Intel Stratix 10 IP core device variations.	—
Added following new registers and signals in Intel Stratix 10 IP core device variations: <ul style="list-style-type: none"> Added two new registers: XCVR_TX_FIFO_DELAY , XCVR_RX_FIFO_DELAY. Added two new signals: latency_sclk, latency_sreset_n 	—

Related Information

- [CPRI v6.0 IP Core User Guide](#)
- [Errata for CPRI IP core in the Knowledge Base](#)

1.3. CPRI v6.0 IP Core v14.1

Table 3. Version 14.1 December 2014

Description	Impact	Notes
Verified in the Quartus II software v14.1.	—	—

Related Information

- [CPRI MegaCore Function User Guide](#)
- [Errata for CPRI IP core in the Knowledge Base](#)

1.4. CPRI v6.0 IP Core v14.0

Table 4. Version 14.0 June 2014

Description	Impact	Notes
First release of the CPRI v6.0 IP core.	—	—
Upgraded to support the new IP Catalog. For more information about the IP Catalog, refer to <i>IP Catalog and Parameter Editor</i> in <i>Introduction to Altera IP Cores</i> .	—	—
Renamed Include Vendor Specific Space (VSS) access through CPU interface parameter to Include all control word access through CPU interface to better explain the function of the parameter. The parameter functionality remains as it was in the 13.1 and 13.0 releases.	—	—
Renamed Receiver buffer depth parameter to Receiver FIFO depth . The parameter functionality remains as it was in the 13.1 and 13.0 releases.	—	—

Related Information

- [Introduction to Altera IP Cores](#)
- [CPRI MegaCore Function User Guide](#)



- [Errata for CPRI IP core in the Knowledge Base](#)

1.5. CPRI v5.0 IP Core v13.1

Note: The CPRI IP core v5.0 is scheduled for product obsolescence and discontinued support as described in PDN1603. Therefore, Intel does not recommend use of this IP core in new designs. For more information about the current Intel FPGA IP offering, refer to the Intel FPGA Intellectual Property website.

Table 5. Version 13.1 November 2013

Description	Impact	Notes
Removed support for HardCopy IV GX device family.	—	—
Improved the demonstration testbench.	—	—
Improved resource utilization.	—	—

Related Information

- [Product Discontinuance Notification: PDN1603](#)
- [Intel FPGA Intellectual Property website](#)
- [CPRI MegaCore Function User Guide](#)
- [Errata for CPRI IP core in the Knowledge Base](#)

1.6. CPRI v5.0 IP Core v13.0

Table 6. Version 13.0 May 2013

Description	Impact	Notes
Added new parameter for user control of inclusion or exclusion of software interface to full control words.	—	—
Added new parameter for user control of inclusion or exclusion of round-trip delay calibration feature.	—	—
Reduced resource utilization in 28-nm devices.	—	—

Related Information

- [Product Discontinuance Notification: PDN1603](#)
- [CPRI MegaCore Function User Guide](#)
- [Errata for CPRI IP core in the Knowledge Base](#)