



OCT Intel[®] FPGA IP Release Notes



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1. OCT Intel® FPGA IP Release Notes

If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

The IP versioning scheme (X.Y.Z) number changes from one software version to another. A change in:

- X indicates a major revision of the IP. If you update your Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

Related Information

- [Intel Quartus Prime Design Suite Update Release Notes](#)
- [Introduction to Intel FPGA IP Cores](#)
- [Intel Agilex™ General Purpose I/O and LVDS SERDES User Guide](#)
- [Intel FPGA OCT IP Core User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)

1.1. OCT Intel FPGA IP v19.3.0

Table 1. v19.3.0 2019.09.30

| Intel Quartus Prime Version | Description | Impact |
|-----------------------------|--|--------|
| 19.3 | Initial release for Intel Agilex™ devices. | — |

1.2. OCT Intel FPGA IP v18.0

Table 2. v18.0 May 2018

| Description | Impact |
|---|--------|
| Renamed the IP core from "Intel FPGA OCT" to "OCT Intel FPGA IP". | — |

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Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [Intel FPGA OCT IP Core User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)

1.3. Intel FPGA OCT IP Core v17.1

Table 3. v17.1 November 2017

| Description | Impact |
|---|--------|
| Added support for Intel Stratix® 10 devices. | — |
| Renamed Altera OCT IP core to Intel FPGA OCT IP core as per Intel rebranding. | — |

Related Information

- [Introduction to Intel FPGA IP Cores](#)
- [Intel FPGA OCT IP Core User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)

1.4. Altera OCT IP Core v14.0 Arria 10 Edition

Table 4. v14.0 Arria 10 Edition August 2014

| Description | Impact |
|---|--------|
| <ul style="list-style-type: none"> • Changed signal names • Added user mode OCT | |

Table 5. Signal Name Changes

| Old Name | New Name | Notes |
|--|------------------------------------|-------|
| core_rzqin_export | rzqin | - |
| core_series_termination_control_export | oct_#_series_termination_control | - |
| core_parallel_termination_control_export | oct_#_parallel_termination_control | - |

Related Information

- [Altera OCT Megafunction User Guide](#)
- [Errata for other IP cores in the Knowledge Base](#)

1.5. User Guide Archives

If an IP core version is not listed, the user guide for the previous IP core version applies.

| IP Core Version | User Guide |
|-----------------|---|
| 17.1 | Intel FPGA OCT IP Core User Guide |