

TYPE	BANK	R24A Package
Transceiver I/O	10A	68
Transceiver I/O	11A	-
Transceiver I/O	1C	-
Transceiver I/O	1D	-
Transceiver I/O	1E	-
Transceiver I/O	1F	-
GPIO	2A	96
GPIO	2B	96
GPIO	2C	96
GPIO	2D	96
GPIO	3A	96
GPIO	3B	96
HPS shared GPIO	3C	96
HPS shared GPIO	3D	96
HPS shared LVDS I/O	HPS	82
SDM shared LVDS I/O	SDM	48
3V I/O	U10	29

- i. Total LVDS channels per bank supporting SERDES Non-DPA and DPA mode is equivalent to (LVDS I/O per bank)/2, inclusive of clock pair. Please refer to Dedicated Tx/Rx Channel column in the pin-out table for the channel availability.
- ii. Total LVDS channels supporting SERDES Soft-CDR mode is 12 pairs per bank. Please refer to Soft CDR column in the pin out table for the channel availability.

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
SDM			TD0						CR62				
SDM			TMS						CT61				
SDM			TCK						CU62				
SDM			TDI						CV61				
SDM			OSC_CLK_1						CC60				
SDM			SDM_ID0	PWRMGT_SCL					CF59				
SDM			SDM_ID1	AVSTx8_DATA2_AS_DATA1_SDMMMC_CFG_DATA1					CU60				
SDM			SDM_ID5	AS_nCS00_SDMMMC_CFG_CCLK_MSEL0					CR60				
SDM			SDM_ID3	AVSTx8_DATA3_AS_DATA2_SDMMMC_CFG_DATA2					CK59				
SDM			nCONFIG						CB57				
SDM			SDM_ID4	AVSTx8_DATA1_AS_DATA0_SDMMMC_CFG_CMD					CN60				
SDM			SDM_ID2	AVSTx8_DATA0_AS_CLK_SDMMMC_CFG_DATA0					CT59				
SDM			SDM_ID7	AS_nCS02_MSEL1					CY59				
SDM			SDM_ID11	AVSTx8_VALID_PWRMGT_SDA					CE60				
SDM			nSTATUS						CH59				
SDM			SDM_ID16	PWRMGT_SDA					CA60				
SDM			SDM_ID13	AVSTx8_DATAS_SDMMMC_CFG_DATAS					CB58				
SDM			SDM_ID9	AS_nCS01_MSEL2					CV59				
SDM			SDM_ID6	AVSTx8_DATA4_AS_DATA3_SDMMMC_CFG_DATA3					CM59				
SDM			SDM_ID10	AVSTx8_DATA7_SDMMMC_CFG_DATA7					CC58				
SDM			SDM_ID8	AVSTx8_READY_AS_nCS03_SDMMMC_CFG_DATA4					CL60				
SDM			SDM_ID12	PWRMGT_SDA					CC56				
SDM			SDM_ID15	AVSTx8_DATA6_SDMMMC_CFG_DATA6					CA58				
SDM			SDM_ID14	AVSTx8_CLK_PWRMGT_SCL					CG60				
SDM			RREF_SDM						CA54				
SDM			VSIGP_0						CG62				
SDM			VSIGN_0						CE62				
SDM			VSIGP_1						CN62				
SDM			VSIGN_1						CL62				
2D	95	VREFB2DNO	IO				DIFF_RX_2D1n	No	CT3	DQ0	DQ0	DQ0	DQ0
2D	94	VREFB2DNO	IO				DIFF_RX_2D1p	No	CV3	DQ0	DQ0	DQ0	DQ0
2D	93	VREFB2DNO	IO				DIFF_TX_2D1n	No	CM4	DQ0	DQ0	DQ0	DQ0
2D	92	VREFB2DNO	IO				DIFF_TX_2D1p	No	CU4	DQ0	DQ0	DQ0	DQ0
2D	91	VREFB2DNO	IO				DIFF_RX_2D2n	No	CT5	DQsn0	DQ0	DQ0	DQ0
2D	90	VREFB2DNO	IO				DIFF_RX_2D2p	No	CV5	DQsn0	DQ0	DQ0	DQ0
2D	89	VREFB2DNO	IO				DIFF_TX_2D2n	No	CR6	DQsn1	DQsn0/CQn0	DQ0	DQ0
2D	88	VREFB2DNO	IO				DIFF_TX_2D2p	No	CU6	DQsn1	DQsn0/CQ0	DQ0	DQ0
2D	87	VREFB2DNO	IO	CDR			DIFF_RX_2D3n	Yes	CT7	DQ1	DQ0	DQ0	DQ0
2D	86	VREFB2DNO	IO	CDR			DIFF_RX_2D3p	Yes	CV7	DQ1	DQ0	DQ0	DQ0
2D	85	VREFB2DNO	IO				DIFF_TX_2D3n	No	CR8	DQ1	DQ0	DQ0	DQ0
2D	84	VREFB2DNO	IO				DIFF_TX_2D3p	No	CU8	DQ1	DQ0	DQ0	DQ0
2D	83	VREFB2DNO	IO				DIFF_RX_2D4n	No	CT1	DQ2	DQ1	DQ0	DQ0
2D	82	VREFB2DNO	IO				DIFF_RX_2D4p	No	CV1	DQ2	DQ1	DQ0	DQ0
2D	81	VREFB2DNO	IO				DIFF_TX_2D4n	No	CY3	DQ2	DQ1	DQ0	DQ0
2D	80	VREFB2DNO	IO				DIFF_TX_2D4p	No	DA4	DQ2	DQ1	DQ0	DQ0
2D	79	VREFB2DNO	IO				DIFF_RX_2D5n	No	CY5	DQsn2	DQ1	DQ0	DQ0
2D	78	VREFB2DNO	IO				DIFF_RX_2D5p	No	D85	DQsn2	DQ1	DQ0	DQ0
2D	77	VREFB2DNO	IO	PLL_2D_T_CLKOUT1n			DIFF_TX_2D5n	No	DA6	DQsn3	DQsn1/CQn1	DQsn0/CQn0	DQ0
2D	76	VREFB2DNO	IO	PLL_2D_T_CLKOUT1p,PLL_2D_T_CLKOUT1,PLL_2D_T_FB1			DIFF_TX_2D5p	No	DC6	DQsn3	DQsn1/CQ1	DQsn0/CQ0	DQ0
2D	75	VREFB2DNO	IO	CDR			DIFF_RX_2D6n	Yes	CY7	DQ3	DQ1	DQ0	DQ0
2D	74	VREFB2DNO	IO	RZQ_T_2D.CDR			DIFF_RX_2D6p	Yes	D87	DQ3	DQ1	DQ0	DQ0
2D	73	VREFB2DNO	IO	CLK_T_2D_1n			DIFF_TX_2D6n	No	DA8	DQ3	DQ1	DQ0	DQ0
2D	72	VREFB2DNO	IO	CLK_T_2D_1p			DIFF_TX_2D6p	No	DC8	DQ3	DQ1	DQ0	DQ0
2D	71	VREFB2DNO	IO	CLK_T_2D_0n			DIFF_RX_2D7n	No	CR10	DQ4	DQ2	DQ1	DQ0
2D	70	VREFB2DNO	IO	CLK_T_2D_0p			DIFF_RX_2D7p	No	CU10	DQ4	DQ2	DQ1	DQ0
2D	69	VREFB2DNO	IO				DIFF_TX_2D7n	No	CT11	DQ4	DQ2	DQ1	DQ0
2D	68	VREFB2DNO	IO				DIFF_TX_2D7p	No	CV11	DQ4	DQ2	DQ1	DQ0
2D	67	VREFB2DNO	IO	PLL_2D_T_CLKOUT0n			DIFF_RX_2D8n	No	CR12	DQsn4	DQ2	DQ1	DQ0
2D	66	VREFB2DNO	IO	PLL_2D_T_CLKOUT0p,PLL_2D_T_CLKOUT0,PLL_2D_T_FB0			DIFF_RX_2D8p	No	CU12	DQsn4	DQ2	DQ1	DQ0
2D	65	VREFB2DNO	IO				DIFF_TX_2D8n	No	CT13	DQsn5	DQsn2/CQn2	DQ1	DQsn0/CQn0
2D	64	VREFB2DNO	IO				DIFF_TX_2D8p	No	CV13	DQsn5	DQsn2/CQ2	DQ1	DQsn0/CQ0
2D	63	VREFB2DNO	IO	CDR			DIFF_RX_2D9n	Yes	CR14	DQ5	DQ2	DQ1	DQ0
2D	62	VREFB2DNO	IO	CDR			DIFF_RX_2D9p	Yes	CU14	DQ5	DQ2	DQ1	DQ0
2D	61	VREFB2DNO	IO				DIFF_TX_2D9n	No	CT15	DQ5	DQ2	DQ1	DQ0
2D	60	VREFB2DNO	IO				DIFF_TX_2D9p	No	CV15	DQ5	DQ2	DQ1	DQ0
2D	59	VREFB2DNO	IO				DIFF_RX_2D10n	No	DA10	DQ6	DQ3	DQ1	DQ0
2D	58	VREFB2DNO	IO				DIFF_RX_2D10p	No	DC10	DQ6	DQ3	DQ1	DQ0
2D	57	VREFB2DNO	IO				DIFF_TX_2D10n	No	CY11	DQ6	DQ3	DQ1	DQ0
2D	56	VREFB2DNO	IO				DIFF_TX_2D10p	No	DB11	DQ6	DQ3	DQ1	DQ0
2D	55	VREFB2DNO	IO				DIFF_RX_2D11n	No	DA12	DQsn6	DQ3	DQ1	DQ0
2D	54	VREFB2DNO	IO				DIFF_RX_2D11p	No	DC12	DQsn6	DQ3	DQ1	DQ0
2D	53	VREFB2DNO	IO				DIFF_TX_2D11n	No	CY13	DQsn7	DQsn3/CQn3	DQsn1/CQn1	DQ0
2D	52	VREFB2DNO	IO				DIFF_TX_2D11p	No	DB13	DQsn7	DQsn3/CQ3	DQsn1/CQ1	DQ0
2D	51	VREFB2DNO	IO	CDR			DIFF_RX_2D12n	Yes	DA4	DQ7	DQ3	DQ1	DQ0
2D	50	VREFB2DNO	IO	CDR			DIFF_RX_2D12p	Yes	DC14	DQ7	DQ3	DQ1	DQ0
2D	49	VREFB2DNO	IO				DIFF_TX_2D12n	No	CY15	DQ7	DQ3	DQ1	DQ0
2D	48	VREFB2DNO	IO				DIFF_TX_2D12p	No	DB15	DQ7	DQ3	DQ1	DQ0
2D	47	VREFB2DNO	IO	CDR			DIFF_RX_2D13n	Yes	CF3	DQ8	DQ4	DQ2	DQ1
2D	46	VREFB2DNO	IO	CDR			DIFF_RX_2D13p	Yes	CH3	DQ8	DQ4	DQ2	DQ1
2D	45	VREFB2DNO	IO				DIFF_TX_2D13n	No	CE4	DQ8	DQ4	DQ2	DQ1
2D	44	VREFB2DNO	IO				DIFF_TX_2D13p	No	CG4	DQ8	DQ4	DQ2	DQ1
2D	43	VREFB2DNO	IO				DIFF_RX_2D14n	No	CF5	DQsn8	DQ4	DQ2	DQ1
2D	42	VREFB2DNO	IO				DIFF_RX_2D14p	No	CH5	DQsn8	DQ4	DQ2	DQ1
2D	41	VREFB2DNO	IO				DIFF_TX_2D14n	No	CE6	DQsn9	DQsn4/CQn4	DQ2	DQ1
2D	40	VREFB2DNO	IO				DIFF_TX_2D14p	No	CG6	DQsn9	DQsn4/CQ4	DQ2	DQ1
2D	39	VREFB2DNO	IO	CDR			DIFF_RX_2D15n	Yes	CF7	DQ9	DQ4	DQ2	DQ1
2D	38	VREFB2DNO	IO	CDR			DIFF_RX_2D15p	Yes	CH7	DQ9	DQ4	DQ2	DQ1
2D	37	VREFB2DNO	IO				DIFF_TX_2D15n	No	CE8	DQ9	DQ4	DQ2	DQ1
2D	36	VREFB2DNO	IO				DIFF_TX_2D15p	No	CG8	DQ9	DQ4	DQ2	DQ1
2D	35	VREFB2DNO	IO	CDR			DIFF_RX_2D16n	Yes	CK3	DQ10	DQ5	DQ2	DQ1















Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
3B	70	VREFB3BNO	IO	CLK_T_3B_0p			DIFF_RX_3B7n	No	G38	DQ100	DQ50	DQ25	DQ12
3B	69	VREFB3BNO	IO				DIFF_TX_3B7n	No	H37	DQ100	DQ50	DQ25	DQ12
3B	68	VREFB3BNO	IO				DIFF_TX_3B7p	No	F37	DQ100	DQ50	DQ25	DQ12
3B	67	VREFB3BNO	IO	PLL_3B_T_CLKOUT0n			DIFF_RX_3B8n	No	J36	DQ5n100	DQ50	DQ25	DQ12
3B	66	VREFB3BNO	IO	PLL_3B_T_CLKOUT0p,PLL_3B_T_CLKOUT0,PLL_3B_T_FB0			DIFF_RX_3B8p	No	G36	DQ5100	DQ50	DQ25	DQ12
3B	65	VREFB3BNO	IO				DIFF_TX_3B8n	No	H35	DQ5n101	DQ5n50/CQn50	DQ25	DQ5n12/CQn12
3B	64	VREFB3BNO	IO				DIFF_TX_3B8p	No	F35	DQ5101	DQ550/CQ50	DQ25	DQ512/CQ12
3B	63	VREFB3BNO	IO	CDR			DIFF_RX_3B89n	No	J34	DQ101	DQ50	DQ25	DQ12
3B	62	VREFB3BNO	IO	CDR			DIFF_RX_3B8p	Yes	G34	DQ101	DQ50	DQ25	DQ12
3B	61	VREFB3BNO	IO				DIFF_TX_3B9n	No	H33	DQ101	DQ50	DQ25	DQ12
3B	60	VREFB3BNO	IO				DIFF_TX_3B89p	No	F33	DQ101	DQ50	DQ25	DQ12
3B	59	VREFB3BNO	IO				DIFF_RX_3B10n	No	C38	DQ102	DQ51	DQ25	DQ12
3B	58	VREFB3BNO	IO				DIFF_RX_3B10p	No	A38	DQ102	DQ51	DQ25	DQ12
3B	57	VREFB3BNO	IO				DIFF_TX_3B10n	No	D37	DQ102	DQ51	DQ25	DQ12
3B	56	VREFB3BNO	IO				DIFF_TX_3B10p	No	B37	DQ102	DQ51	DQ25	DQ12
3B	55	VREFB3BNO	IO				DIFF_RX_3B11n	No	C36	DQ5n102	DQ51	DQ25	DQ12
3B	54	VREFB3BNO	IO				DIFF_RX_3B11p	No	A36	DQ5102	DQ51	DQ25	DQ12
3B	53	VREFB3BNO	IO				DIFF_TX_3B11n	No	D35	DQ5n103	DQ5n51/CQn51	DQ5n25/CQn25	DQ12
3B	52	VREFB3BNO	IO				DIFF_TX_3B11p	No	B35	DQ5103	DQ51/CQ51	DQ525/CQ25	DQ12
3B	51	VREFB3BNO	IO	CDR			DIFF_RX_3B12n	Yes	C34	DQ103	DQ51	DQ25	DQ12
3B	50	VREFB3BNO	IO	CDR			DIFF_RX_3B12p	Yes	A34	DQ103	DQ51	DQ25	DQ12
3B	49	VREFB3BNO	IO				DIFF_TX_3B12n	No	D33	DQ103	DQ51	DQ25	DQ12
3B	48	VREFB3BNO	IO				DIFF_TX_3B12p	No	B33	DQ103	DQ51	DQ25	DQ12
3B	47	VREFB3BNO	IO	CDR			DIFF_RX_3B13n	Yes	V45	DQ104	DQ52	DQ26	DQ13
3B	46	VREFB3BNO	IO	CDR			DIFF_RX_3B13p	Yes	T45	DQ104	DQ52	DQ26	DQ13
3B	45	VREFB3BNO	IO				DIFF_TX_3B13n	No	W44	DQ104	DQ52	DQ26	DQ13
3B	44	VREFB3BNO	IO				DIFF_TX_3B13p	No	U44	DQ104	DQ52	DQ26	DQ13
3B	43	VREFB3BNO	IO				DIFF_RX_3B14n	No	V43	DQ5n104	DQ52	DQ26	DQ13
3B	42	VREFB3BNO	IO				DIFF_RX_3B14p	No	T43	DQ5104	DQ52	DQ26	DQ13
3B	41	VREFB3BNO	IO				DIFF_TX_3B14n	No	W42	DQ5n105	DQ5n52/CQn52	DQ26	DQ13
3B	40	VREFB3BNO	IO				DIFF_TX_3B14p	No	U42	DQ5105	DQ552/CQ52	DQ26	DQ13
3B	39	VREFB3BNO	IO	CDR			DIFF_RX_3B15n	Yes	V41	DQ105	DQ52	DQ26	DQ13
3B	38	VREFB3BNO	IO	CDR			DIFF_RX_3B15p	Yes	T41	DQ105	DQ52	DQ26	DQ13
3B	37	VREFB3BNO	IO				DIFF_TX_3B15n	No	W40	DQ105	DQ52	DQ26	DQ13
3B	36	VREFB3BNO	IO				DIFF_TX_3B15p	No	U40	DQ105	DQ52	DQ26	DQ13
3B	35	VREFB3BNO	IO	CDR			DIFF_RX_3B16n	Yes	P45	DQ106	DQ53	DQ26	DQ13
3B	34	VREFB3BNO	IO	CDR			DIFF_RX_3B16p	Yes	M45	DQ106	DQ53	DQ26	DQ13
3B	33	VREFB3BNO	IO				DIFF_TX_3B16n	No	N44	DQ106	DQ53	DQ26	DQ13
3B	32	VREFB3BNO	IO				DIFF_TX_3B16p	No	L44	DQ106	DQ53	DQ26	DQ13
3B	31	VREFB3BNO	IO				DIFF_RX_3B17n	No	P43	DQ5n106	DQ53	DQ26	DQ13
3B	30	VREFB3BNO	IO				DIFF_RX_3B17p	No	M43	DQ5106	DQ53	DQ26	DQ13
3B	29	VREFB3BNO	IO	PLL_3B_B_CLKOUT1n			DIFF_TX_3B17n	No	N42	DQ5n107	DQ5n53/CQn53	DQ5n26/CQn26	DQ13
3B	28	VREFB3BNO	IO	PLL_3B_B_CLKOUT1p,PLL_3B_B_CLKOUT1,PLL_3B_B_FB1			DIFF_TX_3B17p	No	L42	DQ5107	DQ553/CQ53	DQ526/CQ26	DQ13
3B	27	VREFB3BNO	IO	CDR			DIFF_RX_3B18n	Yes	P41	DQ107	DQ53	DQ26	DQ13
3B	26	VREFB3BNO	IO	RZQ_B_3B,CDR			DIFF_RX_3B18p	Yes	M41	DQ107	DQ53	DQ26	DQ13
3B	25	VREFB3BNO	IO	CLK_B_3B_1n			DIFF_TX_3B18n	No	N40	DQ107	DQ53	DQ26	DQ13
3B	24	VREFB3BNO	IO	CLK_B_3B_1p			DIFF_TX_3B18p	No	L40	DQ107	DQ53	DQ26	DQ13
3B	23	VREFB3BNO	IO	CLK_B_3B_0n,CDR			DIFF_RX_3B19n	Yes	W38	DQ108	DQ54	DQ27	DQ13
3B	22	VREFB3BNO	IO	CLK_B_3B_0p,CDR			DIFF_RX_3B19p	Yes	U38	DQ108	DQ54	DQ27	DQ13
3B	21	VREFB3BNO	IO				DIFF_TX_3B19n	No	V37	DQ108	DQ54	DQ27	DQ13
3B	20	VREFB3BNO	IO				DIFF_TX_3B19p	No	T37	DQ108	DQ54	DQ27	DQ13
3B	19	VREFB3BNO	IO	PLL_3B_B_CLKOUT0n			DIFF_RX_3B20n	No	W36	DQ5n108	DQ54	DQ27	DQ13
3B	18	VREFB3BNO	IO	PLL_3B_B_CLKOUT0p,PLL_3B_B_CLKOUT0,PLL_3B_B_FB0			DIFF_RX_3B20p	No	U36	DQ5108	DQ54	DQ27	DQ13
3B	17	VREFB3BNO	IO				DIFF_TX_3B20n	No	V35	DQ5n109	DQ5n54/CQn54	DQ27	DQ5n13/CQn13
3B	16	VREFB3BNO	IO				DIFF_TX_3B20p	No	T35	DQ5109	DQ554/CQ54	DQ27	DQ513/CQ13
3B	15	VREFB3BNO	IO	CDR			DIFF_RX_3B21n	Yes	W34	DQ109	DQ54	DQ27	DQ13
3B	14	VREFB3BNO	IO	CDR			DIFF_RX_3B21p	Yes	U34	DQ109	DQ54	DQ27	DQ13
3B	13	VREFB3BNO	IO				DIFF_TX_3B21n	No	V33	DQ109	DQ54	DQ27	DQ13
3B	12	VREFB3BNO	IO				DIFF_TX_3B21p	No	T33	DQ109	DQ54	DQ27	DQ13
3B	11	VREFB3BNO	IO	CDR			DIFF_RX_3B22n	Yes	N38	DQ110	DQ55	DQ27	DQ13
3B	10	VREFB3BNO	IO	CDR			DIFF_RX_3B22p	Yes	L38	DQ110	DQ55	DQ27	DQ13
3B	9	VREFB3BNO	IO				DIFF_TX_3B22n	No	P37	DQ110	DQ55	DQ27	DQ13
3B	8	VREFB3BNO	IO				DIFF_TX_3B22p	No	M37	DQ110	DQ55	DQ27	DQ13
3B	7	VREFB3BNO	IO				DIFF_RX_3B23n	No	N36	DQ5n110	DQ55	DQ27	DQ13
3B	6	VREFB3BNO	IO				DIFF_RX_3B23p	No	L36	DQ5110	DQ55	DQ27	DQ13
3B	5	VREFB3BNO	IO				DIFF_TX_3B23n	No	P35	DQ5n111	DQ5n55/CQn55	DQ5n27/CQn27	DQ13
3B	4	VREFB3BNO	IO				DIFF_TX_3B23p	No	M35	DQ5111	DQ555/CQ55	DQ527/CQ27	DQ13
3B	3	VREFB3BNO	IO	CDR			DIFF_RX_3B24n	Yes	N34	DQ111	DQ55	DQ27	DQ13
3B	2	VREFB3BNO	IO	CDR			DIFF_RX_3B24p	Yes	L34	DQ111	DQ55	DQ27	DQ13
3B	1	VREFB3BNO	IO				DIFF_TX_3B24n	No	P33	DQ111	DQ55	DQ27	DQ13
3B	0	VREFB3BNO	IO				DIFF_TX_3B24p	No	M33	DQ111	DQ55	DQ27	DQ13
3A	95	VREFB3AANO	IO				DIFF_RX_3A1n	No	H59	DQ112	DQ56	DQ28	DQ14
3A	94	VREFB3AANO	IO				DIFF_RX_3A1p	No	F59	DQ112	DQ56	DQ28	DQ14
3A	93	VREFB3AANO	IO				DIFF_TX_3A1n	No	J58	DQ112	DQ56	DQ28	DQ14
3A	92	VREFB3AANO	IO				DIFF_TX_3A1p	No	G58	DQ112	DQ56	DQ28	DQ14
3A	91	VREFB3AANO	IO				DIFF_RX_3A2n	No	H57	DQ5n112	DQ56	DQ28	DQ14
3A	90	VREFB3AANO	IO		AVST_READY		DIFF_RX_3A2p	No	F57	DQ5112	DQ56	DQ28	DQ14
3A	89	VREFB3AANO	IO		AVST_DATA31		DIFF_TX_3A2n	No	J56	DQ5n113	DQ5n56/CQn56	DQ28	DQ14
3A	88	VREFB3AANO	IO		AVST_DATA30		DIFF_TX_3A2p	No	G56	DQ5113	DQ556/CQ56	DQ28	DQ14
3A	87	VREFB3AANO	IO	CDR	AVST_DATA29		DIFF_RX_3A3n	Yes	H55	DQ113	DQ56	DQ28	DQ14
3A	86	VREFB3AANO	IO	CDR	AVST_DATA28		DIFF_RX_3A3p	Yes	F55	DQ113	DQ56	DQ28	DQ14
3A	85	VREFB3AANO	IO		AVST_DATA27		DIFF_TX_3A3n	No	J54	DQ113	DQ56	DQ28	DQ14
3A	84	VREFB3AANO	IO		AVST_DATA26		DIFF_TX_3A3p	No	G54	DQ113	DQ56	DQ28	DQ14
3A	83	VREFB3AANO	IO		AVST_DATA25		DIFF_RX_3A4n	No	H61	DQ114	DQ57	DQ28	DQ14
3A	82	VREFB3AANO	IO		AVST_DATA24		DIFF_RX_3A4p	No	F61	DQ114	DQ57	DQ28	DQ14
3A	81	VREFB3AANO	IO		AVST_DATA23		DIFF_TX_3A4n	No	D59	DQ114	DQ57	DQ28	DQ14
3A	80	VREFB3AANO	IO		AVST_DATA22		DIFF_TX_3A4p	No	C58	DQ114	DQ57	DQ28	DQ14
3A	79	VREFB3AANO	IO		AVST_DATA21		DIFF_RX_3A5n	No	D57	DQ5n114	DQ57	DQ28	DQ14
3A	78	VREFB3AANO	IO		AVST_DATA20		DIFF_RX_3A5p	No	B57	DQ5114	DQ57	DQ28	DQ14
3A	77	VREFB3AANO	IO	PLL_3A_T_CLKOUT1n		AVST_DATA19	DIFF_TX_3A5n	No	C56	DQ5n115	DQ5n57/CQn57	DQ5n28/CQn28	DQ14



Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
HPS			HPS_IOA_14	GPIO0_IO13,NAND_RB,USB1_STP,EMAC0_TX_CTL		HPS_IOA_14			V1				
HPS			HPS_IOA_15	GPIO0_IO14,NAND_CE_N,USB1_DIR,EMAC0_RX_CLK		HPS_IOA_15			AA4				
HPS			HPS_IOA_16	GPIO0_IO15,USB1_DATA0,EMAC0_RX_CTL		HPS_IOA_16			T1				
HPS			HPS_IOA_17	GPIO0_IO16,NAND_A0Q8,USB1_DATA1,EMAC0_TXD0		HPS_IOA_17			AD5				
HPS			HPS_IOA_18	GPIO0_IO17,NAND_A0Q9,USB1_NXT,EMAC0_TXD1		HPS_IOA_18			P1				
HPS			HPS_IOA_19	GPIO0_IO18,NAND_A0Q10,USB1_DATA2,EMAC0_RXD0		HPS_IOA_19			AF7				
HPS			HPS_IOA_20	GPIO0_IO19,SPIM1_SS1_N,NAND_A0Q11,USB1_DATA3,EMAC0_RXD1		HPS_IOA_20			M1				
HPS			HPS_IOA_21	GPIO0_IO20,SPIM1_CLK,SPIS0_CLK,UART0_CTS_N,I2C1_SDA,NAND_A0Q12,USB1_DATA4,EMAC0_TXD2		HPS_IOA_21			AF9				
HPS			HPS_IOA_22	GPIO0_IO21,SPIM1_MOSI,SPIS0_MOSI,UART0_RTS_N,I2C1_SCL,NAND_A0Q13,USB1_DATA5,EMAC0_TXD3		HPS_IOA_22			W2				
HPS			HPS_IOA_23	GPIO0_IO22,SPIM1_MISO,SPIS0_SSD_N,UART0_TX,I2C0_SDA,NAND_A0Q14,USB1_DATA6,EMAC0_RXD2		HPS_IOA_23			AB5				
HPS			HPS_IOA_24	GPIO0_IO23,SPIM1_SSD_N,SPIS0_MISO,UART0_RX,I2C0_SCL,NAND_A0Q15,USB1_DATA7,EMAC0_RXD3		HPS_IOA_24			U2				
HPS			HPS_IOB_1	GPIO1_I00,SPIM1_CLK,UART0_CTS_N,NAND_A0Q0,EMAC1_TX_CLK		HPS_IOB_1			AC6				
HPS			HPS_IOB_2	GPIO1_I01,SPIM1_MOSI,UART0_RTS_N,NAND_A0Q1,EMAC1_TX_CTL		HPS_IOB_2			H1				
HPS			HPS_IOB_3	GPIO1_I02,SPIM1_MISO,UART0_TX,I2C0_SDA,NAND_WE_N,EMAC1_RX_CLK		HPS_IOB_3			AA6				
HPS			HPS_IOB_4	GPIO1_I03,SPIM1_SSD_N,UART0_RX,I2C0_SCL,NAND_RE_N,EMAC1_RX_CTL		HPS_IOB_4			F1				
HPS			HPS_IOB_5	GPIO1_I04,SPIM1_SS1_N,SPIS1_CLK,UART1_CTS_N,NAND_WP_N,EMAC1_TXD0		HPS_IOB_5			AD7				
HPS			HPS_IOB_6	GPIO1_I05,SPIS1_MOSI,UART1_RTS_N,NAND_A0Q2,EMAC1_TXD1		HPS_IOB_6			N2				
HPS			HPS_IOB_7	GPIO1_I06,SPIS1_SSD_N,UART1_TX,I2C1_SDA,NAND_A0Q3,EMAC1_RXD0		HPS_IOB_7			AB7				
HPS			HPS_IOB_8	GPIO1_I07,SPIS1_MISO,UART1_RX,I2C1_SCL,NAND_CLE,EMAC1_RXD1		HPS_IOB_8			L2				
HPS			HPS_IOB_9	GPIO1_I08,ITAG_TCK,SPIS0_CLK,MADIO2_MADIO2_C,EMAC2_SDA,NAND_A0Q4,EMAC1_TXD2		HPS_IOB_9			AC8				
HPS			HPS_IOB_10	GPIO1_I09,ITAG_TMS,SPIS0_MOSI,MADIO2_MADIO2_C,EMAC2_SCL,NAND_A0Q5,EMAC1_TXD3		HPS_IOB_10			J2				
HPS			HPS_IOB_11	GPIO1_I010,ITAG_TDO,SPIS0_SSD_N,MADIO0_MADIO2_C,EMAC2_SDA,NAND_A0Q6,EMAC1_RXD2		HPS_IOB_11			AA8				
HPS			HPS_IOB_12	GPIO1_I011,ITAG_TDI,SPIS0_MISO,MADIO0_MADIO2_C,EMAC2_SCL,NAND_A0Q7,EMAC1_RXD3		HPS_IOB_12			G2				
HPS			HPS_IOB_13	GPIO1_I012,I2C1_SDA,NAND_ALE,SDMMC_DATA0,EMAC2_TX_CLK		HPS_IOB_13			AD9				
HPS			HPS_IOB_14	GPIO1_I013,I2C1_SCL,NAND_RB,SDMMC_CMD,EMAC2_TX_CTL		HPS_IOB_14			V3				
HPS			HPS_IOB_15	GPIO1_I014,UART1_TX,NAND_CE_N,SDMMC_CCLK,EMAC2_RX_CLK		HPS_IOB_15			AB9				
HPS			HPS_IOB_16	GPIO1_I015,UART1_RX,SDMMC_DATA1,EMAC2_RX_CTL		HPS_IOB_16			T3				
HPS			HPS_IOB_17	GPIO1_I016,UART1_CTS_N,NAND_A0Q8,SDMMC_DATA2,EMAC2_TXD0		HPS_IOB_17			AC10				
HPS			HPS_IOB_18	GPIO1_I017,SPIM0_SS1_N,UART1_RTS_N,NAND_A0Q9,SDMMC_DATA3,EMAC2_TXD1		HPS_IOB_18			P3				
HPS			HPS_IOB_19	GPIO1_I018,SPIM0_MISO,MADIO1_MADIO2_C,EMAC1_SDA,NAND_A0Q10,SDMMC_DATA4,EMAC2_RXD0		HPS_IOB_19			AD11				
HPS			HPS_IOB_20	GPIO1_I019,SPIM0_SSD_N,MADIO1_MADIO2_C,EMAC1_SCL,NAND_A0Q11,SDMMC_DATA5,EMAC2_RXD1		HPS_IOB_20			M3				
HPS			HPS_IOB_21	GPIO1_I020,SPIM0_CLK,SPIS1_CLK,I2C_EMAC2_SDA,NAND_A0Q12,SDMMC_DATA6,EMAC2_TXD2		HPS_IOB_21			AC12				
HPS			HPS_IOB_22	GPIO1_I021,SPIM0_MOSI,SPIS1_MOSI,I2C_EMAC2_SCL,NAND_A0Q13,SDMMC_DATA7,EMAC2_TXD3		HPS_IOB_22			H3				
HPS			HPS_IOB_23	GPIO1_I022,SPIM0_MISO,SPIS1_SSD_N,MADIO0_MADIO2_C,EMAC0_SDA,NAND_A0Q14,EMAC2_RXD2		HPS_IOB_23			AD13				
HPS			HPS_IOB_24	GPIO1_I023,SPIM0_SSD_N,SPIS1_MISO,MADIO0_MADIO2_C,EMAC0_SCL,NAND_A0Q15,EMAC2_RXD3		HPS_IOB_24			F3				
9A			GXER9A_TX_CH0p						AK1				
9A			GXER9A_TX_CH1p						AL4				
9A			GXER9A_TX_CH2p						AP1				
9A			GXER9A_TX_CH3p						AB4				
9A			GXER9A_TX_CH8p						AV1				
9A			GXER9A_TX_CH9p						AW4				
9A			GXER9A_TX_CH10p						BB1				
9A			GXER9A_TX_CH11p						BC4				
9A			GXER9A_TX_CH12p						BF1				
9A			GXER9A_TX_CH13p						BG4				
9A			GXER9A_TX_CH14p						BK1				
9A			GXER9A_TX_CH15p						BL4				
9A			GXER9A_TX_CH20p						BP1				
9A			GXER9A_TX_CH21p						BN4				
9A			GXER9A_TX_CH22p						BV1				
9A			GXER9A_TX_CH23p						BW4				
9A			GXER9A_TX_CH0n						AJ2				
9A			GXER9A_TX_CH1n						AM5				
9A			GXER9A_TX_CH2n						AN2				
9A			GXER9A_TX_CH3n						AT5				
9A			GXER9A_TX_CH8n						AU2				
9A			GXER9A_TX_CH9n						AY5				
9A			GXER9A_TX_CH10n						BA2				
9A			GXER9A_TX_CH11n						BD5				
9A			GXER9A_TX_CH12n						BE2				
9A			GXER9A_TX_CH13n						BH5				
9A			GXER9A_TX_CH14n						BJ2				
9A			GXER9A_TX_CH15n						BM5				
9A			GXER9A_TX_CH20n						BN2				
9A			GXER9A_TX_CH21n						BT5				
9A			GXER9A_TX_CH22n						BU2				
9A			GXER9A_TX_CH23n						BV5				
9A			GXER9A_RX_CH0p						AK7				
9A			GXER9A_RX_CH1p						AL10				
9A			GXER9A_RX_CH2p						AP7				
9A			GXER9A_RX_CH3p						AR10				
9A			GXER9A_RX_CH8p						AV7				
9A			GXER9A_RX_CH9p						AW10				
9A			GXER9A_RX_CH10p						BB7				
9A			GXER9A_RX_CH11p						BC10				
9A			GXER9A_RX_CH12p						BF7				
9A			GXER9A_RX_CH13p						BG10				
9A			GXER9A_RX_CH14p						BK7				
9A			GXER9A_RX_CH15p						BL10				
9A			GXER9A_RX_CH20p						BP7				
9A			GXER9A_RX_CH21p						BR10				
9A			GXER9A_RX_CH22p						BV7				
9A			GXER9A_RX_CH23p						BW10				
9A			GXER9A_RX_CH0n						AJ8				
9A			GXER9A_RX_CH1n						AM11				
9A			GXER9A_RX_CH2n						AN8				
9A			GXER9A_RX_CH3n						AT11				
9A			GXER9A_RX_CH8n						AU8				
9A			GXER9A_RX_CH9n						AY11				
9A			GXER9A_RX_CH10n						BA8				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
9A			GXER9A_RX_CH11n						BD11				
9A			GXER9A_RX_CH12n						BE8				
9A			GXER9A_RX_CH13n						BH11				
9A			GXER9A_RX_CH14n						BJ8				
9A			GXER9A_RX_CH15n						BM11				
9A			GXER9A_RX_CH20n						BN8				
9A			GXER9A_RX_CH21n						BT11				
9A			GXER9A_RX_CH22n						BU8				
9A			GXER9A_RX_CH23n						BY11				
9A			REFCLK_GXER9A_CH0p						AT13				
9A			REFCLK_GXER9A_CH0n						AP13				
9A			REFCLK_GXER9A_CH1p						AR14				
9A			REFCLK_GXER9A_CH1n						AN14				
9A			REFCLK_GXER9A_CH2p						AJ12				
9A			REFCLK_GXER9A_CH2n						AH11				
9A			REFCLK_GXER9A_CH3p						AK13				
9A			REFCLK_GXER9A_CH3n						AH13				
9A			REFCLK_GXER9A_CH4p						AJ14				
9A			REFCLK_GXER9A_CH4n						AL14				
9A			REFCLK_GXER9A_CH5p						AR16				
9A			REFCLK_GXER9A_CH5n						AN16				
9A			REFCLK_GXER9A_CH6p						AJ16				
9A			REFCLK_GXER9A_CH6n						AL16				
9A			REFCLK_GXER9A_CH7p						AH15				
9A			REFCLK_GXER9A_CH7n						AK15				
9A			REFCLK_GXER9A_CH8p						AH17				
9A			REFCLK_GXER9A_CH8n						AK17				
10A			GXPL10A_TX_CH0p						BP55				
10A			GXPL10A_TX_CH1p						BN52				
10A			GXPL10A_TX_CH2p						BK55				
10A			GXPL10A_TX_CH3p						BJ52				
10A			GXPL10A_TX_CH4p						BF55				
10A			GXPL10A_TX_CH5p						BE52				
10A			GXPL10A_TX_CH6p						BB55				
10A			GXPL10A_TX_CH7p						BA52				
10A			GXPL10A_TX_CH8p						AV55				
10A			GXPL10A_TX_CH9p						AU52				
10A			GXPL10A_TX_CH10p						AP55				
10A			GXPL10A_TX_CH11p						AN52				
10A			GXPL10A_TX_CH12p						AK55				
10A			GXPL10A_TX_CH13p						AJ52				
10A			GXPL10A_TX_CH14p						AF55				
10A			GXPL10A_TX_CH15p						AE52				
10A			GXPL10A_TX_CH0n						BR56				
10A			GXPL10A_TX_CH1n						BM53				
10A			GXPL10A_TX_CH2n						BL56				
10A			GXPL10A_TX_CH3n						BH53				
10A			GXPL10A_TX_CH4n						BG56				
10A			GXPL10A_TX_CH5n						BD53				
10A			GXPL10A_TX_CH6n						BC56				
10A			GXPL10A_TX_CH7n						AY53				
10A			GXPL10A_TX_CH8n						AW56				
10A			GXPL10A_TX_CH9n						AT53				
10A			GXPL10A_TX_CH10n						AR56				
10A			GXPL10A_TX_CH11n						AM53				
10A			GXPL10A_TX_CH12n						AL56				
10A			GXPL10A_TX_CH13n						AH53				
10A			GXPL10A_TX_CH14n						AG56				
10A			GXPL10A_TX_CH15n						AD53				
10A			GXPL10A_RX_CH0p						BP61				
10A			GXPL10A_RX_CH1p						BN58				
10A			GXPL10A_RX_CH2p						BK61				
10A			GXPL10A_RX_CH3p						BJ58				
10A			GXPL10A_RX_CH4p						BF61				
10A			GXPL10A_RX_CH5p						BE58				
10A			GXPL10A_RX_CH6p						BB61				
10A			GXPL10A_RX_CH7p						BA58				
10A			GXPL10A_RX_CH8p						AV61				
10A			GXPL10A_RX_CH9p						AU58				
10A			GXPL10A_RX_CH10p						AP61				
10A			GXPL10A_RX_CH11p						AN58				
10A			GXPL10A_RX_CH12p						AK61				
10A			GXPL10A_RX_CH13p						AJ58				
10A			GXPL10A_RX_CH14p						AF61				
10A			GXPL10A_RX_CH15p						AE58				
10A			GXPL10A_RX_CH0n						BR62				
10A			GXPL10A_RX_CH1n						BM59				
10A			GXPL10A_RX_CH2n						BL62				
10A			GXPL10A_RX_CH3n						BH59				
10A			GXPL10A_RX_CH4n						BG62				
10A			GXPL10A_RX_CH5n						BD59				
10A			GXPL10A_RX_CH6n						BC62				
10A			GXPL10A_RX_CH7n						AY59				
10A			GXPL10A_RX_CH8n						AW62				
10A			GXPL10A_RX_CH9n						AT59				
10A			GXPL10A_RX_CH10n						AR62				
10A			GXPL10A_RX_CH11n						AM59				
10A			GXPL10A_RX_CH12n						AL62				
10A			GXPL10A_RX_CH13n						AH59				
10A			GXPL10A_RX_CH14n						AG62				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
10A			GXP110A_RX_CH15n						AD59				
U10_WHR			J_PIN_PERST_N_U10_P						BU58				
10A			REFCLK_GXP110A_CH0n						AH49				
10A			REFCLK_GXP110A_CH0p						AJ48				
10A			REFCLK_GXP110A_CH2n						AD49				
10A			REFCLK_GXP110A_CH2p						AE48				
			GND						BW54				
			GND						BY59				
			GND						BN48				
			GND						BV57				
			GND						BU54				
			GND						Y9				
			GND						Y7				
			GND						Y61				
			GND						Y59				
			GND						Y57				
			GND						Y55				
			GND						Y53				
			GND						Y51				
			GND						Y5				
			GND						Y49				
			GND						Y47				
			GND						Y45				
			GND						Y43				
			GND						Y41				
			GND						Y39				
			GND						Y37				
			GND						Y35				
			GND						Y33				
			GND						Y31				
			GND						Y3				
			GND						Y29				
			GND						Y27				
			GND						Y25				
			GND						Y23				
			GND						Y21				
			GND						Y19				
			GND						Y17				
			GND						Y15				
			GND						Y13				
			GND						Y11				
			GND						Y1				
			GND						W60				
			GND						W46				
			GND						W4				
			GND						W32				
			GND						W18				
			GND						V53				
			GND						V39				
			GND						V25				
			GND						V11				
			GND						U60				
			GND						U46				
			GND						U4				
			GND						U32				
			GND						U18				
			GND						T53				
			GND						T39				
			GND						T25				
			GND						T11				
			GND						R8				
			GND						R62				
			GND						R60				
			GND						R6				
			GND						R58				
			GND						R56				
			GND						R54				
			GND						R52				
			GND						R50				
			GND						R48				
			GND						R46				
			GND						R44				
			GND						R42				
			GND						R40				
			GND						R4				
			GND						R38				
			GND						R36				
			GND						R34				
			GND						R32				
			GND						R30				
			GND						R28				
			GND						R26				
			GND						R24				
			GND						R22				
			GND						R20				
			GND						R2				
			GND						R18				
			GND						R16				
			GND						R14				
			GND						R12				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						R10				
			GND						P53				
			GND						P39				
			GND						P25				
			GND						P11				
			GND						N60				
			GND						N46				
			GND						N4				
			GND						N32				
			GND						N18				
			GND						M53				
			GND						M39				
			GND						M25				
			GND						M11				
			GND						L60				
			GND						L46				
			GND						L4				
			GND						L32				
			GND						L18				
			GND						K9				
			GND						K7				
			GND						K61				
			GND						K59				
			GND						K57				
			GND						K55				
			GND						K53				
			GND						K51				
			GND						K5				
			GND						K49				
			GND						K47				
			GND						K45				
			GND						K43				
			GND						K41				
			GND						K39				
			GND						K37				
			GND						K35				
			GND						K33				
			GND						K31				
			GND						K3				
			GND						K29				
			GND						K27				
			GND						K25				
			GND						K23				
			GND						K21				
			GND						K19				
			GND						K17				
			GND						K15				
			GND						K13				
			GND						K11				
			GND						K1				
			GND						J60				
			GND						J46				
			GND						J4				
			GND						J32				
			GND						J18				
			GND						H53				
			GND						H39				
			GND						H25				
			GND						H11				
			GND						G60				
			GND						G46				
			GND						G4				
			GND						G32				
			GND						G18				
			GND						F53				
			GND						F39				
			GND						F25				
			GND						F11				
			GND						E8				
			GND						E62				
			GND						E60				
			GND						E6				
			GND						E58				
			GND						E56				
			GND						E54				
			GND						E52				
			GND						E50				
			GND						E48				
			GND						E46				
			GND						E44				
			GND						E42				
			GND						E40				
			GND						E4				
			GND						E38				
			GND						E36				
			GND						E34				
			GND						E32				
			GND						E30				
			GND						E28				
			GND						E26				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						E24				
			GND						E22				
			GND						E20				
			GND						E2				
			GND						E18				
			GND						E16				
			GND						E14				
			GND						E12				
			GND						E10				
			GND						DC60				
			GND						DC58				
			GND						DC44				
			GND						DC4				
			GND						DC30				
			GND						DC2				
			GND						DC16				
			GND						DB9				
			GND						DB61				
			GND						DB59				
			GND						DB51				
			GND						DB37				
			GND						DB3				
			GND						DB23				
			GND						DB1				
			GND						DA62				
			GND						DA60				
			GND						DA58				
			GND						DA44				
			GND						DA30				
			GND						DA2				
			GND						DA16				
			GND						D61				
			GND						D53				
			GND						D39				
			GND						D3				
			GND						D25				
			GND						D11				
			GND						D1				
			GND						CY9				
			GND						CY61				
			GND						CY51				
			GND						CY37				
			GND						CY23				
			GND						CY1				
			GND						CW8				
			GND						CW62				
			GND						CW60				
			GND						CW6				
			GND						CW58				
			GND						CW56				
			GND						CW54				
			GND						CW52				
			GND						CW50				
			GND						CW48				
			GND						CW46				
			GND						CW44				
			GND						CW42				
			GND						CW40				
			GND						CW4				
			GND						CW38				
			GND						CW36				
			GND						CW34				
			GND						CW32				
			GND						CW30				
			GND						CW28				
			GND						CW26				
			GND						CW24				
			GND						CW22				
			GND						CW20				
			GND						CW2				
			GND						CW18				
			GND						CW16				
			GND						CW14				
			GND						CW12				
			GND						CW10				
			GND						CV9				
			GND						CV51				
			GND						CV37				
			GND						CV23				
			GND						CU58				
			GND						CU44				
			GND						CU30				
			GND						CU2				
			GND						CU16				
			GND						CT9				
			GND						CTS1				
			GND						CT37				
			GND						CT23				
			GND						CR58				
			GND						CR44				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						CR30				
			GND						CR2				
			GND						CR16				
			GND						CP9				
			GND						CP7				
			GND						CP61				
			GND						CP59				
			GND						CP57				
			GND						CP55				
			GND						CP53				
			GND						CP51				
			GND						CP5				
			GND						CP49				
			GND						CP47				
			GND						CP45				
			GND						CP43				
			GND						CP41				
			GND						CP39				
			GND						CP37				
			GND						CP35				
			GND						CP33				
			GND						CP31				
			GND						CP3				
			GND						CP29				
			GND						CP27				
			GND						CP25				
			GND						CP23				
			GND						CP21				
			GND						CP19				
			GND						CP17				
			GND						CP15				
			GND						CP13				
			GND						CP11				
			GND						CP1				
			GND						CN58				
			GND						CN44				
			GND						CN30				
			GND						CN2				
			GND						CN16				
			GND						CN9				
			GND						CN51				
			GND						CN37				
			GND						CN23				
			GND						CL58				
			GND						CL44				
			GND						CL30				
			GND						CL2				
			GND						CL16				
			GND						CK9				
			GND						CK51				
			GND						CK37				
			GND						CK23				
			GND						CJ8				
			GND						CJ62				
			GND						CJ60				
			GND						CJ5				
			GND						CJ58				
			GND						CJ56				
			GND						CJ54				
			GND						CJ52				
			GND						CJ50				
			GND						CJ48				
			GND						CJ46				
			GND						CJ44				
			GND						CJ42				
			GND						CJ40				
			GND						CJ4				
			GND						CJ38				
			GND						CJ36				
			GND						CJ34				
			GND						CJ32				
			GND						CJ30				
			GND						CJ28				
			GND						CJ26				
			GND						CJ24				
			GND						CJ22				
			GND						CJ20				
			GND						CJ2				
			GND						CJ18				
			GND						CJ16				
			GND						CJ14				
			GND						CJ12				
			GND						CJ10				
			GND						CH9				
			GND						CH51				
			GND						CH37				
			GND						CH23				
			GND						CG58				
			GND						CG44				
			GND						CG30				



Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						CG2				
			GND						CG16				
			GND						CF9				
			GND						CF51				
			GND						CF37				
			GND						CF23				
			GND						CE58				
			GND						CE44				
			GND						CE30				
			GND						CE2				
			GND						CE16				
			GND						CD9				
			GND						CD7				
			GND						CD61				
			GND						CD59				
			GND						CD57				
			GND						CD55				
			GND						CD53				
			GND						CD51				
			GND						CD5				
			GND						CD49				
			GND						CD47				
			GND						CD45				
			GND						CD43				
			GND						CD41				
			GND						CD39				
			GND						CD37				
			GND						CD35				
			GND						CD33				
			GND						CD31				
			GND						CD3				
			GND						CD29				
			GND						CD27				
			GND						CD25				
			GND						CD23				
			GND						CD21				
			GND						CD19				
			GND						CD17				
			GND						CD15				
			GND						CD13				
			GND						CD11				
			GND						CD1				
			GND						CC6				
			GND						CC54				
			GND						CC46				
			GND						CC44				
			GND						CC42				
			GND						CC40				
			GND						CC4				
			GND						CC12				
			GND						CC10				
			GND						CB9				
			GND						CB55				
			GND						CB5				
			GND						CB3				
			GND						CB23				
			GND						CB11				
			GND						CA8				
			GND						CA6				
			GND						CA46				
			GND						CA44				
			GND						CA42				
			GND						CA40				
			GND						CA4				
			GND						CA38				
			GND						CA36				
			GND						CA34				
			GND						CA32				
			GND						CA30				
			GND						CA28				
			GND						CA26				
			GND						CA24				
			GND						CA22				
			GND						CA20				
			GND						CA2				
			GND						CA12				
			GND						CA10				
			GND						CA2				
			GND						CA6				
			GND						CA				
			GND						C32				
			GND						C2				
			GND						C18				
			GND						BY9				
			GND						BY7				
			GND						BY51				
			GND						BY3				
			GND						BY23				
			GND						BY13				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BY1				
			GND						BW8				
			GND						BW62				
			GND						BW60				
			GND						BW6				
			GND						BW58				
			GND						BW56				
			GND						BW52				
			GND						BW50				
			GND						BW48				
			GND						BW46				
			GND						BW44				
			GND						BW42				
			GND						BW40				
			GND						BW38				
			GND						BW36				
			GND						BW34				
			GND						BW32				
			GND						BW30				
			GND						BW28				
			GND						BW26				
			GND						BW24				
			GND						BW22				
			GND						BW20				
			GND						BW2				
			GND						BW16				
			GND						BW14				
			GND						BW12				
			GND						BV9				
			GND						BV61				
			GND						BV59				
			GND						BV5				
			GND						BV3				
			GND						BV25				
			GND						BV11				
			GND						BU62				
			GND						BU6				
			GND						BU56				
			GND						BU42				
			GND						BU40				
			GND						BU4				
			GND						BU38				
			GND						BU36				
			GND						BU34				
			GND						BU32				
			GND						BU30				
			GND						BU28				
			GND						BU26				
			GND						BU24				
			GND						BU22				
			GND						BU20				
			GND						BU18				
			GND						BU12				
			GND						BU10				
			GND						BT9				
			GND						BT7				
			GND						BT61				
			GND						BT57				
			GND						BT55				
			GND						BT3				
			GND						BT25				
			GND						BT17				
			GND						BT15				
			GND						BT13				
			GND						BT1				
			GND						BR8				
			GND						BR60				
			GND						BR6				
			GND						BR58				
			GND						BR54				
			GND						BR52				
			GND						BR48				
			GND						BR46				
			GND						BR42				
			GND						BR40				
			GND						BR38				
			GND						BR22				
			GND						BR2				
			GND						BR18				
			GND						BR16				
			GND						BR14				
			GND						BR12				
			GND						BP9				
			GND						BP59				
			GND						BP57				
			GND						BP53				
			GND						BP51				
			GND						BP5				
			GND						BP49				
			GND						BP45				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BP43				
			GND						BP3				
			GND						BP13				
			GND						BP11				
			GND						BN62				
			GND						BN60				
			GND						BN6				
			GND						BN56				
			GND						BN54				
			GND						BN42				
			GND						BN40				
			GND						BN4				
			GND						BN38				
			GND						BN36				
			GND						BN34				
			GND						BN22				
			GND						BN30				
			GND						BN28				
			GND						BN26				
			GND						BN24				
			GND						BN22				
			GND						BN20				
			GND						BN18				
			GND						BN16				
			GND						BN12				
			GND						BN10				
			GND						BM9				
			GND						BM7				
			GND						BM61				
			GND						BM57				
			GND						BM55				
			GND						BM51				
			GND						BM3				
			GND						BM19				
			GND						BM13				
			GND						BM1				
			GND						BL8				
			GND						BL60				
			GND						BL6				
			GND						BL58				
			GND						BL54				
			GND						BL52				
			GND						BL42				
			GND						BL40				
			GND						BL38				
			GND						BL36				
			GND						BL34				
			GND						BL32				
			GND						BL30				
			GND						BL28				
			GND						BL26				
			GND						BL24				
			GND						BL22				
			GND						BL2				
			GND						BL18				
			GND						BL16				
			GND						BL12				
			GND						BK9				
			GND						BK59				
			GND						BK57				
			GND						BK53				
			GND						BK51				
			GND						BK5				
			GND						BK47				
			GND						BK45				
			GND						BK43				
			GND						BK3				
			GND						BK13				
			GND						BK11				
			GND						BJ62				
			GND						BJ60				
			GND						BJ6				
			GND						BJ56				
			GND						BJ54				
			GND						BJ50				
			GND						BJ48				
			GND						BJ46				
			GND						BJ44				
			GND						BJ42				
			GND						BJ40				
			GND						BJ4				
			GND						BJ38				
			GND						BJ26				
			GND						BJ24				
			GND						BJ22				
			GND						BJ18				
			GND						BJ16				
			GND						BJ12				
			GND						BJ10				
			GND						BH9				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BH7				
			GND						BH61				
			GND						BH57				
			GND						BH55				
			GND						BH51				
			GND						BH3				
			GND						BH17				
			GND						BH15				
			GND						BH13				
			GND						BH1				
			GND						BG8				
			GND						BG60				
			GND						BG6				
			GND						BG58				
			GND						BG54				
			GND						BG52				
			GND						BG50				
			GND						BG48				
			GND						BG46				
			GND						BG44				
			GND						BG42				
			GND						BG40				
			GND						BG38				
			GND						BG36				
			GND						BG24				
			GND						BG22				
			GND						BG20				
			GND						BG2				
			GND						BG18				
			GND						BG12				
			GND						BF9				
			GND						BF59				
			GND						BF57				
			GND						BF53				
			GND						BF51				
			GND						BF5				
			GND						BF49				
			GND						BF47				
			GND						BF45				
			GND						BF3				
			GND						BF19				
			GND						BF17				
			GND						BF15				
			GND						BF13				
			GND						BF11				
			GND						BE62				
			GND						BE60				
			GND						BE6				
			GND						BE56				
			GND						BE54				
			GND						BE50				
			GND						BE48				
			GND						BE42				
			GND						BE40				
			GND						BE4				
			GND						BE38				
			GND						BE36				
			GND						BE24				
			GND						BE22				
			GND						BE18				
			GND						BE12				
			GND						BE10				
			GND						BD9				
			GND						BD7				
			GND						BD61				
			GND						BD57				
			GND						BD55				
			GND						BD51				
			GND						BD47				
			GND						BD45				
			GND						BD3				
			GND						BD17				
			GND						BD15				
			GND						BD13				
			GND						BD1				
			GND						BC8				
			GND						BC60				
			GND						BC6				
			GND						BC58				
			GND						BC54				
			GND						BC52				
			GND						BC50				
			GND						BC42				
			GND						BC40				
			GND						BC38				
			GND						BC26				
			GND						BC24				
			GND						BC22				
			GND						BC2				
			GND						BC18				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BC16				
			GND						BC12				
			GND						BB9				
			GND						BB59				
			GND						BB57				
			GND						BB53				
			GND						BB51				
			GND						BB5				
			GND						BB47				
			GND						BB45				
			GND						BB43				
			GND						BB3				
			GND						BB13				
			GND						BB11				
			GND						BA62				
			GND						BA60				
			GND						BA6				
			GND						BA56				
			GND						BA54				
			GND						BA50				
			GND						BA48				
			GND						BA46				
			GND						BA42				
			GND						BA40				
			GND						BA4				
			GND						BA38				
			GND						BA24				
			GND						BA22				
			GND						BA20				
			GND						BA18				
			GND						BA16				
			GND						BA12				
			GND						BA10				
			GND						BA1				
			GND						B59				
			GND						B53				
			GND						B39				
			GND						B3				
			GND						B25				
			GND						B11				
			GND						B1				
			GND						AY9				
			GND						AY7				
			GND						AY61				
			GND						AY57				
			GND						AY55				
			GND						AY51				
			GND						AY45				
			GND						AY3				
			GND						AY13				
			GND						AY1				
			GND						AW8				
			GND						AW60				
			GND						AW6				
			GND						AW58				
			GND						AW54				
			GND						AW52				
			GND						AW50				
			GND						AW48				
			GND						AW42				
			GND						AW40				
			GND						AW38				
			GND						AW26				
			GND						AW24				
			GND						AW22				
			GND						AW2				
			GND						AW18				
			GND						AW16				
			GND						AW12				
			GND						AV9				
			GND						AV59				
			GND						AV57				
			GND						AV53				
			GND						AV51				
			GND						AV5				
			GND						AV45				
			GND						AV3				
			GND						AV19				
			GND						AV17				
			GND						AV13				
			GND						AV11				
			GND						AU62				
			GND						AU60				
			GND						AU6				
			GND						AU56				
			GND						AU54				
			GND						AU50				
			GND						AU48				
			GND						AU42				
			GND						AU40				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AU4				
			GND						AU38				
			GND						AU26				
			GND						AU24				
			GND						AU22				
			GND						AU18				
			GND						AU16				
			GND						AU14				
			GND						AU12				
			GND						AU10				
			GND						AT9				
			GND						AT7				
			GND						AT61				
			GND						AT57				
			GND						AT55				
			GND						AT51				
			GND						AT47				
			GND						AT45				
			GND						AT43				
			GND						AT3				
			GND						AT15				
			GND						AT1				
			GND						AR8				
			GND						AR60				
			GND						AR6				
			GND						AR58				
			GND						AR54				
			GND						AR52				
			GND						AR50				
			GND						AR46				
			GND						AR42				
			GND						AR40				
			GND						AR38				
			GND						AR26				
			GND						AR24				
			GND						AR22				
			GND						AR2				
			GND						AR12				
			GND						AP9				
			GND						AP89				
			GND						AP57				
			GND						AP53				
			GND						AP51				
			GND						AP5				
			GND						AP47				
			GND						AP45				
			GND						AP43				
			GND						AP3				
			GND						AP19				
			GND						AP15				
			GND						AP11				
			GND						AN62				
			GND						AN60				
			GND						AN6				
			GND						AN56				
			GND						AN54				
			GND						AN50				
			GND						AN44				
			GND						AN42				
			GND						AN40				
			GND						AN4				
			GND						AN38				
			GND						AN36				
			GND						AN34				
			GND						AN32				
			GND						AN30				
			GND						AN28				
			GND						AN26				
			GND						AN24				
			GND						AN22				
			GND						AN12				
			GND						AN10				
			GND						AM9				
			GND						AM7				
			GND						AM61				
			GND						AM57				
			GND						AM55				
			GND						AM51				
			GND						AM47				
			GND						AM45				
			GND						AM43				
			GND						AM3				
			GND						AM17				
			GND						AM15				
			GND						AM13				
			GND						AM1				
			GND						AL8				
			GND						AL60				
			GND						AL6				
			GND						AL58				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AL54				
			GND						AL52				
			GND						AL50				
			GND						AL48				
			GND						AL46				
			GND						AL44				
			GND						AL42				
			GND						AL40				
			GND						AL38				
			GND						AL36				
			GND						AL34				
			GND						AL32				
			GND						AL30				
			GND						AL28				
			GND						AL26				
			GND						AL24				
			GND						AL22				
			GND						AL20				
			GND						AL2				
			GND						AL18				
			GND						AL12				
			GND						AK9				
			GND						AK59				
			GND						AK57				
			GND						AK53				
			GND						AK51				
			GND						AK5				
			GND						AK49				
			GND						AK47				
			GND						AK45				
			GND						AK43				
			GND						AK3				
			GND						AK19				
			GND						AK11				
			GND						AJ62				
			GND						AJ60				
			GND						AJ6				
			GND						AJ56				
			GND						AJ54				
			GND						AJ50				
			GND						AJ44				
			GND						AJ42				
			GND						AJ40				
			GND						AJ4				
			GND						AJ38				
			GND						AJ22				
			GND						AJ18				
			GND						AJ10				
			GND						AH9				
			GND						AH7				
			GND						AH61				
			GND						AH57				
			GND						AH55				
			GND						AH51				
			GND						AH47				
			GND						AH39				
			GND						AH3				
			GND						AH1				
			GND						AG8				
			GND						AG60				
			GND						AG58				
			GND						AG54				
			GND						AG52				
			GND						AG50				
			GND						AG48				
			GND						AG44				
			GND						AG42				
			GND						AG40				
			GND						AG38				
			GND						AG36				
			GND						AG34				
			GND						AG32				
			GND						AG30				
			GND						AG28				
			GND						AG26				
			GND						AG24				
			GND						AG22				
			GND						AG20				
			GND						AG2				
			GND						AG18				
			GND						AG16				
			GND						AG14				
			GND						AG12				
			GND						AG10				
			GND						AF59				
			GND						AF57				
			GND						AF53				
			GND						AF51				
			GND						AF49				
			GND						AF47				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AF45				
			GND						AF39				
			GND						AF17				
			GND						AF15				
			GND						AF13				
			GND						AF11				
			GND						AE8				
			GND						AE62				
			GND						AE60				
			GND						AE6				
			GND						AE56				
			GND						AE54				
			GND						AE50				
			GND						AE46				
			GND						AE42				
			GND						AE40				
			GND						AE4				
			GND						AE38				
			GND						AE36				
			GND						AE34				
			GND						AE32				
			GND						AE30				
			GND						AE28				
			GND						AE26				
			GND						AE24				
			GND						AE22				
			GND						AE20				
			GND						AE2				
			GND						AE18				
			GND						AE16				
			GND						AE14				
			GND						AE12				
			GND						AE10				
			GND						AD61				
			GND						AD57				
			GND						AD55				
			GND						AD51				
			GND						AD47				
			GND						AD41				
			GND						AC60				
			GND						AC58				
			GND						AC54				
			GND						AC52				
			GND						AC50				
			GND						AC48				
			GND						AC40				
			GND						AC38				
			GND						AC36				
			GND						AC34				
			GND						AC32				
			GND						AC30				
			GND						AC28				
			GND						AC26				
			GND						AC24				
			GND						AC20				
			GND						AB59				
			GND						AB57				
			GND						AB53				
			GND						AB51				
			GND						AB49				
			GND						AB41				
			GND						AB21				
			GND						AA60				
			GND						AA58				
			GND						AA54				
			GND						AA50				
			GND						AA40				
			GND						A60				
			GND						A58				
			GND						A46				
			GND						A4				
			GND						A32				
			GND						A2				
			GND						A18				
			GNDSENSE						BA26				
			VCC						BV23				
			VCC						BV21				
			VCC						BT23				
			VCC						BT21				
			VCC						BP41				
			VCC						BP39				
			VCC						BP37				
			VCC						BP35				
			VCC						BP33				
			VCC						BP31				
			VCC						BP29				
			VCC						BP27				
			VCC						BP25				
			VCC						BP23				
			VCC						BP21				



Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCC						BM41				
			VCC						BM39				
			VCC						BM37				
			VCC						BM35				
			VCC						BM33				
			VCC						BM31				
			VCC						BM29				
			VCC						BM27				
			VCC						BM25				
			VCC						BM23				
			VCC						BM21				
			VCC						BK41				
			VCC						BK39				
			VCC						BK37				
			VCC						BK25				
			VCC						BK23				
			VCC						BK21				
			VCC						BH41				
			VCC						BH39				
			VCC						BH37				
			VCC						BH25				
			VCC						BH23				
			VCC						BH21				
			VCC						BF41				
			VCC						BF39				
			VCC						BF37				
			VCC						BF25				
			VCC						BF23				
			VCC						BF21				
			VCC						BD41				
			VCC						BD39				
			VCC						BD37				
			VCC						BD25				
			VCC						BD23				
			VCC						BD21				
			VCC						BB41				
			VCC						BB39				
			VCC						BB37				
			VCC						BB23				
			VCC						BB21				
			VCC						BY41				
			VCC						BY39				
			VCC						BY37				
			VCC						AY25				
			VCC						AY23				
			VCC						AY21				
			VCC						AW41				
			VCC						AV39				
			VCC						AV37				
			VCC						AV25				
			VCC						AV23				
			VCC						AV21				
			VCC						AT41				
			VCC						AT39				
			VCC						AT37				
			VCC						AT25				
			VCC						AT23				
			VCC						AT21				
			VCC						AP41				
			VCC						AP39				
			VCC						AP37				
			VCC						AP25				
			VCC						AP23				
			VCC						AP21				
			VCC						AM41				
			VCC						AM39				
			VCC						AM37				
			VCC						AM35				
			VCC						AM33				
			VCC						AM31				
			VCC						AM29				
			VCC						AM27				
			VCC						AM25				
			VCC						AM23				
			VCC						AM21				
			VCC						AK41				
			VCC						AK39				
			VCC						AK37				
			VCC						AK35				
			VCC						AK33				
			VCC						AK31				
			VCC						AK29				
			VCC						AK27				
			VCC						AK25				
			VCC						AK21				
			VCC						AH41				
			VCC						AF41				
			VCCPT						BR36				
			VCCPT						BR34				
			VCCPT						BR32				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCPT						BR30				
			VCCPT						AJ36				
			VCCPT						AJ34				
			VCCPT						AJ32				
			VCCPT						AJ30				
			DNU						BY55				
			DNU						CF61				
			DNU						CH61				
			DNU						CB53				
			DNU						BY53				
			DNU						AH19				
			DNU						AJ20				
			DNU						CB61				
			DNU						AD45				
			DNU						BW18				
			DNU						AD19				
			DNU						BY61				
			DNU						AE44				
			DNU						BY19				
			DNU						AF19				
			DNU						AM19				
			DNU						AT19				
			DNU						AP17				
			DNU						AT17				
			DNU						AJ46				
			DNU						BN50				
			DNU						BK49				
			DNU						BM49				
			DNU						BL48				
			DNU						BL44				
			DNU						BU44				
			DNU						BT49				
			DNU						BV49				
			DNU						BV51				
			DNU						BU50				
			DNU						BTS1				
			DNU						BL46				
			DNU						BR50				
			DNU						BN46				
			DNU						BV53				
			DNU						BP47				
			DNU						BN44				
			DNU						BU52				
			DNU						BV55				
			DNU						BTS3				
			DNU						BM47				
			DNU						BV45				
			DNU						BM45				
			DNU						BT45				
			DNU						BL46				
			DNU						BL48				
			DNU						BV47				
			DNU						BT47				
			DNU						BL50				
			TEMPDIODE0An						CA52				
			TEMPDIODE0Ap						CC52				
			TEMPDIODE0Cn						AH43				
			TEMPDIODE0Cp						AF43				
			TEMPDIODE1n						AG46				
			TEMPDIODE1p						AH45				
			TEMPDIODE4n						AR20				
			TEMPDIODE4p						AN18				
			VCCBAT						CC48				
			VCCBAT						CA48				
			VCCA_PLL						CC34				
			VCCA_PLL						CC32				
			VCCA_PLL						CC30				
			VCCA_PLL						CC28				
			VCCA_PLL						AA34				
			VCCA_PLL						AA32				
			VCCA_PLL						AA30				
			VCCA_PLL						AA28				
			VCCIO_HPS						AH21				
			VCCIO_HPS						AF21				
			VCCIO_HPS						AD21				
			VCCIO_PIO_2A						CB39				
			VCCIO_PIO_2A						CB37				
			VCCIO_PIO_2A						BY39				
			VCCIO_PIO_2A						BY37				
			VCCIO_PIO_2B						CB35				
			VCCIO_PIO_2B						CB33				
			VCCIO_PIO_2B						BY35				
			VCCIO_PIO_2B						BY33				
			VCCIO_PIO_2C						CB31				
			VCCIO_PIO_2C						CB29				
			VCCIO_PIO_2C						BY31				
			VCCIO_PIO_2C						BY29				
			VCCIO_PIO_2D						CB27				
			VCCIO_PIO_2D						CB25				
			VCCIO_PIO_2D						BY27				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCIO_PIO_2D										
			VCCIO_PIO_3A						BY25				
			VCCIO_PIO_3A						AD39				
			VCCIO_PIO_3A						AD37				
			VCCIO_PIO_3A						AB39				
			VCCIO_PIO_3A						AB37				
			VCCIO_PIO_3B						AD35				
			VCCIO_PIO_3B						AD33				
			VCCIO_PIO_3B						AB35				
			VCCIO_PIO_3B						AB33				
			VCCIO_PIO_3C						AD31				
			VCCIO_PIO_3C						AD29				
			VCCIO_PIO_3C						AB31				
			VCCIO_PIO_3C						AB29				
			VCCIO_PIO_3D						AD27				
			VCCIO_PIO_3D						AD25				
			VCCIO_PIO_3D						AB27				
			VCCIO_PIO_3D						AB25				
			VCCIO_PIO_SDM						BV43				
			VCCIO_SDM						CB41				
			VCCIO_SDM						BY41				
2A		VREFB2AN0	VREFB2AN0						CC38				
2B		VREFB2BN0	VREFB2BN0						CC36				
2C		VREFB2CN0	VREFB2CN0						CC26				
2D		VREFB2DN0	VREFB2DN0						CC24				
3A		VREFB3AN0	VREFB3AN0						AA38				
3B		VREFB3BN0	VREFB3BN0						AA36				
3C		VREFB3CN0	VREFB3CN0						AA26				
3D		VREFB3DN0	VREFB3DN0						AA24				
			VREFN_ADC						CA62				
			VREFP_ADC						CC62				
			NC						W62				
			NC						V61				
			NC						U62				
			NC						T61				
			NC						P61				
			NC						N62				
			NC						M61				
			NC						L62				
			NC						J62				
			NC						G62				
			NC						CM61				
			NC						CM1				
			NC						CK61				
			NC						CK1				
			NC						CH1				
			NC						CF1				
			NC						CC8				
			NC						CC50				
			NC						CC22				
			NC						CC20				
			NC						CC2				
			NC						CC18				
			NC						CC16				
			NC						CC14				
			NC						CB7				
			NC						CB51				
			NC						CB19				
			NC						CB17				
			NC						CB15				
			NC						CB13				
			NC						CB1				
			NC						CA56				
			NC						CA50				
			NC						CA18				
			NC						CA16				
			NC						CA14				
			NC						BY57				
			NC						BY21				
			NC						BY17				
			NC						BY15				
			NC						BV19				
			NC						BV17				
			NC						BV15				
			NC						BV13				
			NC						BU60				
			NC						BU16				
			NC						BU14				
			NC						BT59				
			NC						AD43				
			NC						AD17				
			NC						AD15				
			NC						AC62				
			NC						AC56				
			NC						AC46				
			NC						AC44				
			NC						AC42				
			NC						AC18				
			NC						AC16				
			NC						AC14				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			NC						AB61				
			NC						AB55				
			NC						AB47				
			NC						AB45				
			NC						AB43				
			NC						AB19				
			NC						AB17				
			NC						AB15				
			NC						AB13				
			NC						AB11				
			NC						AA62				
			NC						AA56				
			NC						AA52				
			NC						AA48				
			NC						AA46				
			NC						AA44				
			NC						AA42				
			NC						AA20				
			NC						AA18				
			NC						AA16				
			NC						AA14				
			NC						AA12				
			NC						AA10				
			VCCRCORE						AV37				
			VCCRCORE						BT37				
			VCCRCORE						BR28				
			VCCRCORE						BR26				
			VCCRCORE						BR24				
			VCCRCORE						AJ28				
			VCCRCORE						AJ26				
			VCCRCORE						AJ24				
			VCCRCORE						AK37				
			VCCRCORE						AF37				
			VCCRTPLL_GXER1						BG16				
			VCCRTPLL_GXER1						BE16				
			VCCRT_GXER1						BP15				
			VCCRT_GXER1						BN14				
			VCCRT_GXER1						BM15				
			VCCRT_GXER1						BL14				
			VCCRT_GXER1						BK15				
			VCCRT_GXER1						BJ14				
			VCCRT_GXER1						BG14				
			VCCRT_GXER1						BE14				
			VCCRT_GXER1						BC14				
			VCCRT_GXER1						BB15				
			VCCRT_GXER1						BA14				
			VCCRT_GXER1						AY15				
			VCCRT_GXER1						AW14				
			VCCRT_GXER1						AV15				
			VCCRT_GXPL1						BD49				
			VCCRT_GXPL1						BC48				
			VCCRT_GXPL1						BB49				
			VCCRT_GXPL1						AY49				
			VCCRT_GXPL1						AV49				
			VCCRT_GXPL1						AT49				
			VCCRT_GXPL1						AK48				
			VCCRT_GXPL1						AK48				
			VCCRT_GXPL1						AM49				
			IO_AUX_BREF10_P						BM43				
			IO_AUX_BREF20						AN20				
			VCCADC						CB43				
			VCCADC						BY43				
			VCCCLK_GXER1						AR18				
			VCCCLK_GXPL1						BH47				
			VCCCLK_GXPL1						BH45				
			VCCFUSEWR_SDM						CB49				
			VCCFUSEWR_SDM						BY49				
			VCCFUSE_GXP						BR44				
			VCCCH						BF43				
			VCCCH						BD43				
			VCCCH						AY43				
			VCCCH						AV43				
			VCCCH						BL20				
			VCCCH						BJ20				
			VCCCH						BE20				
			VCCCH						BC20				
			VCCCH_GXER1						BP17				
			VCCCH_GXER1						BM17				
			VCCCH_GXER1						BK17				
			VCCCH_GXER1						BB17				
			VCCCH_GXER1						AV17				
			VCCCH_GXPL1						BE46				
			VCCCH_GXPL1						BC46				
			VCCCH_GXPL1						AY47				
			VCCCH_GXPL1						AW46				
			VCCCH_GXPL1						AV47				
			VCCCH_GXPL1						AJ46				
			VCCCH_GXPL1						AN46				
			VCCCH_SDM						BT43				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCLSENSE						B825				
			VCCL_HPS						AK23				
			VCCL_HPS						AH25				
			VCCL_HPS						AH23				
			VCCL_HPS						AF25				
			VCCL_HPS						AF23				
			VCCL_SDM						BV41				
			VCCL_SDM						BV39				
			VCCL_SDM						BT41				
			VCCL_SDM						BT39				
			VCCP						BV35				
			VCCP						BV33				
			VCCP						BV31				
			VCCP						BV29				
			VCCP						BV27				
			VCCP						BT35				
			VCCP						BT33				
			VCCP						BT31				
			VCCP						BT29				
			VCCP						BT27				
			VCCP						AH35				
			VCCP						AH33				
			VCCP						AH31				
			VCCP						AH29				
			VCCP						AH27				
			VCCP						AF35				
			VCCP						AF33				
			VCCP						AF31				
			VCCP						AF29				
			VCCP						AF27				
			VCCPLL_DG_HPS						AD23				
			VCCPLL_DG_HPS						AD23				
			VCCPLL_DG_SDM						CB45				
			VCCPLL_DG_SDM						BY45				
			VCCPLL_HPS						AC22				
			VCCPLL_HPS						AA22				
			VCCPLL_SDM						CB47				
			VCCPLL_SDM						BY47				
			VCC_HSSI_GXER1						BT19				
			VCC_HSSI_GXER1						BK20				
			VCC_HSSI_GXER1						BP19				
			VCC_HSSI_GXER1						BK19				
			VCC_HSSI_GXER1						BH19				
			VCC_HSSI_GXER1						BD19				
			VCC_HSSI_GXER1						BB19				
			VCC_HSSI_GXER1						AY19				
			VCC_HSSI_GXER1						AW20				
			VCC_HSSI_GXER1						AJ20				
			VCC_HSSI_GXPL1						BH43				
			VCC_HSSI_GXPL1						BE44				
			VCC_HSSI_GXPL1						BC44				
			VCC_HSSI_GXPL1						BA44				
			VCC_HSSI_GXPL1						AW44				
			VCC_HSSI_GXPL1						AU44				
			VCC_HSSI_GXPL1						AR44				
			U10_P_IO_RESREF_0						BH49				

Date	Version	Changes
April 2020	2020.04.27	Initial release.
April 2020	2020.04.30	Updated the I/O resource count tab.
July 2020	2020.07.22	Added Pin List R17A.
August 2020	2020.08.19	Changed pin BW54 from DNU to GND in Pin List R24A.
January 2021	2021.01.22	Removed support for Pin List R17A.

(1) For more information about pin definition and pin connection guidelines, refer to the [Intel® Agilex™ Device Family Pin Connection Guidelines](#)