

TYPE	BANK	R24A Package
Transceiver I/O	10A	68
LVDS I/O	2A	96
LVDS I/O	2B	96
LVDS I/O	2C	96
LVDS I/O	2D	96
LVDS I/O	3A	96
LVDS I/O	3B	96
HPS shared LVDS I/O	3C	96
HPS shared LVDS I/O	3D	96
Transceiver I/O	9A	82
HPS shared LVDS I/O	HPS	48
SDM shared LVDS I/O	SDM	29

- i. Total LVDS channels per bank supporting SERDES Non-DPA and DPA mode is equivalent to (LVDS I/O per bank)/2, inclusive of clock pair. Please refer to Dedicated Tx/Rx Channel column in the pin-out table for the channel availability.
- ii. Total LVDS channels supporting SERDES Soft-CDR mode is 12 pairs per bank. Please refer to Soft CDR column in the pin out table for the channel availability.

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
SDM			TDO							CR62				
SDM			TMS							CT61				
SDM			TCK							CU62				
SDM			TDI							CV61				
SDM			OSC_CLK_1							CC60				
SDM			SDM_ID0	PWRMGT_SCL						CF59				
SDM			SDM_ID1	AVSTx8_DATA2_AS_DATA1.SDMCMC_CFG_DATA1						CL60				
SDM			SDM_ID5	AS_nCS00.SDMCMC_CFG_CLK_MSEL0						CR60				
SDM			SDM_ID3	AVSTx8_DATA3_AS_DATA2.SDMCMC_CFG_DATA2						CK59				
SDM			nCONFIG							CB57				
SDM			SDM_ID4	AVSTx8_DATA1_AS_DATA0.SDMCMC_CFG_CMD						CM60				
SDM			SDM_ID2	AVSTx8_DATA0_AS_CLK.SDMCMC_CFG_DATA0						CT59				
SDM			SDM_ID7	AS_nCS02.MSEL1						CV59				
SDM			SDM_ID11	AVSTx8_VALID.PWRMGT_SDA						CE60				
SDM			nSTATUS							CF59				
SDM			SDM_ID16	PWRMGT_SDA						CA60				
SDM			SDM_ID13	AVSTx8_DATA5.SDMCMC_CFG_DATA5						CB59				
SDM			SDM_ID9	AS_nCS01.MSEL2						CV59				
SDM			SDM_ID6	AVSTx8_DATA4_AS_DATA3.SDMCMC_CFG_DATA3						CM59				
SDM			SDM_ID10	AVSTx8_DATA7.SDMCMC_CFG_DATA7						CC58				
SDM			SDM_ID8	AVSTx8_READY_AS_nCS03.SDMCMC_CFG_DATA4						CL60				
SDM			SDM_ID12	PWRMGT_SDA						CC56				
SDM			SDM_ID15	AVSTx8_DATA6.SDMCMC_CFG_DATA6						CA58				
SDM			SDM_ID14	AVSTx8_CLK.PWRMGT_SCL						CG60				
SDM			RREF_SDM							CA54				
SDM			VSIGP_0							CG62				
SDM			VSIGN_0							CE62				
SDM			VSIGP_1							CM62				
SDM			VSIGN_1							CL62				
2D	95	VREFB2DNO	IO				DIFF_RX_2D1n	No		CT3	DQ0	DQ0	DQ0	DQ0
2D	94	VREFB2DNO	IO				DIFF_RX_2D1p	No		CV3	DQ0	DQ0	DQ0	DQ0
2D	93	VREFB2DNO	IO				DIFF_TX_2D1n	No		CR4	DQ0	DQ0	DQ0	DQ0
2D	92	VREFB2DNO	IO				DIFF_TX_2D1p	No		CU4	DQ0	DQ0	DQ0	DQ0
2D	91	VREFB2DNO	IO				DIFF_RX_2D2n	No		CT5	DQsn0	DQ0	DQ0	DQ0
2D	90	VREFB2DNO	IO				DIFF_RX_2D2p	No		CV5	DQsn0	DQ0	DQ0	DQ0
2D	89	VREFB2DNO	IO				DIFF_TX_2D2n	No		CR6	DQsn1	DQsn0/CQn0	DQ0	DQ0
2D	88	VREFB2DNO	IO				DIFF_TX_2D2p	No		CU6	DQsn1	DQsn0/CQ0	DQ0	DQ0
2D	87	VREFB2DNO	IO	CDR			DIFF_RX_2D3n	Yes		CT7	DQ1	DQ0	DQ0	DQ0
2D	86	VREFB2DNO	IO	CDR			DIFF_RX_2D3p	Yes		CV7	DQ1	DQ0	DQ0	DQ0
2D	85	VREFB2DNO	IO				DIFF_TX_2D3n	No		CR8	DQ1	DQ0	DQ0	DQ0
2D	84	VREFB2DNO	IO				DIFF_TX_2D3p	No		CU8	DQ1	DQ0	DQ0	DQ0
2D	83	VREFB2DNO	IO				DIFF_RX_2D4n	No		CT1	DQ2	DQ1	DQ0	DQ0
2D	82	VREFB2DNO	IO				DIFF_RX_2D4p	No		CV1	DQ2	DQ1	DQ0	DQ0
2D	81	VREFB2DNO	IO				DIFF_TX_2D4n	No		CR3	DQ2	DQ1	DQ0	DQ0
2D	80	VREFB2DNO	IO				DIFF_TX_2D4p	No		CU4	DQ2	DQ1	DQ0	DQ0
2D	79	VREFB2DNO	IO				DIFF_RX_2D5n	No		CV5	DQsn2	DQ1	DQ0	DQ0
2D	78	VREFB2DNO	IO				DIFF_RX_2D5p	No		CR5	DQsn2	DQ1	DQ0	DQ0
2D	77	VREFB2DNO	IO	PLL_2D_T_CLKOUT1n			DIFF_TX_2D5n	No		DA6	DQsn3	DQsn1/CQn1	DQsn0/CQn0	DQ0
2D	76	VREFB2DNO	IO	PLL_2D_T_CLKOUT1p,PLL_2D_T_CLKOUT1,PLL_2D_T_FB1			DIFF_TX_2D5p	No		CU6	DQsn3	DQsn1/CQ1	DQsn0/CQ0	DQ0
2D	75	VREFB2DNO	IO	CDR			DIFF_RX_2D6n	Yes		CV7	DQ3	DQ1	DQ0	DQ0
2D	74	VREFB2DNO	IO	R2Q_2D_CDR			DIFF_RX_2D6p	Yes		DB7	DQ3	DQ1	DQ0	DQ0
2D	73	VREFB2DNO	IO	CLK_T_2D_1n			DIFF_TX_2D6n	No		DA8	DQ3	DQ1	DQ0	DQ0
2D	72	VREFB2DNO	IO	CLK_T_2D_1p			DIFF_TX_2D6p	No		CU8	DQ3	DQ1	DQ0	DQ0
2D	71	VREFB2DNO	IO	CLK_T_2D_0n			DIFF_RX_2D7n	No		CR10	DQ4	DQ2	DQ1	DQ0
2D	70	VREFB2DNO	IO	CLK_T_2D_0p			DIFF_RX_2D7p	No		CU10	DQ4	DQ2	DQ1	DQ0
2D	69	VREFB2DNO	IO				DIFF_TX_2D7n	No		CT11	DQ4	DQ2	DQ1	DQ0
2D	68	VREFB2DNO	IO				DIFF_TX_2D7p	No		CV11	DQ4	DQ2	DQ1	DQ0
2D	67	VREFB2DNO	IO	PLL_2D_T_CLKOUT0n			DIFF_RX_2D8n	No		CR12	DQsn4	DQ2	DQ1	DQ0
2D	66	VREFB2DNO	IO	PLL_2D_T_CLKOUT0p,PLL_2D_T_CLKOUT0,PLL_2D_T_FB0			DIFF_RX_2D8p	No		CU12	DQsn4	DQ2	DQ1	DQ0
2D	65	VREFB2DNO	IO				DIFF_TX_2D8n	No		CT13	DQsn5	DQsn2/CQn2	DQ1	DQ0
2D	64	VREFB2DNO	IO				DIFF_TX_2D8p	No		CV13	DQsn5	DQsn2/CQ2	DQ1	DQ0
2D	63	VREFB2DNO	IO	CDR			DIFF_RX_2D9n	Yes		CR14	DQ5	DQ2	DQ1	DQ0
2D	62	VREFB2DNO	IO	CDR			DIFF_RX_2D9p	Yes		CU14	DQ5	DQ2	DQ1	DQ0
2D	61	VREFB2DNO	IO				DIFF_TX_2D9n	No		CT15	DQ5	DQ2	DQ1	DQ0
2D	60	VREFB2DNO	IO				DIFF_TX_2D9p	No		CV15	DQ5	DQ2	DQ1	DQ0
2D	59	VREFB2DNO	IO				DIFF_RX_2D10n	No		DA10	DQ6	DQ3	DQ1	DQ0
2D	58	VREFB2DNO	IO				DIFF_RX_2D10p	No		CU10	DQ6	DQ3	DQ1	DQ0
2D	57	VREFB2DNO	IO				DIFF_TX_2D10n	No		CV11	DQ6	DQ3	DQ1	DQ0
2D	56	VREFB2DNO	IO				DIFF_TX_2D10p	No		DB11	DQ6	DQ3	DQ1	DQ0
2D	55	VREFB2DNO	IO				DIFF_RX_2D11n	No		DA12	DQsn6	DQ3	DQ1	DQ0
2D	54	VREFB2DNO	IO				DIFF_RX_2D11p	No		CU12	DQsn6	DQ3	DQ1	DQ0
2D	53	VREFB2DNO	IO				DIFF_TX_2D11n	No		CV13	DQsn7	DQsn3/CQn3	DQsn1/CQn1	DQ0
2D	52	VREFB2DNO	IO				DIFF_TX_2D11p	No		DB13	DQsn7	DQsn3/CQ3	DQsn1/CQ1	DQ0
2D	51	VREFB2DNO	IO	CDR			DIFF_RX_2D12n	Yes		DA14	DQ7	DQ3	DQ1	DQ0
2D	50	VREFB2DNO	IO	CDR			DIFF_RX_2D12p	Yes		CU14	DQ7	DQ3	DQ1	DQ0
2D	49	VREFB2DNO	IO				DIFF_TX_2D12n	No		CV15	DQ7	DQ3	DQ1	DQ0
2D	48	VREFB2DNO	IO				DIFF_TX_2D12p	No		DB15	DQ7	DQ3	DQ1	DQ0
2D	47	VREFB2DNO	IO	CDR			DIFF_RX_2D13n	Yes		CF3	DQ8	DQ4	DQ2	DQ1
2D	46	VREFB2DNO	IO	CDR			DIFF_RX_2D13p	Yes		CU3	DQ8	DQ4	DQ2	DQ1
2D	45	VREFB2DNO	IO				DIFF_TX_2D13n	No		CE4	DQ8	DQ4	DQ2	DQ1
2D	44	VREFB2DNO	IO				DIFF_TX_2D13p	No		CU4	DQ8	DQ4	DQ2	DQ1
2D	43	VREFB2DNO	IO				DIFF_RX_2D14n	No		CF5	DQsn8	DQ4	DQ2	DQ1
2D	42	VREFB2DNO	IO				DIFF_RX_2D14p	No		CU5	DQsn8	DQ4	DQ2	DQ1
2D	41	VREFB2DNO	IO				DIFF_TX_2D14n	No		CE6	DQsn9	DQsn4/CQn4	DQ2	DQ1
2D	40	VREFB2DNO	IO				DIFF_TX_2D14p	No		CU6	DQsn9	DQsn4/CQ4	DQ2	DQ1
2D	39	VREFB2DNO	IO	CDR			DIFF_RX_2D15n	Yes		CF7	DQ9	DQ4	DQ2	DQ1
2D	38	VREFB2DNO	IO	CDR			DIFF_RX_2D15p	Yes		CU7	DQ9	DQ4	DQ2	DQ1
2D	37	VREFB2DNO	IO				DIFF_TX_2D15n	No		CE8	DQ9	DQ4	DQ2	DQ1
2D	36	VREFB2DNO	IO				DIFF_TX_2D15p	No		CU8	DQ9	DQ4	DQ2	DQ1
2D	35	VREFB2DNO	IO	CDR			DIFF_RX_2D16n	Yes		CK3	DQ10	DQ5	DQ2	DQ1

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQ0 for X4	DQ5 for X8/9	DQ5 for X16/X18	DQ5 for X32/X36
2D		34	VREFB2DNO	IO	CDR		DIFF_RX_2D16p	No		CM3	DQ10	DQ5	DQ2	DQ1
2D		33	VREFB2DNO	IO			DIFF_TX_2D16n	No		CL4	DQ10	DQ5	DQ2	DQ1
2D		32	VREFB2DNO	IO			DIFF_TX_2D16p	No		CM4	DQ10	DQ5	DQ2	DQ1
2D		31	VREFB2DNO	IO			DIFF_RX_2D17n	No		CK5	DQ5n10	DQ5	DQ2	DQ1
2D		30	VREFB2DNO	IO			DIFF_RX_2D17p	No		CM5	DQ5n10	DQ5	DQ2	DQ1
2D		29	VREFB2DNO	IO	PLL_2D_B_CLKOUT1n		DIFF_TX_2D17n	No		CL6	DQ5n11	DQ5n2/CQn5	DQ5n2/CQn2	DQ1
2D		28	VREFB2DNO	IO	PLL_2D_B_CLKOUT1p,PLL_2D_B_CLKOUT1,PLL_2D_B_FB1		DIFF_TX_2D17p	No		CM6	DQ5n11	DQ5n5/CQ5	DQ5n2/CQ2	DQ1
2D		27	VREFB2DNO	IO	CDR		DIFF_RX_2D18n	Yes		CK7	DQ11	DQ5	DQ2	DQ1
2D		26	VREFB2DNO	IO	R2Q_B_2D,CDR		DIFF_RX_2D18p	Yes		CM7	DQ11	DQ5	DQ2	DQ1
2D		25	VREFB2DNO	IO	CLK_B_2D_1n		DIFF_TX_2D18n	No		CL8	DQ11	DQ5	DQ2	DQ1
2D		24	VREFB2DNO	IO	CLK_B_2D_1p		DIFF_TX_2D18p	No		CM8	DQ11	DQ5	DQ2	DQ1
2D		23	VREFB2DNO	IO	CLK_B_2D_0n,CDR		DIFF_RX_2D19n	Yes		CE10	DQ12	DQ6	DQ3	DQ1
2D		22	VREFB2DNO	IO	CLK_B_2D_0p,CDR		DIFF_RX_2D19p	Yes		CG10	DQ12	DQ6	DQ3	DQ1
2D		21	VREFB2DNO	IO			DIFF_TX_2D19n	No		CF11	DQ12	DQ6	DQ3	DQ1
2D		20	VREFB2DNO	IO			DIFF_TX_2D19p	No		CH11	DQ12	DQ6	DQ3	DQ1
2D		19	VREFB2DNO	IO	PLL_2D_B_CLKOUT0n		DIFF_RX_2D20n	No		CE12	DQ5n12	DQ6	DQ3	DQ1
2D		18	VREFB2DNO	IO	PLL_2D_B_CLKOUT0p,PLL_2D_B_CLKOUT0,PLL_2D_B_FB0		DIFF_RX_2D20p	No		CG12	DQ5n12	DQ6	DQ3	DQ1
2D		17	VREFB2DNO	IO			DIFF_TX_2D20n	No		CF13	DQ5n13	DQ5n6/CQn6	DQ3	DQ5n1/CQn1
2D		16	VREFB2DNO	IO			DIFF_TX_2D20p	No		CH13	DQ5n13	DQ5n6/CQ6	DQ3	DQ5n1/CQ1
2D		15	VREFB2DNO	IO	CDR		DIFF_RX_2D21n	Yes		CE14	DQ13	DQ6	DQ3	DQ1
2D		14	VREFB2DNO	IO	CDR		DIFF_RX_2D21p	Yes		CG14	DQ13	DQ6	DQ3	DQ1
2D		13	VREFB2DNO	IO			DIFF_TX_2D21n	No		CF15	DQ13	DQ6	DQ3	DQ1
2D		12	VREFB2DNO	IO			DIFF_TX_2D21p	No		CH15	DQ13	DQ6	DQ3	DQ1
2D		11	VREFB2DNO	IO	CDR		DIFF_RX_2D22n	Yes		CL10	DQ14	DQ7	DQ3	DQ1
2D		10	VREFB2DNO	IO	CDR		DIFF_RX_2D22p	Yes		CM10	DQ14	DQ7	DQ3	DQ1
2D		9	VREFB2DNO	IO			DIFF_TX_2D22n	No		CK11	DQ14	DQ7	DQ3	DQ1
2D		8	VREFB2DNO	IO			DIFF_TX_2D22p	No		CM11	DQ14	DQ7	DQ3	DQ1
2D		7	VREFB2DNO	IO			DIFF_RX_2D23n	No		CL12	DQ5n14	DQ7	DQ3	DQ1
2D		6	VREFB2DNO	IO			DIFF_RX_2D23p	No		CM12	DQ5n14	DQ7	DQ3	DQ1
2D		5	VREFB2DNO	IO			DIFF_TX_2D23n	No		CK13	DQ5n15	DQ5n7/CQn7	DQ3n3/CQn3	DQ1
2D		4	VREFB2DNO	IO			DIFF_TX_2D23p	No		CM13	DQ5n15	DQ5n7/CQ7	DQ3n3/CQ3	DQ1
2D		3	VREFB2DNO	IO	CDR		DIFF_RX_2D24n	Yes		CL14	DQ15	DQ7	DQ3	DQ1
2D		2	VREFB2DNO	IO	CDR		DIFF_RX_2D24p	Yes		CM14	DQ15	DQ7	DQ3	DQ1
2D		1	VREFB2DNO	IO			DIFF_TX_2D24n	No		CK15	DQ15	DQ7	DQ3	DQ1
2D		0	VREFB2DNO	IO			DIFF_TX_2D24p	No		CM15	DQ15	DQ7	DQ3	DQ1
2C		95	VREFB2CNO	IO			DIFF_RX_2C1n	No		CT17	DQ16	DQ8	DQ4	DQ2
2C		94	VREFB2CNO	IO			DIFF_RX_2C1p	No		CV17	DQ16	DQ8	DQ4	DQ2
2C		93	VREFB2CNO	IO			DIFF_TX_2C1n	No		CR18	DQ16	DQ8	DQ4	DQ2
2C		92	VREFB2CNO	IO			DIFF_TX_2C1p	No		CL18	DQ16	DQ8	DQ4	DQ2
2C		91	VREFB2CNO	IO			DIFF_RX_2C2n	No		CT19	DQ5n16	DQ8	DQ4	DQ2
2C		90	VREFB2CNO	IO			DIFF_RX_2C2p	No		CV19	DQ5n16	DQ8	DQ4	DQ2
2C		89	VREFB2CNO	IO			DIFF_TX_2C2n	No		CR20	DQ5n17	DQ5n8/CQn8	DQ4	DQ2
2C		88	VREFB2CNO	IO			DIFF_TX_2C2p	No		CU20	DQ5n17	DQ5n8/CQ8	DQ4	DQ2
2C		87	VREFB2CNO	IO	CDR		DIFF_RX_2C3n	Yes		CT21	DQ17	DQ8	DQ4	DQ2
2C		86	VREFB2CNO	IO	CDR		DIFF_RX_2C3p	Yes		CV21	DQ17	DQ8	DQ4	DQ2
2C		85	VREFB2CNO	IO			DIFF_TX_2C3n	No		CR22	DQ17	DQ8	DQ4	DQ2
2C		84	VREFB2CNO	IO			DIFF_TX_2C3p	No		CU22	DQ17	DQ8	DQ4	DQ2
2C		83	VREFB2CNO	IO			DIFF_RX_2C4n	No		CT17	DQ18	DQ9	DQ4	DQ2
2C		82	VREFB2CNO	IO			DIFF_RX_2C4p	No		CR17	DQ18	DQ9	DQ4	DQ2
2C		81	VREFB2CNO	IO			DIFF_TX_2C4n	No		DA18	DQ18	DQ9	DQ4	DQ2
2C		80	VREFB2CNO	IO			DIFF_TX_2C4p	No		DC18	DQ18	DQ9	DQ4	DQ2
2C		79	VREFB2CNO	IO			DIFF_RX_2C5n	No		CV19	DQ5n18	DQ9	DQ4	DQ2
2C		78	VREFB2CNO	IO			DIFF_RX_2C5p	No		DB19	DQ5n18	DQ9	DQ4	DQ2
2C		77	VREFB2CNO	IO	PLL_2C_T_CLKOUT1n		DIFF_TX_2C5n	No		DA20	DQ5n19	DQ5n9/CQn9	DQ5n4/CQn4	DQ2
2C		76	VREFB2CNO	IO	PLL_2C_T_CLKOUT1p,PLL_2C_T_CLKOUT1,PLL_2C_T_FB1		DIFF_TX_2C5p	No		DC20	DQ5n19	DQ5n9/CQ9	DQ5n4/CQ4	DQ2
2C		75	VREFB2CNO	IO	CDR		DIFF_RX_2C6n	Yes		CV21	DQ19	DQ9	DQ4	DQ2
2C		74	VREFB2CNO	IO	R2Q_T_2C,CDR		DIFF_RX_2C6p	Yes		DB21	DQ19	DQ9	DQ4	DQ2
2C		73	VREFB2CNO	IO	CLK_T_2C_1n		DIFF_TX_2C6n	No		DA22	DQ19	DQ9	DQ4	DQ2
2C		72	VREFB2CNO	IO	CLK_T_2C_1p		DIFF_TX_2C6p	No		DC22	DQ19	DQ9	DQ4	DQ2
2C		71	VREFB2CNO	IO	CLK_T_2C_0n		DIFF_RX_2C7n	No		CR24	DQ20	DQ10	DQ5	DQ2
2C		70	VREFB2CNO	IO	CLK_T_2C_0p		DIFF_RX_2C7p	No		CU24	DQ20	DQ10	DQ5	DQ2
2C		69	VREFB2CNO	IO			DIFF_TX_2C7n	No		CT25	DQ20	DQ10	DQ5	DQ2
2C		68	VREFB2CNO	IO			DIFF_TX_2C7p	No		CV25	DQ20	DQ10	DQ5	DQ2
2C		67	VREFB2CNO	IO	PLL_2C_T_CLKOUT0n		DIFF_RX_2C8n	No		CR26	DQ5n20	DQ10	DQ5	DQ2
2C		66	VREFB2CNO	IO	PLL_2C_T_CLKOUT0p,PLL_2C_T_CLKOUT0,PLL_2C_T_FB0		DIFF_RX_2C8p	No		CU26	DQ5n20	DQ10	DQ5	DQ2
2C		65	VREFB2CNO	IO			DIFF_TX_2C8n	No		CT27	DQ5n21	DQ5n10/CQn10	DQ5	DQ5n2/CQn2
2C		64	VREFB2CNO	IO			DIFF_TX_2C8p	No		CV27	DQ5n21	DQ5n10/CQ10	DQ5	DQ5n2/CQ2
2C		63	VREFB2CNO	IO	CDR		DIFF_RX_2C9n	Yes		CR28	DQ21	DQ10	DQ5	DQ2
2C		62	VREFB2CNO	IO	CDR		DIFF_RX_2C9p	Yes		CU28	DQ21	DQ10	DQ5	DQ2
2C		61	VREFB2CNO	IO			DIFF_TX_2C9n	No		CT29	DQ21	DQ10	DQ5	DQ2
2C		60	VREFB2CNO	IO			DIFF_TX_2C9p	No		CV29	DQ21	DQ10	DQ5	DQ2
2C		59	VREFB2CNO	IO			DIFF_RX_2C10n	No		DA24	DQ22	DQ11	DQ5	DQ2
2C		58	VREFB2CNO	IO			DIFF_RX_2C10p	No		DC24	DQ22	DQ11	DQ5	DQ2
2C		57	VREFB2CNO	IO			DIFF_TX_2C10n	No		CV25	DQ22	DQ11	DQ5	DQ2
2C		56	VREFB2CNO	IO			DIFF_TX_2C10p	No		DB25	DQ22	DQ11	DQ5	DQ2
2C		55	VREFB2CNO	IO			DIFF_RX_2C11n	No		DA26	DQ5n22	DQ11	DQ5	DQ2
2C		54	VREFB2CNO	IO			DIFF_RX_2C11p	No		DC26	DQ5n22	DQ11	DQ5	DQ2
2C		53	VREFB2CNO	IO			DIFF_TX_2C11n	No		CV27	DQ5n23	DQ5n11/CQn11	DQ5n5/CQn5	DQ2
2C		52	VREFB2CNO	IO			DIFF_TX_2C11p	No		DB27	DQ5n23	DQ5n11/CQ11	DQ5n5/CQ5	DQ2
2C		51	VREFB2CNO	IO	CDR		DIFF_RX_2C12n	Yes		DA28	DQ23	DQ11	DQ5	DQ2
2C		50	VREFB2CNO	IO	CDR		DIFF_RX_2C12p	Yes		DC28	DQ23	DQ11	DQ5	DQ2
2C		49	VREFB2CNO	IO			DIFF_TX_2C12n	No		CV29	DQ23	DQ11	DQ5	DQ2
2C		48	VREFB2CNO	IO			DIFF_TX_2C12p	No		DB29	DQ23	DQ11	DQ5	DQ2
2C		47	VREFB2CNO	IO	CDR		DIFF_RX_2C13n	Yes		CF17	DQ24	DQ12	DQ6	DQ3
2C		46	VREFB2CNO	IO	CDR		DIFF_RX_2C13p	Yes		CH17	DQ24	DQ12	DQ6	DQ3
2C		45	VREFB2CNO	IO			DIFF_TX_2C13n	No		CE18	DQ24	DQ12	DQ6	DQ3
2C		44	VREFB2CNO	IO			DIFF_TX_2C13p	No		CG18	DQ24	DQ12	DQ6	DQ3
2C		43	VREFB2CNO	IO			DIFF_RX_2C14n	No		CF19	DQ5n24	DQ12	DQ6	DQ3
2C		42	VREFB2CNO	IO			DIFF_RX_2C14p	No		CH19	DQ5n24	DQ12	DQ6	DQ3
2C		41	VREFB2CNO	IO			DIFF_TX_2C14n	No		CE20	DQ5n25	DQ5n12/CQn12	DQ6	DQ3

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
2C	40	VREFB2CNO	IO				DIFF_TX_2C14p	No		CG20	DO525	DOS12/CQ12	D06	D03
2C	39	VREFB2CNO	IO	CDR			DIFF_RX_2C15n	Yes		CF21	DO25	DG12	D06	D03
2C	38	VREFB2CNO	IO	CDR			DIFF_RX_2C15p	Yes		CH21	DO25	DG12	D06	D03
2C	37	VREFB2CNO	IO				DIFF_TX_2C15n	No		CE22	DO25	DG12	D06	D03
2C	36	VREFB2CNO	IO				DIFF_TX_2C15p	No		CG22	DO25	DG12	D06	D03
2C	35	VREFB2CNO	IO	CDR			DIFF_RX_2C16n	Yes		CM17	DO26	DG13	D06	D03
2C	34	VREFB2CNO	IO	CDR			DIFF_RX_2C16p	Yes		CM17	DO26	DG13	D06	D03
2C	33	VREFB2CNO	IO				DIFF_TX_2C16n	No		CL18	DO26	DG13	D06	D03
2C	32	VREFB2CNO	IO				DIFF_TX_2C16p	No		CM18	DO26	DG13	D06	D03
2C	31	VREFB2CNO	IO				DIFF_RX_2C17n	No		CK19	DOSn26	DG13	D06	D03
2C	30	VREFB2CNO	IO				DIFF_RX_2C17p	No		CM19	DO526	DG13	D06	D03
2C	29	VREFB2CNO	IO	PLL_2C_B_CLKOUT1n			DIFF_TX_2C17n	No		CL20	DOSn27	DOSn13/CQn13	DOSn6/CQn6	D03
2C	28	VREFB2CNO	IO	PLL_2C_B_CLKOUT1p,PLL_2C_B_CLKOUT1,PLL_2C_B_FB1			DIFF_TX_2C17p	No		CM20	DO527	DOS13/CQ13	DOS6/CO6	D03
2C	27	VREFB2CNO	IO	CDR			DIFF_RX_2C18n	Yes		CG21	DO27	DG13	D06	D03
2C	26	VREFB2CNO	IO	RZQ_B_2C.CDR			DIFF_RX_2C18p	Yes		CM21	DO27	DG13	D06	D03
2C	25	VREFB2CNO	IO	CLK_B_2C_1n			DIFF_TX_2C18n	No		CL22	DO27	DG13	D06	D03
2C	24	VREFB2CNO	IO	CLK_B_2C_1p			DIFF_TX_2C18p	No		CM22	DO27	DG13	D06	D03
2C	23	VREFB2CNO	IO	CLK_B_2C_0n.CDR			DIFF_RX_2C19n	Yes		CE24	DO28	DG14	D07	D03
2C	22	VREFB2CNO	IO	CLK_B_2C_0p.CDR			DIFF_RX_2C19p	Yes		CG24	DO28	DG14	D07	D03
2C	21	VREFB2CNO	IO				DIFF_TX_2C19n	No		CF25	DO28	DG14	D07	D03
2C	20	VREFB2CNO	IO				DIFF_TX_2C19p	No		CH25	DO28	DG14	D07	D03
2C	19	VREFB2CNO	IO	PLL_2C_B_CLKOUT0n			DIFF_RX_2C20n	No		CE26	DOSn28	DG14	D07	D03
2C	18	VREFB2CNO	IO	PLL_2C_B_CLKOUT0p,PLL_2C_B_CLKOUT0,PLL_2C_B_FB0			DIFF_RX_2C20p	No		CG26	DOSn28	DG14	D07	D03
2C	17	VREFB2CNO	IO				DIFF_TX_2C20n	No		CF27	DOSn29	DOSn14/CQn14	D07	DOSn3/CO3
2C	16	VREFB2CNO	IO				DIFF_TX_2C20p	No		CH27	DOS29	DOS14/CQ14	D07	DOS3/CO3
2C	15	VREFB2CNO	IO	CDR			DIFF_RX_2C21n	Yes		CE28	DO29	DG14	D07	D03
2C	14	VREFB2CNO	IO	CDR			DIFF_RX_2C21p	Yes		CG28	DO29	DG14	D07	D03
2C	13	VREFB2CNO	IO				DIFF_TX_2C21n	No		CF29	DO29	DG14	D07	D03
2C	12	VREFB2CNO	IO				DIFF_TX_2C21p	No		CH29	DO29	DG14	D07	D03
2C	11	VREFB2CNO	IO	CDR			DIFF_RX_2C22n	Yes		CL24	DO30	DG15	D07	D03
2C	10	VREFB2CNO	IO	CDR			DIFF_RX_2C22p	Yes		CM24	DO30	DG15	D07	D03
2C	9	VREFB2CNO	IO				DIFF_TX_2C22n	No		CK25	DO30	DG15	D07	D03
2C	8	VREFB2CNO	IO				DIFF_TX_2C22p	No		CM25	DO30	DG15	D07	D03
2C	7	VREFB2CNO	IO				DIFF_RX_2C23n	No		CL26	DOSn30	DG15	D07	D03
2C	6	VREFB2CNO	IO				DIFF_RX_2C23p	No		CM26	DO30	DG15	D07	D03
2C	5	VREFB2CNO	IO				DIFF_TX_2C23n	No		CK27	DOSn31	DOSn15/CQn15	DOSn7/CQn7	D03
2C	4	VREFB2CNO	IO				DIFF_TX_2C23p	No		CM27	DOS31	DOS15/CQ15	DOS7/CO7	D03
2C	3	VREFB2CNO	IO	CDR			DIFF_RX_2C24n	Yes		CL28	DO31	DG15	D07	D03
2C	2	VREFB2CNO	IO	CDR			DIFF_RX_2C24p	Yes		CM28	DO31	DG15	D07	D03
2C	1	VREFB2CNO	IO				DIFF_TX_2C24n	No		CK29	DO31	DG15	D07	D03
2C	0	VREFB2CNO	IO				DIFF_TX_2C24p	No		CM29	DO31	DG15	D07	D03
2B	95	VREFB2BNO	IO				DIFF_RX_2B1n	No		CT43	DO32	DG16	D08	D04
2B	94	VREFB2BNO	IO				DIFF_RX_2B1p	No		CV43	DO32	DG16	D08	D04
2B	93	VREFB2BNO	IO				DIFF_TX_2B1n	No		CR42	DO32	DG16	D08	D04
2B	92	VREFB2BNO	IO				DIFF_TX_2B1p	No		CU42	DO32	DG16	D08	D04
2B	91	VREFB2BNO	IO				DIFF_RX_2B2n	No		CT41	DOSn32	DG16	D08	D04
2B	90	VREFB2BNO	IO				DIFF_RX_2B2p	No		CV41	DOS32	DG16	D08	D04
2B	89	VREFB2BNO	IO				DIFF_TX_2B2n	No		CR40	DOSn33	DOSn16/CQn16	D08	D04
2B	88	VREFB2BNO	IO				DIFF_TX_2B2p	No		CU40	DOS33	DOS16/CQ16	D08	D04
2B	87	VREFB2BNO	IO	CDR			DIFF_RX_2B3n	Yes		CT39	DO33	DG16	D08	D04
2B	86	VREFB2BNO	IO	CDR			DIFF_RX_2B3p	Yes		CV39	DO33	DG16	D08	D04
2B	85	VREFB2BNO	IO				DIFF_TX_2B3n	No		CR38	DO33	DG16	D08	D04
2B	84	VREFB2BNO	IO				DIFF_TX_2B3p	No		CU38	DO33	DG16	D08	D04
2B	83	VREFB2BNO	IO				DIFF_RX_2B4n	No		CV43	DO34	DG17	D08	D04
2B	82	VREFB2BNO	IO				DIFF_RX_2B4p	No		DB43	DO34	DG17	D08	D04
2B	81	VREFB2BNO	IO				DIFF_TX_2B4n	No		DA42	DO34	DG17	D08	D04
2B	80	VREFB2BNO	IO				DIFF_TX_2B4p	No		DC42	DO34	DG17	D08	D04
2B	79	VREFB2BNO	IO				DIFF_RX_2B5n	No		CV41	DOSn34	DG17	D08	D04
2B	78	VREFB2BNO	IO				DIFF_RX_2B5p	No		DB41	DO34	DG17	D08	D04
2B	77	VREFB2BNO	IO	PLL_2B_T_CLKOUT1n			DIFF_TX_2B5n	No		DA40	DOSn35	DOSn17/CQn17	DOSn8/CQn8	D04
2B	76	VREFB2BNO	IO	PLL_2B_T_CLKOUT1p,PLL_2B_T_CLKOUT1,PLL_2B_T_FB1			DIFF_TX_2B5p	No		DC40	DOS35	DOS17/CQ17	DOS8/CO8	D04
2B	75	VREFB2BNO	IO	CDR			DIFF_RX_2B6n	Yes		CV39	DO35	DG17	D08	D04
2B	74	VREFB2BNO	IO	RZQ_T_2B.CDR			DIFF_RX_2B6p	Yes		DB39	DO35	DG17	D08	D04
2B	73	VREFB2BNO	IO	CLK_T_2B_1n			DIFF_TX_2B6n	No		DA38	DO35	DG17	D08	D04
2B	72	VREFB2BNO	IO	CLK_T_2B_1p			DIFF_TX_2B6p	No		DC38	DO35	DG17	D08	D04
2B	71	VREFB2BNO	IO	CLK_T_2B_0n			DIFF_RX_2B7n	No		CR36	DO36	DG18	D09	D04
2B	70	VREFB2BNO	IO	CLK_T_2B_0p			DIFF_RX_2B7p	No		CU36	DO36	DG18	D09	D04
2B	69	VREFB2BNO	IO				DIFF_TX_2B7n	No		CT35	DO36	DG18	D09	D04
2B	68	VREFB2BNO	IO				DIFF_TX_2B7p	No		CV35	DO36	DG18	D09	D04
2B	67	VREFB2BNO	IO	PLL_2B_T_CLKOUT0n			DIFF_RX_2B8n	No		CR34	DOSn36	DG18	D09	D04
2B	66	VREFB2BNO	IO	PLL_2B_T_CLKOUT0p,PLL_2B_T_CLKOUT0,PLL_2B_T_FB0			DIFF_RX_2B8p	No		CU34	DOS36	DG18	D09	D04
2B	65	VREFB2BNO	IO				DIFF_TX_2B8n	No		CT33	DOSn37	DOSn18/CQn18	D09	DOSn4/CQn4
2B	64	VREFB2BNO	IO				DIFF_TX_2B8p	No		CV33	DOS37	DOS18/CQ18	D09	DOS4/CO4
2B	63	VREFB2BNO	IO	CDR			DIFF_RX_2B9n	Yes		CR32	DO37	DG18	D09	D04
2B	62	VREFB2BNO	IO	CDR			DIFF_RX_2B9p	Yes		CU32	DO37	DG18	D09	D04
2B	61	VREFB2BNO	IO				DIFF_TX_2B9n	No		CT31	DO37	DG18	D09	D04
2B	60	VREFB2BNO	IO				DIFF_TX_2B9p	No		CV31	DO37	DG18	D09	D04
2B	59	VREFB2BNO	IO				DIFF_RX_2B10n	No		DA36	DO38	DG19	D09	D04
2B	58	VREFB2BNO	IO				DIFF_RX_2B10p	No		DC36	DO38	DG19	D09	D04
2B	57	VREFB2BNO	IO				DIFF_TX_2B10n	No		CV35	DO38	DG19	D09	D04
2B	56	VREFB2BNO	IO				DIFF_TX_2B10p	No		DB35	DO38	DG19	D09	D04
2B	55	VREFB2BNO	IO				DIFF_RX_2B11n	No		DA34	DOSn38	DG19	D09	D04
2B	54	VREFB2BNO	IO				DIFF_RX_2B11p	No		DC34	DOS38	DG19	D09	D04
2B	53	VREFB2BNO	IO				DIFF_TX_2B11n	No		CV33	DOSn39	DOSn19/CQn19	DOSn9/CQn9	D04
2B	52	VREFB2BNO	IO				DIFF_TX_2B11p	No		DB33	DOS39	DOS19/CQ19	DOS9/CO9	D04
2B	51	VREFB2BNO	IO	CDR			DIFF_RX_2B12n	Yes		DA32	DO39	DG19	D09	D04
2B	50	VREFB2BNO	IO	CDR			DIFF_RX_2B12p	Yes		DC32	DO39	DG19	D09	D04
2B	49	VREFB2BNO	IO				DIFF_TX_2B12n	No		CV31	DO39	DG19	D09	D04
2B	48	VREFB2BNO	IO				DIFF_TX_2B12p	No		DB31	DO39	DG19	D09	D04
2B	47	VREFB2BNO	IO	CDR			DIFF_RX_2B13n	Yes		CF43	DO40	DG20	D10	D05

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
2B	46	VREFB2BNO	IO	CDR			DIFF_RX_2B13p	No		CH43	DQ40	DQ20	DQ10	DQ5
2B	45	VREFB2BNO	IO				DIFF_TX_2B13n	No		CE42	DQ40	DQ20	DQ10	DQ5
2B	44	VREFB2BNO	IO				DIFF_TX_2B13p	No		CG42	DQ40	DQ20	DQ10	DQ5
2B	43	VREFB2BNO	IO				DIFF_RX_2B14n	No		CF41	DQ5n40	DQ20	DQ10	DQ5
2B	42	VREFB2BNO	IO				DIFF_RX_2B14p	No		CH41	DQ540	DQ20	DQ10	DQ5
2B	41	VREFB2BNO	IO				DIFF_TX_2B14n	No		CE40	DQ5n41	DQ5n20/CQn20	DQ10	DQ5
2B	40	VREFB2BNO	IO				DIFF_TX_2B14p	No		CG40	DQ541	DQ520/CQ20	DQ10	DQ5
2B	39	VREFB2BNO	IO	CDR			DIFF_RX_2B15n	Yes		CF39	DQ41	DQ20	DQ10	DQ5
2B	38	VREFB2BNO	IO	CDR			DIFF_RX_2B15p	Yes		CH39	DQ41	DQ20	DQ10	DQ5
2B	37	VREFB2BNO	IO				DIFF_TX_2B15n	No		CE38	DQ41	DQ20	DQ10	DQ5
2B	36	VREFB2BNO	IO				DIFF_TX_2B15p	No		CG38	DQ41	DQ20	DQ10	DQ5
2B	35	VREFB2BNO	IO	CDR			DIFF_RX_2B16n	Yes		CF43	DQ42	DQ21	DQ10	DQ5
2B	34	VREFB2BNO	IO	CDR			DIFF_RX_2B16p	Yes		CH43	DQ42	DQ21	DQ10	DQ5
2B	33	VREFB2BNO	IO				DIFF_TX_2B16n	No		CE42	DQ42	DQ21	DQ10	DQ5
2B	32	VREFB2BNO	IO				DIFF_TX_2B16p	No		CG42	DQ42	DQ21	DQ10	DQ5
2B	31	VREFB2BNO	IO				DIFF_RX_2B17n	No		CF41	DQ5n42	DQ21	DQ10	DQ5
2B	30	VREFB2BNO	IO				DIFF_RX_2B17p	No		CH41	DQ542	DQ21	DQ10	DQ5
2B	29	VREFB2BNO	IO	PLL_2B_B_CLKOUT1n			DIFF_TX_2B17n	No		CL40	DQ5n43	DQ5n21/CQn21	DQ5n10/CQn10	DQ5
2B	28	VREFB2BNO	IO	PLL_2B_B_CLKOUT1p,PLL_2B_B_CLKOUT1,PLL_2B_B_FB1			DIFF_TX_2B17p	No		CH40	DQ543	DQ521/CQ21	DQ5010/CQ10	DQ5
2B	27	VREFB2BNO	IO	CDR			DIFF_RX_2B18n	Yes		CF39	DQ43	DQ21	DQ10	DQ5
2B	26	VREFB2BNO	IO	RZQ_B_2B_CDR			DIFF_RX_2B18p	Yes		CH39	DQ43	DQ21	DQ10	DQ5
2B	25	VREFB2BNO	IO	CLK_B_2B_1n			DIFF_TX_2B18n	No		CL38	DQ43	DQ21	DQ10	DQ5
2B	24	VREFB2BNO	IO	CLK_B_2B_1p			DIFF_TX_2B18p	No		CH38	DQ43	DQ21	DQ10	DQ5
2B	23	VREFB2BNO	IO	CLK_B_2B_0n,CDR			DIFF_RX_2B19n	Yes		CF36	DQ44	DQ22	DQ11	DQ5
2B	22	VREFB2BNO	IO	CLK_B_2B_0p,CDR			DIFF_RX_2B19p	Yes		CH36	DQ44	DQ22	DQ11	DQ5
2B	21	VREFB2BNO	IO				DIFF_TX_2B19n	No		CF35	DQ44	DQ22	DQ11	DQ5
2B	20	VREFB2BNO	IO				DIFF_TX_2B19p	No		CH35	DQ44	DQ22	DQ11	DQ5
2B	19	VREFB2BNO	IO	PLL_2B_B_CLKOUT0n			DIFF_RX_2B20n	No		CE34	DQ5n44	DQ22	DQ11	DQ5
2B	18	VREFB2BNO	IO	PLL_2B_B_CLKOUT0p,PLL_2B_B_CLKOUT0,PLL_2B_B_FB0			DIFF_RX_2B20p	No		CF34	DQ544	DQ22	DQ11	DQ5
2B	17	VREFB2BNO	IO				DIFF_TX_2B20n	No		CH33	DQ5n45	DQ5n22/CQn22	DQ11	DQ5n5/CQ5n5
2B	16	VREFB2BNO	IO	CDR			DIFF_TX_2B20p	No		CE33	DQ45	DQ522/CQ22	DQ11	DQ5
2B	15	VREFB2BNO	IO	CDR			DIFF_RX_2B21n	Yes		CF32	DQ45	DQ22	DQ11	DQ5
2B	14	VREFB2BNO	IO	CDR			DIFF_RX_2B21p	Yes		CH32	DQ45	DQ22	DQ11	DQ5
2B	13	VREFB2BNO	IO				DIFF_TX_2B21n	No		CF31	DQ45	DQ22	DQ11	DQ5
2B	12	VREFB2BNO	IO				DIFF_TX_2B21p	No		CH31	DQ45	DQ22	DQ11	DQ5
2B	11	VREFB2BNO	IO	CDR			DIFF_RX_2B22n	Yes		CL36	DQ46	DQ23	DQ11	DQ5
2B	10	VREFB2BNO	IO	CDR			DIFF_RX_2B22p	Yes		CH36	DQ46	DQ23	DQ11	DQ5
2B	9	VREFB2BNO	IO				DIFF_TX_2B22n	No		CF35	DQ46	DQ23	DQ11	DQ5
2B	8	VREFB2BNO	IO				DIFF_TX_2B22p	No		CH35	DQ46	DQ23	DQ11	DQ5
2B	7	VREFB2BNO	IO				DIFF_RX_2B23n	No		CL34	DQ5n46	DQ23	DQ11	DQ5
2B	6	VREFB2BNO	IO				DIFF_RX_2B23p	No		CH34	DQ546	DQ23	DQ11	DQ5
2B	5	VREFB2BNO	IO				DIFF_TX_2B23n	No		CF33	DQ5n47	DQ5n23/CQn23	DQ5n11/CQn11	DQ5
2B	4	VREFB2BNO	IO				DIFF_TX_2B23p	No		CH33	DQ547	DQ523/CQ23	DQ511/CQ11	DQ5
2B	3	VREFB2BNO	IO	CDR			DIFF_RX_2B24n	Yes		CL32	DQ47	DQ23	DQ11	DQ5
2B	2	VREFB2BNO	IO	CDR			DIFF_RX_2B24p	Yes		CH32	DQ47	DQ23	DQ11	DQ5
2B	1	VREFB2BNO	IO				DIFF_TX_2B24n	No		CF31	DQ47	DQ23	DQ11	DQ5
2A	95	VREFB2ANO	IO				DIFF_TX_2A11n	No		CM31	DQ47	DQ23	DQ11	DQ5
2A	94	VREFB2ANO	IO				DIFF_RX_2A11p	No		CF37	DQ48	DQ24	DQ12	DQ6
2A	93	VREFB2ANO	IO				DIFF_TX_2A11n	No		CV57	DQ48	DQ24	DQ12	DQ6
2A	92	VREFB2ANO	IO				DIFF_TX_2A1p	No		CR56	DQ48	DQ24	DQ12	DQ6
2A	91	VREFB2ANO	IO				DIFF_RX_2A2n	No		CV55	DQ5n48	DQ24	DQ12	DQ6
2A	90	VREFB2ANO	IO				DIFF_RX_2A2p	No		CH55	DQ548	DQ24	DQ12	DQ6
2A	89	VREFB2ANO	IO				DIFF_TX_2A2n	No		CF54	DQ5n49	DQ5n24/CQn24	DQ12	DQ6
2A	88	VREFB2ANO	IO				DIFF_TX_2A2p	No		CH54	DQ549	DQ524/CQ24	DQ12	DQ6
2A	87	VREFB2ANO	IO	CDR			DIFF_RX_2A3n	Yes		CF53	DQ49	DQ24	DQ12	DQ6
2A	86	VREFB2ANO	IO	CDR			DIFF_RX_2A3p	Yes		CH53	DQ49	DQ24	DQ12	DQ6
2A	85	VREFB2ANO	IO				DIFF_TX_2A3n	No		CF52	DQ49	DQ24	DQ12	DQ6
2A	84	VREFB2ANO	IO				DIFF_TX_2A3p	No		CH52	DQ49	DQ24	DQ12	DQ6
2A	83	VREFB2ANO	IO				DIFF_RX_2A4n	No		CV57	DQ50	DQ25	DQ12	DQ6
2A	82	VREFB2ANO	IO				DIFF_RX_2A4p	No		CH57	DQ50	DQ25	DQ12	DQ6
2A	81	VREFB2ANO	IO				DIFF_TX_2A4n	No		DA56	DQ50	DQ25	DQ12	DQ6
2A	80	VREFB2ANO	IO				DIFF_TX_2A4p	No		CF56	DQ50	DQ25	DQ12	DQ6
2A	79	VREFB2ANO	IO				DIFF_RX_2A5n	No		CV55	DQ5n50	DQ25	DQ12	DQ6
2A	78	VREFB2ANO	IO				DIFF_RX_2A5p	No		CH55	DQ550	DQ25	DQ12	DQ6
2A	77	VREFB2ANO	IO	PLL_2A_T_CLKOUT1n			DIFF_TX_2A5n	No		DA54	DQ5n51	DQ5n25/CQn25	DQ5n12/CQn12	DQ6
2A	76	VREFB2ANO	IO	PLL_2A_T_CLKOUT1p,PLL_2A_T_CLKOUT1,PLL_2A_T_FB1			DIFF_TX_2A5p	No		CH54	DQ551	DQ525/CQ25	DQ512/CQ12	DQ6
2A	75	VREFB2ANO	IO	CDR			DIFF_RX_2A6n	Yes		CV53	DQ51	DQ25	DQ12	DQ6
2A	74	VREFB2ANO	IO	RZQ_T_2A_CDR			DIFF_RX_2A6p	Yes		CH53	DQ51	DQ25	DQ12	DQ6
2A	73	VREFB2ANO	IO	CLK_T_2A_1n			DIFF_TX_2A6n	No		DA52	DQ51	DQ25	DQ12	DQ6
2A	72	VREFB2ANO	IO	CLK_T_2A_1p			DIFF_TX_2A6p	No		CF52	DQ51	DQ25	DQ12	DQ6
2A	71	VREFB2ANO	IO	CLK_T_2A_0n			DIFF_RX_2A7n	No		CR50	DQ52	DQ26	DQ13	DQ6
2A	70	VREFB2ANO	IO	CLK_T_2A_0p			DIFF_RX_2A7p	No		CH50	DQ52	DQ26	DQ13	DQ6
2A	69	VREFB2ANO	IO				DIFF_TX_2A7n	No		CF49	DQ52	DQ26	DQ13	DQ6
2A	68	VREFB2ANO	IO				DIFF_TX_2A7p	No		CH49	DQ52	DQ26	DQ13	DQ6
2A	67	VREFB2ANO	IO	PLL_2A_T_CLKOUT0n			DIFF_RX_2A8n	No		CR48	DQ5n52	DQ26	DQ13	DQ6
2A	66	VREFB2ANO	IO	PLL_2A_T_CLKOUT0p,PLL_2A_T_CLKOUT0,PLL_2A_T_FB0			DIFF_RX_2A8p	No		CH48	DQ552	DQ26	DQ13	DQ6
2A	65	VREFB2ANO	IO				DIFF_TX_2A8n	No		CF47	DQ5n53	DQ5n26/CQn26	DQ13	DQ5n6/CQn6
2A	64	VREFB2ANO	IO				DIFF_TX_2A8p	No		CH47	DQ553	DQ526/CQ26	DQ13	DQ56/CQ6
2A	63	VREFB2ANO	IO	CDR			DIFF_RX_2A9n	Yes		CR46	DQ53	DQ26	DQ13	DQ6
2A	62	VREFB2ANO	IO	CDR			DIFF_RX_2A9p	Yes		CH46	DQ53	DQ26	DQ13	DQ6
2A	61	VREFB2ANO	IO				DIFF_TX_2A9n	No		CF45	DQ53	DQ26	DQ13	DQ6
2A	60	VREFB2ANO	IO				DIFF_TX_2A9p	No		CH45	DQ53	DQ26	DQ13	DQ6
2A	59	VREFB2ANO	IO				DIFF_RX_2A10n	No		DA50	DQ54	DQ27	DQ13	DQ6
2A	58	VREFB2ANO	IO				DIFF_RX_2A10p	No		CH50	DQ54	DQ27	DQ13	DQ6
2A	57	VREFB2ANO	IO				DIFF_TX_2A10n	No		CF49	DQ54	DQ27	DQ13	DQ6
2A	56	VREFB2ANO	IO				DIFF_TX_2A10p	No		CH49	DQ54	DQ27	DQ13	DQ6
2A	55	VREFB2ANO	IO				DIFF_RX_2A11n	No		DA48	DQ5n54	DQ27	DQ13	DQ6
2A	54	VREFB2ANO	IO				DIFF_RX_2A11p	No		CH48	DQ554	DQ27	DQ13	DQ6
2A	53	VREFB2ANO	IO				DIFF_TX_2A11n	No		CF47	DQ5n55	DQ5n27/CQn27	DQ5n13/CQn13	DQ6

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
2A	52	VREFB2ANO	IO				DIFF_TX_2A11p	No		D847	D0S55	D0S27/C027	D0S13/C013	D06
2A	51	VREFB2ANO	IO	CDR			DIFF_RX_2A12n	Yes		DA46	D0S55	D027	D013	D06
2A	50	VREFB2ANO	IO	CDR			DIFF_RX_2A12p	Yes		DC46	D0S55	D027	D013	D06
2A	49	VREFB2ANO	IO				DIFF_TX_2A12n	No		CY45	D0S55	D027	D013	D06
2A	48	VREFB2ANO	IO				DIFF_TX_2A12p	No		DB45	D0S55	D027	D013	D06
2A	47	VREFB2ANO	IO	CDR			DIFF_RX_2A13n	Yes		CF57	D0S56	D028	D014	D07
2A	46	VREFB2ANO	IO	CDR			DIFF_RX_2A13p	Yes		CH57	D0S56	D028	D014	D07
2A	45	VREFB2ANO	IO				DIFF_TX_2A13n	No		CE56	D0S56	D028	D014	D07
2A	44	VREFB2ANO	IO				DIFF_TX_2A13p	No		CG56	D0S56	D028	D014	D07
2A	43	VREFB2ANO	IO				DIFF_RX_2A14n	No		CF55	D0S56	D028	D014	D07
2A	42	VREFB2ANO	IO				DIFF_RX_2A14p	No		CH55	D0S56	D028	D014	D07
2A	41	VREFB2ANO	IO				DIFF_TX_2A14n	No		CE54	D0S57	D0S28/C0n28	D014	D07
2A	40	VREFB2ANO	IO				DIFF_TX_2A14p	No		CG54	D0S57	D0S28/C028	D014	D07
2A	39	VREFB2ANO	IO	CDR			DIFF_RX_2A15n	Yes		CF53	D0S7	D028	D014	D07
2A	38	VREFB2ANO	IO	CDR			DIFF_RX_2A15p	Yes		CH53	D0S7	D028	D014	D07
2A	37	VREFB2ANO	IO				DIFF_TX_2A15n	No		CE52	D0S7	D028	D014	D07
2A	36	VREFB2ANO	IO				DIFF_TX_2A15p	No		CG52	D0S7	D028	D014	D07
2A	35	VREFB2ANO	IO	CDR			DIFF_RX_2A16n	Yes		CK57	D0S8	D029	D014	D07
2A	34	VREFB2ANO	IO	CDR			DIFF_RX_2A16p	Yes		CM57	D0S8	D029	D014	D07
2A	33	VREFB2ANO	IO				DIFF_TX_2A16n	No		CL56	D0S8	D029	D014	D07
2A	32	VREFB2ANO	IO				DIFF_TX_2A16p	No		CM56	D0S8	D029	D014	D07
2A	31	VREFB2ANO	IO				DIFF_RX_2A17n	No		CK55	D0S58	D029	D014	D07
2A	30	VREFB2ANO	IO				DIFF_RX_2A17p	No		CM55	D0S58	D029	D014	D07
2A	29	VREFB2ANO	IO	PLL_2A_B_CLKOUT1n			DIFF_TX_2A17n	No		CL54	D0S59	D0S29/C0n29	D0S14/C0n14	D07
2A	28	VREFB2ANO	IO	PLL_2A_B_CLKOUT1p,PLL_2A_B_CLKOUT1,PLL_2A_B_FB1			DIFF_TX_2A17p	No		CM54	D0S59	D0S29/C029	D0S14/C014	D07
2A	27	VREFB2ANO	IO	CDR			DIFF_RX_2A18n	Yes		CK53	D0S9	D029	D014	D07
2A	26	VREFB2ANO	IO	RZQ_B_2A_CDR			DIFF_RX_2A18p	Yes		CM53	D0S9	D029	D014	D07
2A	25	VREFB2ANO	IO	CLK_B_2A_1n			DIFF_TX_2A18n	No		CL52	D0S9	D029	D014	D07
2A	24	VREFB2ANO	IO	CLK_B_2A_1p			DIFF_TX_2A18p	No		CM52	D0S9	D029	D014	D07
2A	23	VREFB2ANO	IO	CLK_B_2A_0n_CDR			DIFF_RX_2A19n	Yes		CE50	D060	D030	D015	D07
2A	22	VREFB2ANO	IO	CLK_B_2A_0p_CDR			DIFF_RX_2A19p	Yes		CE50	D060	D030	D015	D07
2A	21	VREFB2ANO	IO				DIFF_TX_2A19n	No		CF49	D060	D030	D015	D07
2A	20	VREFB2ANO	IO				DIFF_TX_2A19p	No		CH49	D060	D030	D015	D07
2A	19	VREFB2ANO	IO	PLL_2A_B_CLKOUT0n			DIFF_RX_2A20n	No		CE48	D0S60	D030	D015	D07
2A	18	VREFB2ANO	IO	PLL_2A_B_CLKOUT0p,PLL_2A_B_CLKOUT0,PLL_2A_B_FB0			DIFF_RX_2A20p	No		CG48	D0S60	D030	D015	D07
2A	17	VREFB2ANO	IO				DIFF_TX_2A20n	No		CF47	D0S61	D0S30/C0n30	D015	D0S7/C07
2A	16	VREFB2ANO	IO				DIFF_TX_2A20p	No		CH47	D0S61	D0S30/C030	D015	D0S7/C07
2A	15	VREFB2ANO	IO	CDR			DIFF_RX_2A21n	Yes		CE46	D061	D030	D015	D07
2A	14	VREFB2ANO	IO	CDR			DIFF_RX_2A21p	Yes		CG46	D061	D030	D015	D07
2A	13	VREFB2ANO	IO				DIFF_TX_2A21n	No		CF45	D061	D030	D015	D07
2A	12	VREFB2ANO	IO				DIFF_TX_2A21p	No		CH45	D061	D030	D015	D07
2A	11	VREFB2ANO	IO	CDR			DIFF_RX_2A22n	Yes		CL50	D062	D031	D015	D07
2A	10	VREFB2ANO	IO	CDR			DIFF_RX_2A22p	Yes		CM50	D062	D031	D015	D07
2A	9	VREFB2ANO	IO				DIFF_TX_2A22n	No		CK49	D062	D031	D015	D07
2A	8	VREFB2ANO	IO				DIFF_TX_2A22p	No		CM49	D062	D031	D015	D07
2A	7	VREFB2ANO	IO				DIFF_RX_2A23n	No		CL48	D0S62	D031	D015	D07
2A	6	VREFB2ANO	IO				DIFF_RX_2A23p	No		CM48	D0S62	D031	D015	D07
2A	5	VREFB2ANO	IO				DIFF_TX_2A23n	No		CK47	D0S63	D0S31/C0n31	D0S15/C0n15	D07
2A	4	VREFB2ANO	IO	CDR			DIFF_TX_2A23p	No		CM47	D0S63	D0S31/C031	D0S15/C015	D07
2A	3	VREFB2ANO	IO	CDR			DIFF_RX_2A24n	Yes		CL46	D063	D031	D015	D07
2A	2	VREFB2ANO	IO	CDR			DIFF_RX_2A24p	Yes		CM46	D063	D031	D015	D07
2A	1	VREFB2ANO	IO				DIFF_TX_2A24n	No		CK45	D063	D031	D015	D07
2A	0	VREFB2ANO	IO				DIFF_TX_2A24p	No		CM45	D063	D031	D015	D07
3D	95	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D1n	No		H5	D064	D032	D016	D08
3D	94	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D1p	No		F5	D064	D032	D016	D08
3D	93	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D1n	No		H6	D064	D032	D016	D08
3D	92	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D1p	No		G6	D064	D032	D016	D08
3D	91	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D2n	No		H7	D0S64	D032	D016	D08
3D	90	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D2p	No		F7	D0S64	D032	D016	D08
3D	89	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D2n	No		J8	D0S65	D0S32/C0n32	D016	D08
3D	88	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D2p	No		G8	D0S65	D0S32/C032	D016	D08
3D	87	VREFB3DNO	IO	CDR		HPS_DDR	DIFF_RX_3D3n	Yes		H9	D065	D032	D016	D08
3D	86	VREFB3DNO	IO	CDR		HPS_DDR	DIFF_RX_3D3p	Yes		F9	D065	D032	D016	D08
3D	85	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D3n	No		J10	D065	D032	D016	D08
3D	84	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D3p	No		G10	D065	D032	D016	D08
3D	83	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D4n	No		D5	D066	D033	D016	D08
3D	82	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D4p	No		B5	D066	D033	D016	D08
3D	81	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D4n	No		C6	D066	D033	D016	D08
3D	80	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D4p	No		A6	D066	D033	D016	D08
3D	79	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D5n	No		D7	D0S66	D033	D016	D08
3D	78	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D5p	No		B7	D0S66	D033	D016	D08
3D	77	VREFB3DNO	IO	PLL_3D_T_CLKOUT1n		HPS_DDR	DIFF_TX_3D5n	No		C8	D0S67	D0S33/C0n33	D0S16/C0n16	D08
3D	76	VREFB3DNO	IO	PLL_3D_T_CLKOUT1p,PLL_3D_T_CLKOUT1,PLL_3D_T_FB1		HPS_DDR	DIFF_RX_3D5p	No		A8	D0S67	D0S33/C033	D0S16/C016	D08
3D	75	VREFB3DNO	IO	CDR		HPS_DDR	DIFF_RX_3D6n	Yes		D9	D067	D033	D016	D08
3D	74	VREFB3DNO	IO	RZQ_T_3D_CDR		HPS_DDR	DIFF_RX_3D6p	Yes		B9	D067	D033	D016	D08
3D	73	VREFB3DNO	IO	CLK_T_3D_1n		HPS_DDR	DIFF_TX_3D6n	No		C10	D067	D033	D016	D08
3D	72	VREFB3DNO	IO	CLK_T_3D_1p		HPS_DDR	DIFF_TX_3D6p	No		A10	D067	D033	D016	D08
3D	71	VREFB3DNO	IO	CLK_T_3D_0n		HPS_DDR	DIFF_RX_3D7n	No		J12	D068	D034	D017	D08
3D	70	VREFB3DNO	IO	CLK_T_3D_0p		HPS_DDR	DIFF_RX_3D7p	No		G12	D068	D034	D017	D08
3D	69	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D7n	No		H13	D068	D034	D017	D08
3D	68	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D7p	No		F13	D068	D034	D017	D08
3D	67	VREFB3DNO	IO	PLL_3D_T_CLKOUT0n		HPS_DDR	DIFF_RX_3D8n	No		I14	D0S68	D034	D017	D08
3D	66	VREFB3DNO	IO	PLL_3D_T_CLKOUT0p,PLL_3D_T_CLKOUT0,PLL_3D_T_FB0		HPS_DDR	DIFF_RX_3D8p	No		G14	D0S68	D034	D017	D08
3D	65	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D8n	No		H15	D0S69	D0S34/C0n34	D017	D0S8/C0n8
3D	64	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D8p	No		F15	D0S69	D0S34/C034	D017	D0S8/C08
3D	63	VREFB3DNO	IO	CDR		HPS_DDR	DIFF_RX_3D9n	Yes		I16	D069	D034	D017	D08
3D	62	VREFB3DNO	IO	CDR		HPS_DDR	DIFF_RX_3D9p	Yes		G16	D069	D034	D017	D08
3D	61	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D9n	No		H17	D069	D034	D017	D08
3D	60	VREFB3DNO	IO			HPS_DDR	DIFF_TX_3D9p	No		F17	D069	D034	D017	D08
3D	59	VREFB3DNO	IO			HPS_DDR	DIFF_RX_3D10n	No		C12	D070	D035	D017	D08

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
3D	58	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D10p	No		A12	DO70	DQ35	DQ17	DQ8
3D	57	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D10n	No		D13	DO70	DQ35	DQ17	DQ8
3D	56	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D10p	No		B13	DO70	DQ35	DQ17	DQ8
3D	55	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D11n	No		C14	DQsn70	DQ35	DQ17	DQ8
3D	54	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D11p	No		A14	DO570	DQ35	DQ17	DQ8
3D	53	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D11n	No		D15	DQsn71	DQsn35/CQn35	DQsn17/CQn17	DQ8
3D	52	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D11p	No		B15	DQ571	DQ35/CQ35	DQ517/CQ17	DQ8
3D	51	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D12n	Yes		C16	DO71	DQ35	DQ17	DQ8
3D	50	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D12p	Yes		A16	DO71	DQ35	DQ17	DQ8
3D	49	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D12n	No		D17	DO71	DQ35	DQ17	DQ8
3D	48	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D12p	No		B17	DO71	DQ35	DQ17	DQ8
3D	47	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D13n	Yes		V5	DO72	DQ36	DQ18	DO9
3D	46	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D13p	Yes		T5	DO72	DQ36	DQ18	DO9
3D	45	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D13n	No		W6	DO72	DQ36	DQ18	DO9
3D	44	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D13p	No		V6	DO72	DQ36	DQ18	DO9
3D	43	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D14n	No		V7	DQsn72	DQ36	DQ18	DO9
3D	42	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D14p	No		T7	DQ572	DQ36	DQ18	DO9
3D	41	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D14n	No		W8	DQsn73	DQsn36/CQn36	DQ18	DO9
3D	40	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D14p	No		U8	DQ573	DQ36/CQ36	DQ18	DO9
3D	39	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D15n	Yes		V9	DO73	DQ36	DQ18	DO9
3D	38	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D15p	Yes		T9	DO73	DQ36	DQ18	DO9
3D	37	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D15n	No		W10	DO73	DQ36	DQ18	DO9
3D	36	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D15p	No		L10	DO73	DQ36	DQ18	DO9
3D	35	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D16n	Yes		P5	DO74	DQ37	DQ18	DO9
3D	34	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D16p	Yes		M5	DO74	DQ37	DQ18	DO9
3D	33	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D16n	No		N6	DO74	DQ37	DQ18	DO9
3D	32	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D16p	No		L6	DO74	DQ37	DQ18	DO9
3D	31	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D17n	No		P7	DQsn74	DQ37	DQ18	DO9
3D	30	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D17p	No		M7	DO574	DQ37	DQ18	DO9
3D	29	VREFB3D0N	IO	P1L_3D_B_CLKOUT1n		HPS_DDR	DIFF_TX_3D17n	No		N8	DQsn75	DQsn37/CQn37	DQsn18/CQn18	DO9
3D	28	VREFB3D0N	IO	P1L_3D_B_CLKOUT1p,P1L_3D_B_CLKOUT1,P1L_3D_B_F81		HPS_DDR	DIFF_TX_3D17p	No		L8	DQ575	DQ37/CQ37	DQsn18/CQ18	DO9
3D	27	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D18n	Yes		P9	DO75	DQ37	DQ18	DO9
3D	26	VREFB3D0N	IO	RZQ_B_3D_CDR		HPS_DDR	DIFF_RX_3D18p	Yes		M9	DO75	DQ37	DQ18	DO9
3D	25	VREFB3D0N	IO	CLK_B_3D_1n		HPS_DDR	DIFF_TX_3D18n	No		N10	DO75	DQ37	DQ18	DO9
3D	24	VREFB3D0N	IO	CLK_B_3D_1p		HPS_DDR	DIFF_TX_3D18p	No		L10	DO75	DQ37	DQ18	DO9
3D	23	VREFB3D0N	IO	CLK_B_3D_0n,CDR		HPS_DDR	DIFF_RX_3D19n	Yes		W12	DO76	DQ38	DQ19	DO9
3D	22	VREFB3D0N	IO	CLK_B_3D_0p,CDR		HPS_DDR	DIFF_RX_3D19p	Yes		J12	DO76	DQ38	DQ19	DO9
3D	21	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D19n	No		V13	DO76	DQ38	DQ19	DO9
3D	20	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D19p	No		T13	DO76	DQ38	DQ19	DO9
3D	19	VREFB3D0N	IO	P1L_3D_B_CLKOUT0n		HPS_DDR	DIFF_RX_3D20n	No		W14	DQsn76	DQ38	DQ19	DO9
3D	18	VREFB3D0N	IO	P1L_3D_B_CLKOUT0p,P1L_3D_B_CLKOUT0,P1L_3D_B_F80		HPS_DDR	DIFF_RX_3D20p	No		L14	DQ576	DQ38	DQ19	DO9
3D	17	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D20n	No		V15	DQsn77	DQsn38/CQn38	DQsn19/CQn19	DO9
3D	16	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D20p	No		T15	DQ577	DQ38/CQ38	DQ19	DO9
3D	15	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D21n	Yes		W16	DO77	DQ38	DQ19	DO9
3D	14	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D21p	Yes		J16	DO77	DQ38	DQ19	DO9
3D	13	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D21n	No		V17	DO77	DQ38	DQ19	DO9
3D	12	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D21p	No		T17	DO77	DQ38	DQ19	DO9
3D	11	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D22n	Yes		N12	DO78	DQ39	DQ19	DO9
3D	10	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D22p	Yes		L12	DO78	DQ39	DQ19	DO9
3D	9	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D22n	No		P13	DO78	DQ39	DQ19	DO9
3D	8	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D22p	No		M13	DO78	DQ39	DQ19	DO9
3D	7	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D23n	No		N14	DQsn78	DQ39	DQ19	DO9
3D	6	VREFB3D0N	IO			HPS_DDR	DIFF_RX_3D23p	No		L14	DQ578	DQ39	DQ19	DO9
3D	5	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D23n	No		P15	DQsn79	DQsn39/CQn39	DQsn19/CQn19	DO9
3D	4	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D23p	No		M15	DQ579	DQ39/CQ39	DQsn19/CQn19	DO9
3D	3	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D24n	Yes		N16	DO79	DQ39	DQ19	DO9
3D	2	VREFB3D0N	IO	CDR		HPS_DDR	DIFF_RX_3D24p	Yes		L16	DO79	DQ39	DQ19	DO9
3D	1	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D24n	No		P17	DO79	DQ39	DQ19	DO9
3D	0	VREFB3D0N	IO			HPS_DDR	DIFF_TX_3D24p	No		M17	DO79	DQ39	DQ19	DO9
3C	95	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C1n	No		H19	DQ80	DQ40	DQ20	DQ10
3C	94	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C1p	No		F19	DQ80	DQ40	DQ20	DQ10
3C	93	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C1n	No		J20	DQ80	DQ40	DQ20	DQ10
3C	92	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C1p	No		G20	DQ80	DQ40	DQ20	DQ10
3C	91	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C2n	No		H21	DQsn80	DQ40	DQ20	DQ10
3C	90	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C2p	No		F21	DQ580	DQ40	DQ20	DQ10
3C	89	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C2n	No		J22	DQsn81	DQsn40/CQn40	DQ20	DQ10
3C	88	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C2p	No		G22	DQ581	DQsn40/CQn40	DQ20	DQ10
3C	87	VREFB3C0N	IO	CDR		HPS_DDR	DIFF_RX_3C3n	Yes		H23	DQ81	DQ40	DQ20	DQ10
3C	86	VREFB3C0N	IO	CDR		HPS_DDR	DIFF_RX_3C3p	Yes		F23	DQ81	DQ40	DQ20	DQ10
3C	85	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C3n	No		J24	DQ81	DQ40	DQ20	DQ10
3C	84	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C3p	No		G24	DQ81	DQ40	DQ20	DQ10
3C	83	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C4n	No		D19	DQ82	DQ41	DQ20	DQ10
3C	82	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C4p	No		B19	DQ82	DQ41	DQ20	DQ10
3C	81	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C4n	No		C20	DQ82	DQ41	DQ20	DQ10
3C	80	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C4p	No		A20	DQ82	DQ41	DQ20	DQ10
3C	79	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C5n	No		D21	DQsn82	DQ41	DQ20	DQ10
3C	78	VREFB3C0N	IO			HPS_DDR	DIFF_RX_3C5p	No		B21	DQ582	DQ41	DQ20	DQ10
3C	77	VREFB3C0N	IO	P1L_3C_T_CLKOUT1n		HPS_DDR	DIFF_TX_3C5n	No		C22	DQsn83	DQsn41/CQn41	DQsn20/CQn20	DQ10
3C	76	VREFB3C0N	IO	P1L_3C_T_CLKOUT1p,P1L_3C_T_CLKOUT1,P1L_3C_T_F81		HPS_DDR	DIFF_TX_3C5p	No		A22	DQ583	DQsn41/CQn41	DQsn20/CQn20	DQ10
3C	75	VREFB3C0N	IO	CDR		HPS_DDR	DIFF_RX_3C6n	Yes		D23	DQ83	DQ41	DQ20	DQ10
3C	74	VREFB3C0N	IO	RZQ_T_3C_CDR		HPS_DDR	DIFF_RX_3C6p	Yes		B23	DQ83	DQ41	DQ20	DQ10
3C	73	VREFB3C0N	IO	CLK_T_3C_1n		HPS_DDR	DIFF_TX_3C6n	No		C24	DQ83	DQ41	DQ20	DQ10
3C	72	VREFB3C0N	IO	CLK_T_3C_1p		HPS_DDR	DIFF_TX_3C6p	No		A24	DQ83	DQ41	DQ20	DQ10
3C	71	VREFB3C0N	IO	CLK_T_3C_0n		HPS_DDR	DIFF_RX_3C7n	No		J26	DQ84	DQ42	DQ21	DQ10
3C	70	VREFB3C0N	IO	CLK_T_3C_0p		HPS_DDR	DIFF_RX_3C7p	No		G26	DQ84	DQ42	DQ21	DQ10
3C	69	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C7n	No		H27	DQ84	DQ42	DQ21	DQ10
3C	68	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C7p	No		F27	DQ84	DQ42	DQ21	DQ10
3C	67	VREFB3C0N	IO	P1L_3C_T_CLKOUT0n		HPS_DDR	DIFF_RX_3C8n	No		J28	DQsn84	DQ42	DQ21	DQ10
3C	66	VREFB3C0N	IO	P1L_3C_T_CLKOUT0p,P1L_3C_T_CLKOUT0,P1L_3C_T_F80		HPS_DDR	DIFF_RX_3C8p	No		G28	DQ584	DQ42	DQ21	DQ10
3C	65	VREFB3C0N	IO			HPS_DDR	DIFF_TX_3C8n	No		H29	DQsn85	DQsn42/CQn42	DQ21	DQsn10/CQn10

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
3C	64	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C8p	No		F29	D0S85	D0S42/C042	D021	D0S10/C010
3C	63	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C9n	Yes		J30	D0S85	D042	D021	D010
3C	62	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C9p	Yes		G30	D0S85	D042	D021	D010
3C	61	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C9n	No		H31	D0S85	D042	D021	D010
3C	60	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C9p	No		F31	D0S85	D042	D021	D010
3C	59	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C10n	No		C26	D0S86	D043	D021	D010
3C	58	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C10p	No		A26	D0S86	D043	D021	D010
3C	57	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C10n	No		D27	D0S86	D043	D021	D010
3C	56	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C10p	No		E27	D0S86	D043	D021	D010
3C	55	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C11n	No		C28	D0S86	D043	D021	D010
3C	54	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C11p	No		A28	D0S86	D043	D021	D010
3C	53	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C11n	No		D29	D0S87	D0S43/CQn43	D0S21/CQn21	D010
3C	52	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C11p	No		E29	D0S87	D0S43/CQ43	D0S21/CQ21	D010
3C	51	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C12n	Yes		C30	D0S87	D043	D021	D010
3C	50	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C12p	Yes		A30	D0S87	D043	D021	D010
3C	49	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C12n	No		D31	D0S87	D043	D021	D010
3C	48	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C12p	No		E31	D0S87	D043	D021	D010
3C	47	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C13n	Yes		V19	D0S88	D044	D022	D011
3C	46	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C13p	Yes		T19	D0S88	D044	D022	D011
3C	45	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C13n	No		W20	D0S88	D044	D022	D011
3C	44	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C13p	No		U20	D0S88	D044	D022	D011
3C	43	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C14n	No		V21	D0S88	D044	D022	D011
3C	42	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C14p	No		T21	D0S88	D044	D022	D011
3C	41	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C14n	No		W22	D0S89	D0S44/CQn44	D022	D011
3C	40	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C14p	No		U22	D0S89	D0S44/CQ44	D022	D011
3C	39	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C15n	Yes		V23	D0S89	D044	D022	D011
3C	38	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C15p	Yes		T23	D0S89	D044	D022	D011
3C	37	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C15n	No		W24	D0S89	D044	D022	D011
3C	36	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C15p	No		U24	D0S89	D044	D022	D011
3C	35	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C16n	Yes		P19	D0S90	D045	D022	D011
3C	34	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C16p	Yes		M19	D0S90	D045	D022	D011
3C	33	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C16n	No		N20	D0S90	D045	D022	D011
3C	32	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C16p	No		L20	D0S90	D045	D022	D011
3C	31	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C17n	No		P21	D0S90	D045	D022	D011
3C	30	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C17p	No		M21	D0S90	D045	D022	D011
3C	29	VREFB3CNO	IO	PLL_3C_B_CLKOUT1n		HPS_DDR	DIFF_TX_3C17n	No		N22	D0S91	D0S45/CQn45	D0S22/CQn22	D011
3C	28	VREFB3CNO	IO	PLL_3C_B_CLKOUT1p,PLL_3C_B_CLKOUT1,PLL_3C_B_FB1		HPS_DDR	DIFF_TX_3C17p	No		L22	D0S91	D0S45/C045	D0S22/C022	D011
3C	27	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C18n	Yes		P23	D0S91	D045	D022	D011
3C	26	VREFB3CNO	IO	R2Q_B_3C_CDR		HPS_DDR	DIFF_RX_3C18p	Yes		M23	D0S91	D045	D022	D011
3C	25	VREFB3CNO	IO	CLK_B_3C_1n		HPS_DDR	DIFF_TX_3C18n	No		N24	D0S91	D045	D022	D011
3C	24	VREFB3CNO	IO	CLK_B_3C_1p		HPS_DDR	DIFF_TX_3C18p	No		L24	D0S91	D045	D022	D011
3C	23	VREFB3CNO	IO	CLK_B_3C_0n,CDR		HPS_DDR	DIFF_RX_3C19n	Yes		W26	D0S92	D046	D023	D011
3C	22	VREFB3CNO	IO	CLK_B_3C_0p,CDR		HPS_DDR	DIFF_RX_3C19p	Yes		U26	D0S92	D046	D023	D011
3C	21	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C19n	No		V27	D0S92	D046	D023	D011
3C	20	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C19p	No		T27	D0S92	D046	D023	D011
3C	19	VREFB3CNO	IO	PLL_3C_B_CLKOUT0n		HPS_DDR	DIFF_RX_3C20n	No		W28	D0S92	D046	D023	D011
3C	18	VREFB3CNO	IO	PLL_3C_B_CLKOUT0p,PLL_3C_B_CLKOUT0,PLL_3C_B_FB0		HPS_DDR	DIFF_RX_3C20p	No		U28	D0S92	D046	D023	D011
3C	17	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C20n	No		V29	D0S93	D0S46/CQn46	D0S11/CQn11	
3C	16	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C20p	No		T29	D0S93	D0S46/C046	D0S11/C011	
3C	15	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C21n	Yes		W30	D0S93	D046	D023	D011
3C	14	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C21p	Yes		J30	D0S93	D046	D023	D011
3C	13	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C21n	No		V31	D0S93	D046	D023	D011
3C	12	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C21p	No		T31	D0S93	D046	D023	D011
3C	11	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C22n	Yes		N26	D0S94	D047	D023	D011
3C	10	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C22p	Yes		L26	D0S94	D047	D023	D011
3C	9	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C22n	No		P27	D0S94	D047	D023	D011
3C	8	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C22p	No		M27	D0S94	D047	D023	D011
3C	7	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C23n	No		N28	D0S94	D047	D023	D011
3C	6	VREFB3CNO	IO			HPS_DDR	DIFF_RX_3C23p	No		L28	D0S94	D047	D023	D011
3C	5	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C23n	No		P29	D0S95	D0S47/CQn47	D0S23/CQn23	D011
3C	4	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C23p	No		M29	D0S95	D0S47/C047	D0S23/C023	D011
3C	3	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C24n	Yes		N30	D0S95	D047	D023	D011
3C	2	VREFB3CNO	IO	CDR		HPS_DDR	DIFF_RX_3C24p	Yes		L30	D0S95	D047	D023	D011
3C	1	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C24n	No		P31	D0S95	D047	D023	D011
3C	0	VREFB3CNO	IO			HPS_DDR	DIFF_TX_3C24p	No		M31	D0S95	D047	D023	D011
3B	95	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B1n	No		H45	D0S96	D048	D024	D012
3B	94	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B1p	No		F45	D0S96	D048	D024	D012
3B	93	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B1n	No		J44	D0S96	D048	D024	D012
3B	92	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B1p	No		G44	D0S96	D048	D024	D012
3B	91	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B2n	No		H43	D0S96	D048	D024	D012
3B	90	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B2p	No		F43	D0S96	D048	D024	D012
3B	89	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B2n	No		J42	D0S97	D0S48/CQn48	D024	D012
3B	88	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B2p	No		G42	D0S97	D0S48/C048	D024	D012
3B	87	VREFB3BNO	IO	CDR		HPS_DDR	DIFF_RX_3B3n	Yes		H41	D0S97	D048	D024	D012
3B	86	VREFB3BNO	IO	CDR		HPS_DDR	DIFF_RX_3B3p	Yes		F41	D0S97	D048	D024	D012
3B	85	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B3n	No		J40	D0S97	D048	D024	D012
3B	84	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B3p	No		G40	D0S97	D048	D024	D012
3B	83	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B4n	No		D45	D0S98	D049	D024	D012
3B	82	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B4p	No		B45	D0S98	D049	D024	D012
3B	81	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B4n	No		C44	D0S98	D049	D024	D012
3B	80	VREFB3BNO	IO			HPS_DDR	DIFF_TX_3B4p	No		A44	D0S98	D049	D024	D012
3B	79	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B5n	No		D43	D0S98	D049	D024	D012
3B	78	VREFB3BNO	IO			HPS_DDR	DIFF_RX_3B5p	No		B43	D0S98	D049	D024	D012
3B	77	VREFB3BNO	IO	PLL_3B_T_CLKOUT1n		HPS_DDR	DIFF_TX_3B5n	No		C42	D0S99	D0S49/CQn49	D0S24/CQn24	D012
3B	76	VREFB3BNO	IO	PLL_3B_T_CLKOUT1p,PLL_3B_T_CLKOUT1,PLL_3B_T_FB1		HPS_DDR	DIFF_TX_3B5p	No		A42	D0S99	D0S49/C049	D0S24/C024	D012
3B	75	VREFB3BNO	IO	CDR		HPS_DDR	DIFF_RX_3B6n	Yes		D41	D0S99	D049	D024	D012
3B	74	VREFB3BNO	IO	R2Q_T_3B_CDR		HPS_DDR	DIFF_RX_3B6p	Yes		B41	D0S99	D049	D024	D012
3B	73	VREFB3BNO	IO	CLK_T_3B_1n		HPS_DDR	DIFF_TX_3B6n	No		C40	D0S99	D049	D024	D012
3B	72	VREFB3BNO	IO	CLK_T_3B_1p		HPS_DDR	DIFF_TX_3B6p	No		A40	D0S99	D049	D024	D012
3B	71	VREFB3BNO	IO	CLK_T_3B_0n		HPS_DDR	DIFF_RX_3B7n	No		J38	D0100	D050	D025	D012

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
3B		70	VREFB380	IO	CLK_T_38_0p		DIFF_RX_387p	No		G38	DQ100	DQ50	DQ25	DQ12
3B		69	VREFB380	IO			DIFF_TX_387n	No		H37	DQ100	DQ50	DQ25	DQ12
3B		68	VREFB380	IO			DIFF_TX_387p	No		F37	DQ100	DQ50	DQ25	DQ12
3B		67	VREFB380	IO	PLL_3B_T_CLKOUT0n		DIFF_RX_388n	No		J36	DQsn100	DQ50	DQ25	DQ12
3B		66	VREFB380	IO	PLL_3B_T_CLKOUT0p,PLL_3B_T_CLKOUT0n,PLL_3B_T_FB0		DIFF_RX_388p	No		G36	DQ5100	DQ50	DQ25	DQ12
3B		65	VREFB380	IO			DIFF_TX_388n	No		H35	DQsn101	DQsn50/CQn50	DQ25	DQsn12/CQn12
3B		64	VREFB380	IO			DIFF_TX_388p	No		F35	DQ5101	DQ50/CQ50	DQ25	DQ512/CQ12
3B		63	VREFB380	IO	CDR		DIFF_RX_389n	Yes		J34	DQ101	DQ50	DQ25	DQ12
3B		62	VREFB380	IO	CDR		DIFF_RX_389p	Yes		G34	DQ101	DQ50	DQ25	DQ12
3B		61	VREFB380	IO			DIFF_TX_389n	No		H33	DQ101	DQ50	DQ25	DQ12
3B		60	VREFB380	IO			DIFF_TX_389p	No		F33	DQ101	DQ50	DQ25	DQ12
3B		59	VREFB380	IO			DIFF_RX_3810n	No		C38	DQ102	DQ51	DQ25	DQ12
3B		58	VREFB380	IO			DIFF_RX_3810p	No		A38	DQ102	DQ51	DQ25	DQ12
3B		57	VREFB380	IO			DIFF_TX_3810n	No		D37	DQ102	DQ51	DQ25	DQ12
3B		56	VREFB380	IO			DIFF_TX_3810p	No		B37	DQ102	DQ51	DQ25	DQ12
3B		55	VREFB380	IO			DIFF_RX_3811n	No		C36	DQsn102	DQ51	DQ25	DQ12
3B		54	VREFB380	IO			DIFF_RX_3811p	No		A36	DQ5102	DQ51	DQ25	DQ12
3B		53	VREFB380	IO			DIFF_TX_3811n	No		D35	DQsn103	DQsn51/CQn51	DQsn25/CQn25	DQ12
3B		52	VREFB380	IO			DIFF_TX_3811p	No		B35	DQ5103	DQ51/CQ51	DQ525/CQ25	DQ12
3B		51	VREFB380	IO	CDR		DIFF_RX_3812n	Yes		C34	DQ103	DQ51	DQ25	DQ12
3B		50	VREFB380	IO	CDR		DIFF_RX_3812p	Yes		A34	DQ103	DQ51	DQ25	DQ12
3B		49	VREFB380	IO			DIFF_TX_3812n	No		D33	DQ103	DQ51	DQ25	DQ12
3B		48	VREFB380	IO			DIFF_TX_3812p	No		B33	DQ103	DQ51	DQ25	DQ12
3B		47	VREFB380	IO	CDR		DIFF_RX_3813n	Yes		V45	DQ104	DQ52	DQ26	DQ13
3B		46	VREFB380	IO	CDR		DIFF_RX_3813p	Yes		T45	DQ104	DQ52	DQ26	DQ13
3B		45	VREFB380	IO			DIFF_TX_3813n	No		W44	DQ104	DQ52	DQ26	DQ13
3B		44	VREFB380	IO			DIFF_TX_3813p	No		J44	DQ104	DQ52	DQ26	DQ13
3B		43	VREFB380	IO			DIFF_RX_3814n	No		V43	DQsn104	DQ52	DQ26	DQ13
3B		42	VREFB380	IO			DIFF_RX_3814p	No		T43	DQ5104	DQ52	DQ26	DQ13
3B		41	VREFB380	IO			DIFF_TX_3814n	No		W42	DQsn105	DQsn52/CQn52	DQ26	DQ13
3B		40	VREFB380	IO			DIFF_TX_3814p	No		J42	DQsn105	DQ52/CQ52	DQ26	DQ13
3B		39	VREFB380	IO	CDR		DIFF_RX_3815n	Yes		V41	DQ105	DQ52	DQ26	DQ13
3B		38	VREFB380	IO	CDR		DIFF_RX_3815p	Yes		T41	DQ105	DQ52	DQ26	DQ13
3B		37	VREFB380	IO			DIFF_TX_3815n	No		W40	DQ105	DQ52	DQ26	DQ13
3B		36	VREFB380	IO			DIFF_TX_3815p	No		J40	DQ105	DQ52	DQ26	DQ13
3B		35	VREFB380	IO	CDR		DIFF_RX_3816n	Yes		M45	DQ106	DQ53	DQ26	DQ13
3B		34	VREFB380	IO	CDR		DIFF_RX_3816p	Yes		M45	DQ106	DQ53	DQ26	DQ13
3B		33	VREFB380	IO			DIFF_TX_3816n	No		N44	DQ106	DQ53	DQ26	DQ13
3B		32	VREFB380	IO			DIFF_TX_3816p	No		L44	DQ106	DQ53	DQ26	DQ13
3B		31	VREFB380	IO			DIFF_RX_3817n	No		P43	DQsn106	DQ53	DQ26	DQ13
3B		30	VREFB380	IO			DIFF_RX_3817p	No		M43	DQ5106	DQ53	DQ26	DQ13
3B		29	VREFB380	IO	PLL_3B_B_CLKOUT1n		DIFF_TX_3817n	No		N42	DQsn107	DQsn53/CQn53	DQsn26/CQn26	DQ13
3B		28	VREFB380	IO	PLL_3B_B_CLKOUT1p,PLL_3B_B_CLKOUT1,PLL_3B_B_FB1		DIFF_TX_3817p	No		L42	DQ5107	DQ53/CQ53	DQ526/CQ26	DQ13
3B		27	VREFB380	IO	CDR		DIFF_RX_3818n	Yes		M41	DQ107	DQ53	DQ26	DQ13
3B		26	VREFB380	IO	R2Q_B_38_CDR		DIFF_RX_3818p	Yes		M41	DQ107	DQ53	DQ26	DQ13
3B		25	VREFB380	IO	CLK_B_38_1n		DIFF_TX_3818n	No		N40	DQ107	DQ53	DQ26	DQ13
3B		24	VREFB380	IO	CLK_B_38_1p		DIFF_TX_3818p	No		L40	DQ107	DQ53	DQ26	DQ13
3B		23	VREFB380	IO	CLK_B_38_0n,CDR		DIFF_RX_3819n	Yes		W38	DQ108	DQ54	DQ27	DQ13
3B		22	VREFB380	IO	CLK_B_38_0p,CDR		DIFF_RX_3819p	Yes		J38	DQ108	DQ54	DQ27	DQ13
3B		21	VREFB380	IO			DIFF_TX_3819n	No		V37	DQ108	DQ54	DQ27	DQ13
3B		20	VREFB380	IO			DIFF_TX_3819p	No		T37	DQ108	DQ54	DQ27	DQ13
3B		19	VREFB380	IO	PLL_3B_B_CLKOUT0n		DIFF_RX_3820n	No		W36	DQsn108	DQ54	DQ27	DQ13
3B		18	VREFB380	IO	PLL_3B_B_CLKOUT0p,PLL_3B_B_CLKOUT0,PLL_3B_B_FB0		DIFF_RX_3820p	No		J36	DQ5108	DQ54	DQ27	DQ13
3B		17	VREFB380	IO			DIFF_TX_3820n	No		V35	DQsn109	DQsn54/CQn54	DQ27	DQsn13/CQn13
3B		16	VREFB380	IO			DIFF_TX_3820p	No		T35	DQ5109	DQ54/CQ54	DQ27	DQ513/CQ13
3B		15	VREFB380	IO	CDR		DIFF_RX_3821n	Yes		W34	DQ109	DQ54	DQ27	DQ13
3B		14	VREFB380	IO	CDR		DIFF_RX_3821p	Yes		J34	DQ109	DQ54	DQ27	DQ13
3B		13	VREFB380	IO			DIFF_TX_3821n	No		V33	DQ109	DQ54	DQ27	DQ13
3B		12	VREFB380	IO			DIFF_TX_3821p	No		T33	DQ109	DQ54	DQ27	DQ13
3B		11	VREFB380	IO	CDR		DIFF_RX_3822n	Yes		N38	DQ110	DQ55	DQ27	DQ13
3B		10	VREFB380	IO	CDR		DIFF_RX_3822p	Yes		L38	DQ110	DQ55	DQ27	DQ13
3B		9	VREFB380	IO			DIFF_TX_3822n	No		P37	DQ110	DQ55	DQ27	DQ13
3B		8	VREFB380	IO			DIFF_TX_3822p	No		M37	DQ110	DQ55	DQ27	DQ13
3B		7	VREFB380	IO			DIFF_RX_3823n	No		N36	DQsn110	DQ55	DQ27	DQ13
3B		6	VREFB380	IO			DIFF_RX_3823p	No		L36	DQ5110	DQ55	DQ27	DQ13
3B		5	VREFB380	IO			DIFF_TX_3823n	No		P35	DQsn111	DQsn55/CQn55	DQsn27/CQn27	DQ13
3B		4	VREFB380	IO			DIFF_TX_3823p	No		M35	DQ5111	DQ55/CQ55	DQ527/CQ27	DQ13
3B		3	VREFB380	IO	CDR		DIFF_RX_3824n	Yes		N34	DQ111	DQ55	DQ27	DQ13
3B		2	VREFB380	IO	CDR		DIFF_RX_3824p	Yes		L34	DQ111	DQ55	DQ27	DQ13
3B		1	VREFB380	IO			DIFF_TX_3824n	No		P33	DQ111	DQ55	DQ27	DQ13
3B		0	VREFB380	IO			DIFF_TX_3824p	No		M33	DQ111	DQ55	DQ27	DQ13
3A		95	VREFB380	IO			DIFF_RX_3A1n	No		H59	DQ112	DQ56	DQ28	DQ14
3A		94	VREFB380	IO			DIFF_RX_3A1p	No		F59	DQ112	DQ56	DQ28	DQ14
3A		93	VREFB380	IO			DIFF_TX_3A1n	No		J58	DQ112	DQ56	DQ28	DQ14
3A		92	VREFB380	IO			DIFF_TX_3A1p	No		G58	DQ112	DQ56	DQ28	DQ14
3A		91	VREFB380	IO			DIFF_RX_3A2n	No		H57	DQsn112	DQ56	DQ28	DQ14
3A		90	VREFB380	IO		AVST_READY	DIFF_RX_3A2p	No		F57	DQ5112	DQ56	DQ28	DQ14
3A		89	VREFB380	IO		AVST_DATA31	DIFF_TX_3A2n	No		J56	DQsn113	DQsn56/CQn56	DQ28	DQ14
3A		88	VREFB380	IO		AVST_DATA30	DIFF_TX_3A2p	No		G56	DQ5113	DQ56/CQ56	DQ28	DQ14
3A		87	VREFB380	IO	CDR		DIFF_RX_3A3n	Yes		H55	DQ113	DQ56	DQ28	DQ14
3A		86	VREFB380	IO	CDR		DIFF_RX_3A3p	Yes		F55	DQ113	DQ56	DQ28	DQ14
3A		85	VREFB380	IO		AVST_DATA27	DIFF_TX_3A3n	No		J54	DQ113	DQ56	DQ28	DQ14
3A		84	VREFB380	IO		AVST_DATA26	DIFF_TX_3A3p	No		G54	DQ113	DQ56	DQ28	DQ14
3A		83	VREFB380	IO		AVST_DATA25	DIFF_RX_3A4n	No		H61	DQ114	DQ57	DQ28	DQ14
3A		82	VREFB380	IO		AVST_DATA24	DIFF_RX_3A4p	No		F61	DQ114	DQ57	DQ28	DQ14
3A		81	VREFB380	IO		AVST_DATA23	DIFF_TX_3A4n	No		D59	DQ114	DQ57	DQ28	DQ14
3A		80	VREFB380	IO		AVST_DATA22	DIFF_TX_3A4p	No		C58	DQ114	DQ57	DQ28	DQ14
3A		79	VREFB380	IO		AVST_DATA21	DIFF_RX_3A5n	No		D57	DQsn114	DQ57	DQ28	DQ14
3A		78	VREFB380	IO		AVST_DATA20	DIFF_RX_3A5p	No		B57	DQ5114	DQ57	DQ28	DQ14
3A		77	VREFB380	IO	PLL_3A_T_CLKOUT1n		DIFF_TX_3A5n	No		C56	DQsn115	DQsn57/CQn57	DQsn28/CQn28	DQ14

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
3A	76	VREFB3ANO	IO	PLL_3A_T_CLKOUT1p,PLL_3A_T_CLKOUT1,PLL_3A_T_FB1	AVST_DATA18	IO	DIFF_TX_3A5p	No		A56	DQ5115	DQ557/CQ57	DQ528/CQ28	DQ14
3A	75	VREFB3ANO	IO		AVST_DATA17	IO	DIFF_RX_3A6n	Yes		D55	DQ115	DQ57	DQ28	DQ14
3A	74	VREFB3ANO	IO	RZQ_T_3A,CDR	AVST_DATA16	IO	DIFF_RX_3A6p	Yes		B55	DQ115	DQ57	DQ28	DQ14
3A	73	VREFB3ANO	IO	CLK_T_3A_1n		IO	DIFF_TX_3A6n	No		C54	DQ115	DQ57	DQ28	DQ14
3A	72	VREFB3ANO	IO	CLK_T_3A_1p		IO	DIFF_TX_3A6p	No		A54	DQ115	DQ57	DQ28	DQ14
3A	71	VREFB3ANO	IO	CLK_T_3A_0n		IO	DIFF_RX_3A7n	No		J52	DQ116	DQ58	DQ29	DQ14
3A	70	VREFB3ANO	IO	CLK_T_3A_0p		IO	DIFF_RX_3A7p	No		G52	DQ116	DQ58	DQ29	DQ14
3A	69	VREFB3ANO	IO			IO	DIFF_TX_3A7n	No		H51	DQ116	DQ58	DQ29	DQ14
3A	68	VREFB3ANO	IO			IO	DIFF_TX_3A7p	No		F51	DQ116	DQ58	DQ29	DQ14
3A	67	VREFB3ANO	IO	PLL_3A_T_CLKOUT0n		IO	DIFF_RX_3A8n	No		J50	DQ5n116	DQ58	DQ29	DQ14
3A	66	VREFB3ANO	IO	PLL_3A_T_CLKOUT0p,PLL_3A_T_CLKOUT0,PLL_3A_T_FB0		IO	DIFF_RX_3A8p	No		G50	DQ5n116	DQ58	DQ29	DQ14
3A	65	VREFB3ANO	IO		AVST_CLK	IO	DIFF_TX_3A8n	No		H49	DQ5n117	DQ5n58/CQn58	DQ29	DQ5n14/CQn14
3A	64	VREFB3ANO	IO		AVST_DATA15	IO	DIFF_TX_3A8p	No		F49	DQ5117	DQ558/CQ58	DQ29	DQ514/CQ14
3A	63	VREFB3ANO	IO	CDR	AVST_DATA14	IO	DIFF_RX_3A9n	Yes		H48	DQ117	DQ58	DQ29	DQ14
3A	62	VREFB3ANO	IO	CDR	AVST_DATA13	IO	DIFF_RX_3A9p	Yes		G48	DQ117	DQ58	DQ29	DQ14
3A	61	VREFB3ANO	IO		AVST_DATA12	IO	DIFF_TX_3A9n	No		H47	DQ117	DQ58	DQ29	DQ14
3A	60	VREFB3ANO	IO		AVST_DATA11	IO	DIFF_TX_3A9p	No		F47	DQ117	DQ58	DQ29	DQ14
3A	59	VREFB3ANO	IO		AVST_DATA10	IO	DIFF_RX_3A10n	No		C52	DQ118	DQ59	DQ29	DQ14
3A	58	VREFB3ANO	IO		AVST_DATA9	IO	DIFF_RX_3A10p	No		A52	DQ118	DQ59	DQ29	DQ14
3A	57	VREFB3ANO	IO		AVST_DATA8	IO	DIFF_TX_3A10n	No		D51	DQ118	DQ59	DQ29	DQ14
3A	56	VREFB3ANO	IO		AVST_VALID	IO	DIFF_TX_3A10p	No		B51	DQ118	DQ59	DQ29	DQ14
3A	55	VREFB3ANO	IO		AVST_DATA7	IO	DIFF_RX_3A11n	No		C50	DQ5n118	DQ59	DQ29	DQ14
3A	54	VREFB3ANO	IO		AVST_DATA6	IO	DIFF_RX_3A11p	No		A50	DQ5n118	DQ59	DQ29	DQ14
3A	53	VREFB3ANO	IO		AVST_DATA5	IO	DIFF_TX_3A11n	No		D49	DQ5n119	DQ5n59/CQn59	DQ29	DQ5n14/CQn14
3A	52	VREFB3ANO	IO		AVST_DATA4	IO	DIFF_TX_3A11p	No		B49	DQ5n119	DQ559/CQ59	DQ29	DQ5n14/CQn14
3A	51	VREFB3ANO	IO	CDR	AVST_DATA3	IO	DIFF_RX_3A12n	Yes		C48	DQ119	DQ59	DQ29	DQ14
3A	50	VREFB3ANO	IO	CDR	AVST_DATA2	IO	DIFF_RX_3A12p	Yes		A48	DQ119	DQ59	DQ29	DQ14
3A	49	VREFB3ANO	IO		AVST_DATA1	IO	DIFF_TX_3A12n	No		D47	DQ119	DQ59	DQ29	DQ14
3A	48	VREFB3ANO	IO		AVST_DATA0	IO	DIFF_TX_3A12p	No		B47	DQ119	DQ59	DQ29	DQ14
3A	47	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A13n	Yes		V59	DQ120	DQ60	DQ30	DQ15
3A	46	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A13p	Yes		T59	DQ120	DQ60	DQ30	DQ15
3A	45	VREFB3ANO	IO			IO	DIFF_TX_3A13n	No		W58	DQ120	DQ60	DQ30	DQ15
3A	44	VREFB3ANO	IO			IO	DIFF_TX_3A13p	No		U58	DQ120	DQ60	DQ30	DQ15
3A	43	VREFB3ANO	IO			IO	DIFF_RX_3A14n	No		V57	DQ5n120	DQ60	DQ30	DQ15
3A	42	VREFB3ANO	IO			IO	DIFF_RX_3A14p	No		T57	DQ5n120	DQ60	DQ30	DQ15
3A	41	VREFB3ANO	IO			IO	DIFF_TX_3A14n	No		W56	DQ5n121	DQ5n60/CQn60	DQ30	DQ15
3A	40	VREFB3ANO	IO			IO	DIFF_TX_3A14p	No		U56	DQ5n121	DQ5n60/CQn60	DQ30	DQ15
3A	39	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A15n	Yes		V55	DQ121	DQ60	DQ30	DQ15
3A	38	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A15p	Yes		T55	DQ121	DQ60	DQ30	DQ15
3A	37	VREFB3ANO	IO			IO	DIFF_TX_3A15n	No		W54	DQ121	DQ60	DQ30	DQ15
3A	36	VREFB3ANO	IO			IO	DIFF_TX_3A15p	No		U54	DQ121	DQ60	DQ30	DQ15
3A	35	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A16n	Yes		P59	DQ122	DQ61	DQ30	DQ15
3A	34	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A16p	Yes		M59	DQ122	DQ61	DQ30	DQ15
3A	33	VREFB3ANO	IO			IO	DIFF_TX_3A16n	No		N58	DQ122	DQ61	DQ30	DQ15
3A	32	VREFB3ANO	IO			IO	DIFF_TX_3A16p	No		L58	DQ122	DQ61	DQ30	DQ15
3A	31	VREFB3ANO	IO			IO	DIFF_RX_3A17n	No		P57	DQ5n122	DQ61	DQ30	DQ15
3A	30	VREFB3ANO	IO			IO	DIFF_RX_3A17p	No		M57	DQ5n122	DQ61	DQ30	DQ15
3A	29	VREFB3ANO	IO	PLL_3A_B_CLKOUT1n		IO	DIFF_TX_3A17n	No		N56	DQ5n123	DQ5n61/CQn61	DQ5n30/CQn30	DQ15
3A	28	VREFB3ANO	IO	PLL_3A_B_CLKOUT1p,PLL_3A_B_CLKOUT1,PLL_3A_B_FB1		IO	DIFF_TX_3A17p	No		L56	DQ5n123	DQ551/CQ61	DQ530/CQ30	DQ15
3A	27	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A18n	Yes		P55	DQ123	DQ61	DQ30	DQ15
3A	26	VREFB3ANO	IO	RZQ_B_3A,CDR		IO	DIFF_RX_3A18p	Yes		M55	DQ123	DQ61	DQ30	DQ15
3A	25	VREFB3ANO	IO	CLK_B_3A_1n		IO	DIFF_TX_3A18n	No		N54	DQ123	DQ61	DQ30	DQ15
3A	24	VREFB3ANO	IO	CLK_B_3A_1p		IO	DIFF_TX_3A18p	No		L54	DQ123	DQ61	DQ30	DQ15
3A	23	VREFB3ANO	IO	CLK_B_3A_0n,CDR		IO	DIFF_RX_3A19n	Yes		W52	DQ124	DQ62	DQ31	DQ15
3A	22	VREFB3ANO	IO	CLK_B_3A_0p,CDR		IO	DIFF_RX_3A19p	Yes		U52	DQ124	DQ62	DQ31	DQ15
3A	21	VREFB3ANO	IO			IO	DIFF_TX_3A19n	No		V51	DQ124	DQ62	DQ31	DQ15
3A	20	VREFB3ANO	IO			IO	DIFF_TX_3A19p	No		T51	DQ124	DQ62	DQ31	DQ15
3A	19	VREFB3ANO	IO	PLL_3A_B_CLKOUT0n		IO	DIFF_RX_3A20n	No		W50	DQ5n124	DQ62	DQ31	DQ15
3A	18	VREFB3ANO	IO	PLL_3A_B_CLKOUT0p,PLL_3A_B_CLKOUT0,PLL_3A_B_FB0		IO	DIFF_RX_3A20p	No		U50	DQ5n124	DQ62	DQ31	DQ15
3A	17	VREFB3ANO	IO			IO	DIFF_TX_3A20n	No		V49	DQ5n125	DQ5n62/CQn62	DQ31	DQ5n15/CQn15
3A	16	VREFB3ANO	IO			IO	DIFF_TX_3A20p	No		T49	DQ5n125	DQ562/CQ62	DQ31	DQ5n15/CQ15
3A	15	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A21n	Yes		W48	DQ125	DQ62	DQ31	DQ15
3A	14	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A21p	Yes		U48	DQ125	DQ62	DQ31	DQ15
3A	13	VREFB3ANO	IO			IO	DIFF_TX_3A21n	No		V47	DQ125	DQ62	DQ31	DQ15
3A	12	VREFB3ANO	IO			IO	DIFF_TX_3A21p	No		T47	DQ125	DQ62	DQ31	DQ15
3A	11	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A22n	Yes		N52	DQ126	DQ63	DQ31	DQ15
3A	10	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A22p	Yes		L52	DQ126	DQ63	DQ31	DQ15
3A	9	VREFB3ANO	IO			IO	DIFF_TX_3A22n	No		P51	DQ126	DQ63	DQ31	DQ15
3A	8	VREFB3ANO	IO			IO	DIFF_TX_3A22p	No		M51	DQ126	DQ63	DQ31	DQ15
3A	7	VREFB3ANO	IO			IO	DIFF_RX_3A23n	No		N50	DQ5n126	DQ63	DQ31	DQ15
3A	6	VREFB3ANO	IO			IO	DIFF_RX_3A23p	No		L50	DQ5n126	DQ63	DQ31	DQ15
3A	5	VREFB3ANO	IO			IO	DIFF_TX_3A23n	No		P49	DQ5n127	DQ5n63/CQn63	DQ5n31/CQn31	DQ15
3A	4	VREFB3ANO	IO			IO	DIFF_TX_3A23p	No		M49	DQ5n127	DQ563/CQ63	DQ531/CQ31	DQ15
3A	3	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A24n	Yes		N48	DQ127	DQ63	DQ31	DQ15
3A	2	VREFB3ANO	IO	CDR		IO	DIFF_RX_3A24p	Yes		L48	DQ127	DQ63	DQ31	DQ15
3A	1	VREFB3ANO	IO			IO	DIFF_TX_3A24n	No		P47	DQ127	DQ63	DQ31	DQ15
3A	0	VREFB3ANO	IO			IO	DIFF_TX_3A24p	No		M47	DQ127	DQ63	DQ31	DQ15
HPS				HPS_IOA_1	GPIO0_I0D,SPIM0_SS1_N,SPIS0_CLK,UART0_CTS_N,NAND_ADOQ0,USB0_CLK,SDMMC_CCLK		HPS_IOA_1			AH5				
HPS				HPS_IOA_2	GPIO0_I0I,SPIM1_SS1_N,SPIS0_MOSI,UART0_RTS_N,NAND_ADOQ1,USB0_STP,SDMMC_CMD		HPS_IOA_2			AD1				
HPS				HPS_IOA_3	GPIO0_I0J,SPIS0_SS0_N,UART0_TX,I2C1_SDA,NAND_WE_N,USB0_DIR,SDMMC_DATA0		HPS_IOA_3			AG6				
HPS				HPS_IOA_4	GPIO0_I0K,SPIS0_MISO,UART0_RX,I2C1_SCL,NAND_RE_N,USB0_DATA0,SDMMC_DATA1		HPS_IOA_4			AB1				
HPS				HPS_IOA_5	GPIO0_I0L,SPIM0_CLK,UART1_CTS_N,I2C0_SDA,NAND_WP_N,USB0_DATA1,SDMMC_DATA2		HPS_IOA_5			AG4				
HPS				HPS_IOA_6	GPIO0_I0M,SPIM0_MOSI,UART1_RTS_N,I2C0_SCL,NAND_ADOQ2,USB0_NXT,SDMMC_DATA3		HPS_IOA_6			AD3				
HPS				HPS_IOA_7	GPIO0_I0N,SPIM0_MISO,MDIO2_MDIO,UART1_RX,I2C2_EMAC2_SDA,NAND_ADOQ3,USB0_DATA2,SDMMC_DATA4		HPS_IOA_7			AF5				
HPS				HPS_IOA_8	GPIO0_I0P,SPIM0_SS0_N,MDIO2_MDC,UART1_RX,I2C2_EMAC2_SCL,NAND_CLE,USB0_DATA3,SDMMC_DATA5		HPS_IOA_8			AC2				
HPS				HPS_IOA_9	GPIO0_I0Q,SPIM1_CLK,SPIS1_CLK,MDIO1_MDIO,I2C2_EMAC1_SDA,NAND_ADOQ4,USB0_DATA4,SDMMC_DATA6		HPS_IOA_9			AF1				
HPS				HPS_IOA_10	GPIO0_I0R,SPIM1_MOSI,SPIS1_MOSI,MDIO1_MDC,I2C2_EMAC1_SCL,NAND_ADOQ5,USB0_DATA5,SDMMC_DATA7		HPS_IOA_10			AB3				
HPS				HPS_IOA_11	GPIO0_I0S,SPIM1_MISO,SPIS1_SS0_N,MDIO0_MDIO,I2C2_EMAC0_SDA,NAND_ADOQ6,USB0_DATA6		HPS_IOA_11			AF3				
HPS				HPS_IOA_12	GPIO0_I0T,SPIM1_SS0_N,SPIS1_MISO,MDIO0_MDC,I2C2_EMAC0_SCL,NAND_ADOQ7,USB0_DATA7		HPS_IOA_12			AA2				
HPS				HPS_IOA_13	GPIO0_I0U,NAND_ALE,USB1_CLK,EAC0_TX_CLK		HPS_IOA_13			AC4				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/Y9	DQS for X16/Y18	DQS for X32/Y36
HPS			HPS_IDA_14	GPIO_ID13,NAND_RB,USB1_STP,EMACO_TX_CTL		HPS_IDA_14				V1				
HPS			HPS_IDA_15	GPIO_ID14,NAND_CE,N_USB1_DIR,EMACO_RX_CLK		HPS_IDA_15				AA4				
HPS			HPS_IDA_16	GPIO_ID15,USB1_DATA0,EMACO_RX_CTL		HPS_IDA_16				T1				
HPS			HPS_IDA_17	GPIO_ID16,NAND_ADOQ8,USB1_DATA1,EMACO_TXD0		HPS_IDA_17				AD5				
HPS			HPS_IDA_18	GPIO_ID17,NAND_ADO9,USB1_NXT,EMACO_TXD1		HPS_IDA_18				P1				
HPS			HPS_IDA_19	GPIO_ID18,NAND_ADO10,USB1_DATA2,EMACO_RXD0		HPS_IDA_19				AF7				
HPS			HPS_IDA_20	GPIO_ID19,SPIM1_SS1,N,NAND_ADO11,USB1_DATA3,EMACO_RXD1		HPS_IDA_20				M1				
HPS			HPS_IDA_21	GPIO_ID20,SPIM1_CLK,SPIS0_CLK,UART0_CTS,N,I2C1_SDA,NAND_ADO12,USB1_DATA4,EMACO_TXD2		HPS_IDA_21				AF9				
HPS			HPS_IDA_22	GPIO_ID21,SPIM1_MISO,SPIS0_MISO,UART0_RTS,N,I2C1_SCL,NAND_ADO13,USB1_DATA5,EMACO_TXD3		HPS_IDA_22				W2				
HPS			HPS_IDA_23	GPIO_ID22,SPIM1_MISO,SPIS0_SSD,N,UART0_RX,I2C0_SDA,NAND_ADO14,USB1_DATA6,EMACO_RXD2		HPS_IDA_23				AB5				
HPS			HPS_IDA_24	GPIO_ID23,SPIM1_SSD,N,SPIS0_MISO,UART0_RX,I2C0_SCL,NAND_ADO15,USB1_DATA7,EMACO_RXD3		HPS_IDA_24				U2				
HPS			HPS_IOB_1	GPIO_ID0,SPIM1_CLK,UART0_CTS,N,NAND_ADO0,EMAC1_TX_CLK		HPS_IOB_1				AC6				
HPS			HPS_IOB_2	GPIO_ID1,SPIM1_MISO,UART0_RTS,N,NAND_ADO1,EMAC1_TX_CTL		HPS_IOB_2				H1				
HPS			HPS_IOB_3	GPIO_ID2,SPIM1_MISO,UART0_TX,I2C0_SDA,NAND_WE,N,EMAC1_RX_CLK		HPS_IOB_3				AA6				
HPS			HPS_IOB_4	GPIO_ID3,SPIM1_SSD,N,UART0_RX,I2C0_SCL,NAND_RE,N,EMAC1_RX_CTL		HPS_IOB_4				F1				
HPS			HPS_IOB_5	GPIO_ID4,SPIM1_SS1,N,SPIS1_CLK,UART1_CTS,N,NAND_WP,N,EMAC1_TXD0		HPS_IOB_5				AD7				
HPS			HPS_IOB_6	GPIO_ID5,SPIS1_MOSI,UART1_RTS,N,NAND_ADO2,EMAC1_TXD1		HPS_IOB_6				N2				
HPS			HPS_IOB_7	GPIO_ID6,SPIS1_SSD,N,UART1_TX,I2C1_SDA,NAND_ADO3,EMAC1_RXD0		HPS_IOB_7				AB7				
HPS			HPS_IOB_8	GPIO_ID7,SPIS1_MISO,UART1_RX,I2C1_SCL,NAND_CLE,EMAC1_RXD1		HPS_IOB_8				L2				
HPS			HPS_IOB_9	GPIO_ID8,JTAG_TCK,SPIS0_CLK,MIDIO2_MIDIO,I2C_EMAC2_SDA,NAND_ADO4,EMAC1_TXD2		HPS_IOB_9				AC8				
HPS			HPS_IOB_10	GPIO_ID9,JTAG_TMS,SPIS0_MOSI,MIDIO2_MDC,I2C_EMAC2_SCL,NAND_ADO5,EMAC1_TXD3		HPS_IOB_10				I2				
HPS			HPS_IOB_11	GPIO_ID10,JTAG_TDO,SPIS0_SSD,N,MIDIO_MIDIO,I2C_EMAC2_SDA,NAND_ADO6,EMAC1_RXD2		HPS_IOB_11				AA8				
HPS			HPS_IOB_12	GPIO_ID11,JTAG_TDI,SPIS0_MISO,MIDIO_MDC,I2C_EMAC2_SCL,NAND_ADO7,EMAC1_RXD3		HPS_IOB_12				G2				
HPS			HPS_IOB_13	GPIO_ID12,I2C1_SDA,NAND_ALE,SDMMC_DATA0,EMAC2_TX_CLK		HPS_IOB_13				AD9				
HPS			HPS_IOB_14	GPIO_ID13,I2C1_SCL,NAND_RB,SDMMC_CMD,EMAC2_TX_CTL		HPS_IOB_14				V3				
HPS			HPS_IOB_15	GPIO_ID14,UART1_TX,NAND_CE,N,SDMMC_CLE,EMAC2_RX_CLK		HPS_IOB_15				AB9				
HPS			HPS_IOB_16	GPIO_ID15,UART1_RX,SDMMC_DATA1,EMAC2_RX_CTL		HPS_IOB_16				T3				
HPS			HPS_IOB_17	GPIO_ID16,UART1_CTS,N,NAND_ADO8,SDMMC_DATA2,EMAC2_TXD0		HPS_IOB_17				AC10				
HPS			HPS_IOB_18	GPIO_ID17,SPIM0_SS1,N,UART1_RTS,N,NAND_ADO9,SDMMC_DATA3,EMAC2_TXD1		HPS_IOB_18				P3				
HPS			HPS_IOB_19	GPIO_ID18,SPIM0_MISO,MIDIO1_MIDIO,I2C_EMAC1_SDA,NAND_ADO10,SDMMC_DATA4,EMAC2_RXD0		HPS_IOB_19				AD11				
HPS			HPS_IOB_20	GPIO_ID19,SPIM0_SSD,N,MIDIO1_MDC,I2C_EMAC1_SCL,NAND_ADO11,SDMMC_DATA5,EMAC2_RXD1		HPS_IOB_20				M3				
HPS			HPS_IOB_21	GPIO_ID20,SPIM0_CLK,SPIS1_CLK,I2C_EMAC2_SDA,NAND_ADO12,SDMMC_DATA6,EMAC2_TXD2		HPS_IOB_21				AC12				
HPS			HPS_IOB_22	GPIO_ID21,SPIM0_MOSI,SPIS1_MOSI,I2C_EMAC2_SCL,NAND_ADO13,SDMMC_DATA7,EMAC2_TXD3		HPS_IOB_22				H3				
HPS			HPS_IOB_23	GPIO_ID22,SPIM0_MISO,SPIS1_SSD,N,MIDIO1_MIDIO,I2C_EMAC2_SDA,NAND_ADO14,EMAC2_RXD2		HPS_IOB_23				AD13				
HPS			HPS_IOB_24	GPIO_ID23,SPIM0_SSD,N,SPIS1_MISO,MIDIO1_MDC,I2C_EMAC2_SCL,NAND_ADO15,EMAC2_RXD3		HPS_IOB_24				F3				
9A			GXER9A_TX_CH0p						Yes	AK1				
9A			GXER9A_TX_CH1p						Yes	AL4				
9A			GXER9A_TX_CH2p						Yes	AP1				
9A			GXER9A_TX_CH3p						Yes	AB4				
9A			GXER9A_TX_CH8p						Yes	AV1				
9A			GXER9A_TX_CH9p						Yes	AW4				
9A			GXER9A_TX_CH10p						Yes	BB1				
9A			GXER9A_TX_CH11p						Yes	BC4				
9A			GXER9A_TX_CH12p						Yes	BF1				
9A			GXER9A_TX_CH13p						Yes	BG4				
9A			GXER9A_TX_CH14p						Yes	BK1				
9A			GXER9A_TX_CH15p						Yes	BL4				
9A			GXER9A_TX_CH20p						Yes	BP1				
9A			GXER9A_TX_CH21p						Yes	BR4				
9A			GXER9A_TX_CH22p						Yes	BV1				
9A			GXER9A_TX_CH23p						Yes	BW4				
9A			GXER9A_TX_CH0n						Yes	AJ2				
9A			GXER9A_TX_CH1n						Yes	AM5				
9A			GXER9A_TX_CH2n						Yes	AN2				
9A			GXER9A_TX_CH3n						Yes	AT5				
9A			GXER9A_TX_CH8n						Yes	AU2				
9A			GXER9A_TX_CH9n						Yes	AV5				
9A			GXER9A_TX_CH10n						Yes	SA2				
9A			GXER9A_TX_CH11n						Yes	BD5				
9A			GXER9A_TX_CH12n						Yes	BE2				
9A			GXER9A_TX_CH13n						Yes	BH5				
9A			GXER9A_TX_CH14n						Yes	BJ2				
9A			GXER9A_TX_CH15n						Yes	BM5				
9A			GXER9A_TX_CH20n						Yes	BN2				
9A			GXER9A_TX_CH21n						Yes	BT5				
9A			GXER9A_TX_CH22n						Yes	BU2				
9A			GXER9A_TX_CH23n						Yes	BV5				
9A			GXER9A_RX_CH0p						Yes	AK7				
9A			GXER9A_RX_CH1p						Yes	AL10				
9A			GXER9A_RX_CH2p						Yes	AP7				
9A			GXER9A_RX_CH3p						Yes	AR10				
9A			GXER9A_RX_CH8p						Yes	AV7				
9A			GXER9A_RX_CH9p						Yes	AW10				
9A			GXER9A_RX_CH10p						Yes	BB7				
9A			GXER9A_RX_CH11p						Yes	BC10				
9A			GXER9A_RX_CH12p						Yes	BF7				
9A			GXER9A_RX_CH13p						Yes	BG10				
9A			GXER9A_RX_CH14p						Yes	BK7				
9A			GXER9A_RX_CH15p						Yes	BL10				
9A			GXER9A_RX_CH20p						Yes	BP7				
9A			GXER9A_RX_CH21p						Yes	BR10				
9A			GXER9A_RX_CH22p						Yes	BV7				
9A			GXER9A_RX_CH23p						Yes	BW10				
9A			GXER9A_RX_CH0n						Yes	AJ8				
9A			GXER9A_RX_CH1n						Yes	AM11				
9A			GXER9A_RX_CH2n						Yes	AN8				
9A			GXER9A_RX_CH3n						Yes	AT11				
9A			GXER9A_RX_CH8n						Yes	AU8				
9A			GXER9A_RX_CH9n						Yes	AV11				
9A			GXER9A_RX_CH10n						Yes	BA8				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
9A			GXER9A_RX_CH11n						Yes	BD11				
9A			GXER9A_RX_CH12n						Yes	BE8				
9A			GXER9A_RX_CH13n						Yes	BH11				
9A			GXER9A_RX_CH14n						Yes	BJ8				
9A			GXER9A_RX_CH15n						Yes	BM11				
9A			GXER9A_RX_CH20n						Yes	BN8				
9A			GXER9A_RX_CH21n						Yes	BT11				
9A			GXER9A_RX_CH22n						Yes	BU8				
9A			GXER9A_RX_CH23n						Yes	BV11				
9A			REFCLK_GXER9A_CH0p							AT13				
9A			REFCLK_GXER9A_CH0n							AP13				
9A			REFCLK_GXER9A_CH1p							AR14				
9A			REFCLK_GXER9A_CH1n							AN14				
9A			REFCLK_GXER9A_CH2p							AJ12				
9A			REFCLK_GXER9A_CH2n							AH11				
9A			REFCLK_GXER9A_CH3p							AK13				
9A			REFCLK_GXER9A_CH3n							AH13				
9A			REFCLK_GXER9A_CH4p							AJ14				
9A			REFCLK_GXER9A_CH4n							AL14				
9A			REFCLK_GXER9A_CH5p							AR16				
9A			REFCLK_GXER9A_CH5n							AN16				
9A			REFCLK_GXER9A_CH6p							AJ16				
9A			REFCLK_GXER9A_CH6n							AL16				
9A			REFCLK_GXER9A_CH7p							AH15				
9A			REFCLK_GXER9A_CH7n							AK15				
9A			REFCLK_GXER9A_CH8p							AH17				
9A			REFCLK_GXER9A_CH8n							AK17				
10A			GXPL10A_TX_CH0p							BP55				
10A			GXPL10A_TX_CH1p							BN52				
10A			GXPL10A_TX_CH2p							BK55				
10A			GXPL10A_TX_CH3p							BS2				
10A			GXPL10A_TX_CH4p							BF55				
10A			GXPL10A_TX_CH5p							BE52				
10A			GXPL10A_TX_CH6p							BB55				
10A			GXPL10A_TX_CH7p							BA52				
10A			GXPL10A_TX_CH8p							AV55				
10A			GXPL10A_TX_CH9p							AU52				
10A			GXPL10A_TX_CH10p							AP55				
10A			GXPL10A_TX_CH11p							AN52				
10A			GXPL10A_TX_CH12p							AK55				
10A			GXPL10A_TX_CH13p							AS2				
10A			GXPL10A_TX_CH14p							AF55				
10A			GXPL10A_TX_CH15p							AE52				
10A			GXPL10A_TX_CH0n							BR56				
10A			GXPL10A_TX_CH1n							BM53				
10A			GXPL10A_TX_CH2n							BL56				
10A			GXPL10A_TX_CH3n							BH53				
10A			GXPL10A_TX_CH4n							BS56				
10A			GXPL10A_TX_CH5n							BD53				
10A			GXPL10A_TX_CH6n							BC56				
10A			GXPL10A_TX_CH7n							AV53				
10A			GXPL10A_TX_CH8n							AW56				
10A			GXPL10A_TX_CH9n							AT53				
10A			GXPL10A_TX_CH10n							AR56				
10A			GXPL10A_TX_CH11n							AM53				
10A			GXPL10A_TX_CH12n							AL56				
10A			GXPL10A_TX_CH13n							AH53				
10A			GXPL10A_TX_CH14n							AG56				
10A			GXPL10A_TX_CH15n							AD53				
10A			GXPL10A_RX_CH0p							BP61				
10A			GXPL10A_RX_CH1p							BN58				
10A			GXPL10A_RX_CH2p							BK61				
10A			GXPL10A_RX_CH3p							BJ58				
10A			GXPL10A_RX_CH4p							BF61				
10A			GXPL10A_RX_CH5p							BS58				
10A			GXPL10A_RX_CH6p							BB61				
10A			GXPL10A_RX_CH7p							BA58				
10A			GXPL10A_RX_CH8p							AV61				
10A			GXPL10A_RX_CH9p							AU58				
10A			GXPL10A_RX_CH10p							AP61				
10A			GXPL10A_RX_CH11p							AN58				
10A			GXPL10A_RX_CH12p							AK61				
10A			GXPL10A_RX_CH13p							AS58				
10A			GXPL10A_RX_CH14p							AF61				
10A			GXPL10A_RX_CH15p							AE58				
10A			GXPL10A_RX_CH0n							BR62				
10A			GXPL10A_RX_CH1n							BM59				
10A			GXPL10A_RX_CH2n							BL62				
10A			GXPL10A_RX_CH3n							BH59				
10A			GXPL10A_RX_CH4n							BS62				
10A			GXPL10A_RX_CH5n							BD59				
10A			GXPL10A_RX_CH6n							BC62				
10A			GXPL10A_RX_CH7n							AV59				
10A			GXPL10A_RX_CH8n							AW62				
10A			GXPL10A_RX_CH9n							AT59				
10A			GXPL10A_RX_CH10n							AR62				
10A			GXPL10A_RX_CH11n							AM59				
10A			GXPL10A_RX_CH12n							AL62				
10A			GXPL10A_RX_CH13n							AH59				
10A			GXPL10A_RX_CH14n							AG62				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
10A			GXP10A_RX_CH15n							AD59				
U10 WHR			I_PIN_PERST_N_U10_P							BU58				
10A			REFCLK_GXP10A_CH0n							AH49				
10A			REFCLK_GXP10A_CH0p							AJ48				
10A			REFCLK_GXP10A_CH2n							AD49				
10A			REFCLK_GXP10A_CH2p							AE48				
			GND							BV59				
			GND							BN48				
			GND							BV57				
			GND							BU54				
			GND							Y9				
			GND							Y7				
			GND							Y61				
			GND							Y59				
			GND							Y57				
			GND							Y55				
			GND							Y53				
			GND							Y51				
			GND							Y5				
			GND							Y49				
			GND							Y47				
			GND							Y45				
			GND							Y43				
			GND							Y41				
			GND							Y39				
			GND							Y37				
			GND							Y35				
			GND							Y33				
			GND							Y31				
			GND							Y3				
			GND							Y29				
			GND							Y27				
			GND							Y25				
			GND							Y23				
			GND							Y21				
			GND							Y19				
			GND							Y17				
			GND							Y15				
			GND							Y13				
			GND							Y11				
			GND							Y1				
			GND							W60				
			GND							W46				
			GND							W4				
			GND							W32				
			GND							W18				
			GND							V53				
			GND							V39				
			GND							V25				
			GND							V11				
			GND							U60				
			GND							U46				
			GND							U4				
			GND							U32				
			GND							U18				
			GND							T53				
			GND							T39				
			GND							T25				
			GND							T11				
			GND							R8				
			GND							R62				
			GND							R60				
			GND							R6				
			GND							R58				
			GND							R56				
			GND							R54				
			GND							R52				
			GND							R50				
			GND							R48				
			GND							R46				
			GND							R44				
			GND							R42				
			GND							R40				
			GND							R4				
			GND							R38				
			GND							R36				
			GND							R34				
			GND							R32				
			GND							R30				
			GND							R28				
			GND							R26				
			GND							R24				
			GND							R22				
			GND							R20				
			GND							R2				
			GND							R18				
			GND							R16				
			GND							R14				
			GND							R12				
			GND							R10				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							P53				
			GND							P39				
			GND							P25				
			GND							P11				
			GND							N60				
			GND							N46				
			GND							N4				
			GND							N32				
			GND							N18				
			GND							M53				
			GND							M39				
			GND							M25				
			GND							M11				
			GND							L60				
			GND							L46				
			GND							L4				
			GND							L32				
			GND							L18				
			GND							K9				
			GND							K7				
			GND							K61				
			GND							K59				
			GND							K57				
			GND							K55				
			GND							K53				
			GND							K51				
			GND							K5				
			GND							K49				
			GND							K47				
			GND							K45				
			GND							K43				
			GND							K41				
			GND							K39				
			GND							K37				
			GND							K35				
			GND							K33				
			GND							K31				
			GND							K3				
			GND							K29				
			GND							K27				
			GND							K25				
			GND							K23				
			GND							K21				
			GND							K19				
			GND							K17				
			GND							K15				
			GND							K13				
			GND							K11				
			GND							K1				
			GND							J60				
			GND							J46				
			GND							J4				
			GND							J32				
			GND							J18				
			GND							H53				
			GND							H39				
			GND							H25				
			GND							H11				
			GND							G60				
			GND							G46				
			GND							G4				
			GND							G32				
			GND							G18				
			GND							P53				
			GND							F39				
			GND							F25				
			GND							F11				
			GND							E8				
			GND							E62				
			GND							E60				
			GND							E6				
			GND							E58				
			GND							E56				
			GND							E54				
			GND							E52				
			GND							E50				
			GND							E48				
			GND							E46				
			GND							E44				
			GND							E42				
			GND							E40				
			GND							E4				
			GND							E38				
			GND							E36				
			GND							E34				
			GND							E32				
			GND							E30				
			GND							E28				
			GND							E26				
			GND							E24				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							E22				
			GND							E20				
			GND							E2				
			GND							E18				
			GND							E16				
			GND							E14				
			GND							E12				
			GND							E10				
			GND							DC60				
			GND							DC58				
			GND							DC44				
			GND							DC4				
			GND							DC30				
			GND							DC2				
			GND							DC16				
			GND							DB9				
			GND							DB61				
			GND							DB59				
			GND							DB51				
			GND							DB37				
			GND							DB3				
			GND							DB23				
			GND							DB1				
			GND							DA62				
			GND							DA60				
			GND							DA58				
			GND							DA44				
			GND							DA30				
			GND							DA2				
			GND							DA16				
			GND							DB1				
			GND							DB3				
			GND							DB9				
			GND							D3				
			GND							D25				
			GND							D11				
			GND							D1				
			GND							CY9				
			GND							CY61				
			GND							CY51				
			GND							CY37				
			GND							CY23				
			GND							CY1				
			GND							CW8				
			GND							CW62				
			GND							CW60				
			GND							CW6				
			GND							CW58				
			GND							CW56				
			GND							CW54				
			GND							CW52				
			GND							CW50				
			GND							CW48				
			GND							CW46				
			GND							CW44				
			GND							CW42				
			GND							CW40				
			GND							CW4				
			GND							CW38				
			GND							CW36				
			GND							CW34				
			GND							CW32				
			GND							CW30				
			GND							CW28				
			GND							CW26				
			GND							CW24				
			GND							CW22				
			GND							CW20				
			GND							CW2				
			GND							CW18				
			GND							CW16				
			GND							CW14				
			GND							CW12				
			GND							CW10				
			GND							CY9				
			GND							CY51				
			GND							CY37				
			GND							CY23				
			GND							CU58				
			GND							CU44				
			GND							CU30				
			GND							CU2				
			GND							CU116				
			GND							CT9				
			GND							CTS1				
			GND							CT37				
			GND							CT23				
			GND							CR58				
			GND							CR44				
			GND							CR30				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							CR2				
			GND							CR16				
			GND							CP9				
			GND							CP7				
			GND							CP61				
			GND							CP59				
			GND							CP57				
			GND							CP55				
			GND							CP53				
			GND							CP51				
			GND							CP5				
			GND							CP49				
			GND							CP47				
			GND							CP45				
			GND							CP43				
			GND							CP41				
			GND							CP39				
			GND							CP37				
			GND							CP35				
			GND							CP33				
			GND							CP31				
			GND							CP3				
			GND							CP29				
			GND							CP27				
			GND							CP25				
			GND							CP23				
			GND							CP21				
			GND							CP19				
			GND							CP17				
			GND							CP15				
			GND							CP13				
			GND							CP11				
			GND							CP1				
			GND							CM58				
			GND							CM44				
			GND							CM30				
			GND							CM2				
			GND							CM16				
			GND							CM9				
			GND							CM51				
			GND							CM37				
			GND							CM23				
			GND							CL58				
			GND							CL44				
			GND							CL30				
			GND							CL2				
			GND							CL16				
			GND							CK9				
			GND							CK51				
			GND							CK37				
			GND							CK23				
			GND							CK8				
			GND							CJ62				
			GND							CJ60				
			GND							CJ6				
			GND							CJ58				
			GND							CJ56				
			GND							CJ54				
			GND							CJ52				
			GND							CJ50				
			GND							CJ48				
			GND							CJ46				
			GND							CJ44				
			GND							CJ42				
			GND							CJ40				
			GND							CJ4				
			GND							CJ38				
			GND							CJ36				
			GND							CJ34				
			GND							CJ32				
			GND							CJ30				
			GND							CJ28				
			GND							CJ26				
			GND							CJ24				
			GND							CJ22				
			GND							CJ20				
			GND							CJ2				
			GND							CJ18				
			GND							CJ16				
			GND							CJ14				
			GND							CJ12				
			GND							CJ10				
			GND							CH9				
			GND							CH51				
			GND							CH37				
			GND							CH23				
			GND							CG58				
			GND							CG44				
			GND							CG30				
			GND							CG2				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							CG16				
			GND							CF9				
			GND							CF51				
			GND							CF37				
			GND							CF23				
			GND							CE58				
			GND							CE44				
			GND							CE30				
			GND							CE2				
			GND							CE16				
			GND							CD9				
			GND							CD7				
			GND							CD61				
			GND							CD59				
			GND							CD57				
			GND							CD55				
			GND							CD53				
			GND							CD51				
			GND							CD5				
			GND							CD49				
			GND							CD47				
			GND							CD45				
			GND							CD43				
			GND							CD41				
			GND							CD39				
			GND							CD37				
			GND							CD35				
			GND							CD33				
			GND							CD31				
			GND							CD3				
			GND							CD29				
			GND							CD27				
			GND							CD25				
			GND							CD23				
			GND							CD21				
			GND							CD19				
			GND							CD17				
			GND							CD15				
			GND							CD13				
			GND							CD11				
			GND							CD1				
			GND							CC6				
			GND							CC54				
			GND							CC46				
			GND							CC44				
			GND							CC42				
			GND							CC40				
			GND							CC4				
			GND							CC12				
			GND							CC10				
			GND							CB9				
			GND							CB55				
			GND							CB5				
			GND							CB3				
			GND							CB23				
			GND							CB11				
			GND							CA6				
			GND							CA46				
			GND							CA44				
			GND							CA42				
			GND							CA40				
			GND							CA4				
			GND							CA38				
			GND							CA36				
			GND							CA34				
			GND							CA32				
			GND							CA30				
			GND							CA28				
			GND							CA26				
			GND							CA24				
			GND							CA22				
			GND							CA20				
			GND							CA2				
			GND							CA12				
			GND							CA10				
			GND							C62				
			GND							C60				
			GND							C46				
			GND							C4				
			GND							C32				
			GND							C2				
			GND							C18				
			GND							BY9				
			GND							BY7				
			GND							BY51				
			GND							BY3				
			GND							BY23				
			GND							BY13				
			GND							BY1				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							BW8				
			GND							BW62				
			GND							BW60				
			GND							BW6				
			GND							BW58				
			GND							BW56				
			GND							BW54				
			GND							BW52				
			GND							BW50				
			GND							BW48				
			GND							BW46				
			GND							BW44				
			GND							BW42				
			GND							BW40				
			GND							BW38				
			GND							BW36				
			GND							BW34				
			GND							BW32				
			GND							BW30				
			GND							BW28				
			GND							BW26				
			GND							BW24				
			GND							BW22				
			GND							BW20				
			GND							BW2				
			GND							BW16				
			GND							BW14				
			GND							BW12				
			GND							BW9				
			GND							BV61				
			GND							BV59				
			GND							BV5				
			GND							BV3				
			GND							BV25				
			GND							BV11				
			GND							BU62				
			GND							BU6				
			GND							BU56				
			GND							BU42				
			GND							BU40				
			GND							BU4				
			GND							BU38				
			GND							BU36				
			GND							BU34				
			GND							BU32				
			GND							BU30				
			GND							BU28				
			GND							BU26				
			GND							BU24				
			GND							BU22				
			GND							BU20				
			GND							BU18				
			GND							BU12				
			GND							BU10				
			GND							BT9				
			GND							BT7				
			GND							BT61				
			GND							BT57				
			GND							BT55				
			GND							BT3				
			GND							BT25				
			GND							BT17				
			GND							BT15				
			GND							BT13				
			GND							BT11				
			GND							BR8				
			GND							BR60				
			GND							BR6				
			GND							BR58				
			GND							BR54				
			GND							BR52				
			GND							BR48				
			GND							BR46				
			GND							BR42				
			GND							BR40				
			GND							BR38				
			GND							BR22				
			GND							BR2				
			GND							BR18				
			GND							BR16				
			GND							BR14				
			GND							BR12				
			GND							BP9				
			GND							BP59				
			GND							BP57				
			GND							BP53				
			GND							BP51				
			GND							BP5				
			GND							BP49				
			GND							BP45				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							BP43				
			GND							BP3				
			GND							BP13				
			GND							BP11				
			GND							BN62				
			GND							BN60				
			GND							BN6				
			GND							BN56				
			GND							BN54				
			GND							BN42				
			GND							BN40				
			GND							BN4				
			GND							BN38				
			GND							BN36				
			GND							BN34				
			GND							BN32				
			GND							BN30				
			GND							BN28				
			GND							BN26				
			GND							BN24				
			GND							BN22				
			GND							BN20				
			GND							BN18				
			GND							BN16				
			GND							BN12				
			GND							BN10				
			GND							BN9				
			GND							BN7				
			GND							BN61				
			GND							BN57				
			GND							BN55				
			GND							BN51				
			GND							BN3				
			GND							BN19				
			GND							BN13				
			GND							BN1				
			GND							BL8				
			GND							BL60				
			GND							BL6				
			GND							BL58				
			GND							BL54				
			GND							BL52				
			GND							BL42				
			GND							BL40				
			GND							BL38				
			GND							BL36				
			GND							BL34				
			GND							BL32				
			GND							BL30				
			GND							BL28				
			GND							BL26				
			GND							BL24				
			GND							BL22				
			GND							BL2				
			GND							BL18				
			GND							BL16				
			GND							BL12				
			GND							BK9				
			GND							BK59				
			GND							BK57				
			GND							BK53				
			GND							BK51				
			GND							BK5				
			GND							BK47				
			GND							BK45				
			GND							BK43				
			GND							BK3				
			GND							BK13				
			GND							BK11				
			GND							BJ62				
			GND							BJ60				
			GND							BJ6				
			GND							BJ56				
			GND							BJ54				
			GND							BJ50				
			GND							BJ48				
			GND							BJ46				
			GND							BJ44				
			GND							BJ42				
			GND							BJ40				
			GND							BJ4				
			GND							BJ38				
			GND							BJ26				
			GND							BJ24				
			GND							BJ22				
			GND							BJ18				
			GND							BJ16				
			GND							BJ12				
			GND							BJ10				
			GND							BJ9				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							BH7				
			GND							BH61				
			GND							BH57				
			GND							BH55				
			GND							BH51				
			GND							BH3				
			GND							BH17				
			GND							BH15				
			GND							BH13				
			GND							BH1				
			GND							BG8				
			GND							BG60				
			GND							BG6				
			GND							BG58				
			GND							BG54				
			GND							BG52				
			GND							BG50				
			GND							BG48				
			GND							BG46				
			GND							BG44				
			GND							BG42				
			GND							BG40				
			GND							BG38				
			GND							BG26				
			GND							BG24				
			GND							BG22				
			GND							BG20				
			GND							BG2				
			GND							BG18				
			GND							BG12				
			GND							BF9				
			GND							BF59				
			GND							BF57				
			GND							BF53				
			GND							BF51				
			GND							BF5				
			GND							BF49				
			GND							BF47				
			GND							BF45				
			GND							BF3				
			GND							BF19				
			GND							BF17				
			GND							BF15				
			GND							BF13				
			GND							BF11				
			GND							BE62				
			GND							BE60				
			GND							BE6				
			GND							BE56				
			GND							BE54				
			GND							BE50				
			GND							BE48				
			GND							BE42				
			GND							BE40				
			GND							BE4				
			GND							BE38				
			GND							BE26				
			GND							BE24				
			GND							BE22				
			GND							BE18				
			GND							BE12				
			GND							BE10				
			GND							BD9				
			GND							BD7				
			GND							BD61				
			GND							BD57				
			GND							BD55				
			GND							BD51				
			GND							BD47				
			GND							BD45				
			GND							BD3				
			GND							BD17				
			GND							BD15				
			GND							BD13				
			GND							BD1				
			GND							BC8				
			GND							BC60				
			GND							BC6				
			GND							BC58				
			GND							BC54				
			GND							BC52				
			GND							BC50				
			GND							BC42				
			GND							BC40				
			GND							BC38				
			GND							BC26				
			GND							BC24				
			GND							BC22				
			GND							BC2				
			GND							BC18				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							9C16				
			GND							9C12				
			GND							8B9				
			GND							8B59				
			GND							8B57				
			GND							8B53				
			GND							8B51				
			GND							8B5				
			GND							8B47				
			GND							8B45				
			GND							8B43				
			GND							8B3				
			GND							8B13				
			GND							8B11				
			GND							8A62				
			GND							8A60				
			GND							8A6				
			GND							8A56				
			GND							8A54				
			GND							8A50				
			GND							8A48				
			GND							8A46				
			GND							8A42				
			GND							8A40				
			GND							8A4				
			GND							8A38				
			GND							8A24				
			GND							8A22				
			GND							8A20				
			GND							8A18				
			GND							8A16				
			GND							8A12				
			GND							8A10				
			GND							861				
			GND							859				
			GND							853				
			GND							839				
			GND							83				
			GND							825				
			GND							811				
			GND							81				
			GND							A99				
			GND							A97				
			GND							A961				
			GND							A957				
			GND							A955				
			GND							A951				
			GND							A945				
			GND							A93				
			GND							A913				
			GND							A91				
			GND							AW8				
			GND							AW60				
			GND							AW6				
			GND							AW58				
			GND							AW54				
			GND							AW52				
			GND							AW50				
			GND							AW48				
			GND							AW42				
			GND							AW40				
			GND							AW38				
			GND							AW26				
			GND							AW24				
			GND							AW22				
			GND							AW2				
			GND							AW18				
			GND							AW16				
			GND							AW12				
			GND							AW9				
			GND							AW59				
			GND							AW57				
			GND							AW53				
			GND							AW51				
			GND							AW5				
			GND							AW45				
			GND							AW3				
			GND							AW19				
			GND							AW17				
			GND							AW13				
			GND							AW11				
			GND							AL62				
			GND							AL60				
			GND							AL6				
			GND							AL56				
			GND							AL54				
			GND							AL50				
			GND							AL48				
			GND							AL42				
			GND							AL40				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							AU4				
			GND							AU38				
			GND							AU26				
			GND							AU24				
			GND							AU22				
			GND							AU18				
			GND							AU16				
			GND							AU14				
			GND							AU12				
			GND							AU10				
			GND							AT9				
			GND							AT7				
			GND							AT61				
			GND							AT57				
			GND							AT55				
			GND							AT51				
			GND							AT47				
			GND							AT45				
			GND							AT43				
			GND							AT3				
			GND							AT15				
			GND							AT1				
			GND							AR8				
			GND							AR60				
			GND							AR6				
			GND							AR58				
			GND							AR54				
			GND							AR52				
			GND							AR50				
			GND							AR46				
			GND							AR42				
			GND							AR40				
			GND							AR38				
			GND							AR26				
			GND							AR24				
			GND							AR22				
			GND							AR2				
			GND							AR12				
			GND							AP9				
			GND							AP59				
			GND							AP57				
			GND							AP53				
			GND							AP51				
			GND							AP5				
			GND							AP47				
			GND							AP45				
			GND							AP43				
			GND							AP3				
			GND							AP19				
			GND							AP15				
			GND							AP11				
			GND							AN62				
			GND							AN60				
			GND							AN6				
			GND							AN56				
			GND							AN54				
			GND							AN50				
			GND							AN44				
			GND							AN42				
			GND							AN40				
			GND							AN4				
			GND							AN38				
			GND							AN36				
			GND							AN34				
			GND							AN32				
			GND							AN30				
			GND							AN28				
			GND							AN26				
			GND							AN24				
			GND							AN22				
			GND							AN12				
			GND							AN10				
			GND							AN8				
			GND							AM7				
			GND							AM61				
			GND							AM57				
			GND							AM55				
			GND							AM51				
			GND							AM47				
			GND							AM45				
			GND							AM43				
			GND							AM3				
			GND							AM17				
			GND							AM15				
			GND							AM13				
			GND							AM1				
			GND							AL8				
			GND							AL60				
			GND							AL6				
			GND							AL58				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							AL54				
			GND							AL52				
			GND							AL50				
			GND							AL48				
			GND							AL46				
			GND							AL44				
			GND							AL42				
			GND							AL40				
			GND							AL38				
			GND							AL36				
			GND							AL34				
			GND							AL32				
			GND							AL30				
			GND							AL28				
			GND							AL26				
			GND							AL24				
			GND							AL22				
			GND							AL20				
			GND							AL2				
			GND							AL18				
			GND							AL12				
			GND							AK9				
			GND							AK59				
			GND							AK57				
			GND							AK53				
			GND							AK51				
			GND							AK5				
			GND							AK49				
			GND							AK47				
			GND							AK45				
			GND							AK43				
			GND							AK3				
			GND							AK19				
			GND							AK11				
			GND							AJ62				
			GND							AJ60				
			GND							AJ6				
			GND							AJ56				
			GND							AJ54				
			GND							AJ50				
			GND							AJ44				
			GND							AJ42				
			GND							AJ40				
			GND							AJ4				
			GND							AJ38				
			GND							AJ22				
			GND							AJ18				
			GND							AJ10				
			GND							AH9				
			GND							AH7				
			GND							AH61				
			GND							AH57				
			GND							AH55				
			GND							AH51				
			GND							AH47				
			GND							AH39				
			GND							AH3				
			GND							AH1				
			GND							AG8				
			GND							AG60				
			GND							AG58				
			GND							AG54				
			GND							AG52				
			GND							AG50				
			GND							AG48				
			GND							AG44				
			GND							AG42				
			GND							AG40				
			GND							AG38				
			GND							AG36				
			GND							AG34				
			GND							AG32				
			GND							AG30				
			GND							AG28				
			GND							AG26				
			GND							AG24				
			GND							AG22				
			GND							AG20				
			GND							AG2				
			GND							AG18				
			GND							AG16				
			GND							AG14				
			GND							AG12				
			GND							AG10				
			GND							AF59				
			GND							AF57				
			GND							AF53				
			GND							AF51				
			GND							AF49				
			GND							AF47				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND							AF45				
			GND							AF39				
			GND							AF17				
			GND							AF15				
			GND							AF13				
			GND							AF11				
			GND							AE8				
			GND							AE62				
			GND							AE60				
			GND							AE6				
			GND							AE56				
			GND							AE54				
			GND							AE50				
			GND							AE46				
			GND							AE42				
			GND							AE40				
			GND							AE4				
			GND							AE38				
			GND							AE36				
			GND							AE34				
			GND							AE32				
			GND							AE30				
			GND							AE28				
			GND							AE26				
			GND							AE24				
			GND							AE22				
			GND							AE20				
			GND							AE2				
			GND							AE18				
			GND							AE16				
			GND							AE14				
			GND							AE12				
			GND							AE10				
			GND							AD61				
			GND							AD57				
			GND							AD55				
			GND							AD51				
			GND							AD47				
			GND							AD41				
			GND							AC60				
			GND							AC58				
			GND							AC54				
			GND							AC52				
			GND							AC50				
			GND							AC48				
			GND							AC40				
			GND							AC38				
			GND							AC36				
			GND							AC34				
			GND							AC32				
			GND							AC30				
			GND							AC28				
			GND							AC26				
			GND							AC24				
			GND							AC20				
			GND							AB59				
			GND							AB57				
			GND							AB53				
			GND							AB51				
			GND							AB49				
			GND							AB41				
			GND							AB21				
			GND							AA60				
			GND							AA58				
			GND							AA54				
			GND							AA50				
			GND							AA40				
			GND							A60				
			GND							A58				
			GND							A46				
			GND							A4				
			GND							A32				
			GND							A2				
			GND							A18				
			GNDSENSE							BA26				
			VCC							BV23				
			VCC							BV21				
			VCC							BT23				
			VCC							BT21				
			VCC							BP41				
			VCC							BP39				
			VCC							BP37				
			VCC							BP35				
			VCC							BP33				
			VCC							BP31				
			VCC							BP29				
			VCC							BP27				
			VCC							BP25				
			VCC							BP23				
			VCC							BP21				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCC							BM41				
			VCC							BM39				
			VCC							BM37				
			VCC							BM35				
			VCC							BM33				
			VCC							BM31				
			VCC							BM29				
			VCC							BM27				
			VCC							BM25				
			VCC							BM23				
			VCC							BM21				
			VCC							BK41				
			VCC							BK39				
			VCC							BK37				
			VCC							BK25				
			VCC							BK23				
			VCC							BK21				
			VCC							BH41				
			VCC							BH39				
			VCC							BH37				
			VCC							BH25				
			VCC							BH23				
			VCC							BH21				
			VCC							BF41				
			VCC							BF39				
			VCC							BF37				
			VCC							BF25				
			VCC							BF23				
			VCC							BF21				
			VCC							BD41				
			VCC							BD39				
			VCC							BD37				
			VCC							BD25				
			VCC							BD23				
			VCC							BD21				
			VCC							BB41				
			VCC							BB39				
			VCC							BB37				
			VCC							BB23				
			VCC							BB21				
			VCC							AV41				
			VCC							AV39				
			VCC							AV37				
			VCC							AV25				
			VCC							AV23				
			VCC							AV21				
			VCC							AT41				
			VCC							AT39				
			VCC							AT37				
			VCC							AT25				
			VCC							AT23				
			VCC							AT21				
			VCC							AP41				
			VCC							AP39				
			VCC							AP37				
			VCC							AP25				
			VCC							AP23				
			VCC							AP21				
			VCC							AM41				
			VCC							AM39				
			VCC							AM37				
			VCC							AM35				
			VCC							AM33				
			VCC							AM31				
			VCC							AM29				
			VCC							AM27				
			VCC							AM25				
			VCC							AM23				
			VCC							AM21				
			VCC							AK41				
			VCC							AK39				
			VCC							AK37				
			VCC							AK35				
			VCC							AK33				
			VCC							AK31				
			VCC							AK29				
			VCC							AK27				
			VCC							AK25				
			VCC							AK21				
			VCC							AH41				
			VCC							AF41				
			VCCPT							BR36				
			VCCPT							BR34				
			VCCPT							BR32				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCPT							BR30				
			VCCPT							AJ36				
			VCCPT							AJ34				
			VCCPT							AJ32				
			VCCPT							AJ30				
			DNU							BY55				
			DNU							CF61				
			DNU							CH61				
			DNU							CB53				
			DNU							BY53				
			DNU							AH19				
			DNU							AJ20				
			DNU							CB61				
			DNU							AD45				
			DNU							BW18				
			DNU							AD19				
			DNU							BW61				
			DNU							AE44				
			DNU							BY19				
			DNU							AF19				
			DNU							AM19				
			DNU							AT19				
			DNU							AP17				
			DNU							AT17				
			DNU							AJ46				
			DNU							BN50				
			DNU							BK49				
			DNU							BM49				
			DNU							BL48				
			DNU							BL44				
			DNU							BL44				
			DNU							BT49				
			DNU							BV49				
			DNU							BV51				
			DNU							BU50				
			DNU							BT51				
			DNU							BL46				
			DNU							BR50				
			DNU							BN46				
			DNU							BY53				
			DNU							BP47				
			DNU							BN44				
			DNU							BU52				
			DNU							BV55				
			DNU							BT53				
			DNU							BM47				
			DNU							BV45				
			DNU							BN45				
			DNU							BT45				
			DNU							BU46				
			DNU							BU48				
			DNU							BV47				
			DNU							BT47				
			DNU							BL50				
			TEMPDIODE4n							CA52				
			TEMPDIODE4p							CC52				
			TEMPDIODE0n							AH43				
			TEMPDIODE0p							AF43				
			TEMPDIODE1n							AG46				
			TEMPDIODE1p							AH45				
			TEMPDIODE4n							AR20				
			TEMPDIODE4p							AN18				
			VCCBAT							CC48				
			VCCBAT							CA48				
			VCCA_PLL							CC34				
			VCCA_PLL							CC32				
			VCCA_PLL							CC30				
			VCCA_PLL							CC28				
			VCCA_PLL							AA34				
			VCCA_PLL							AA32				
			VCCA_PLL							AA30				
			VCCA_PLL							AA28				
			VCCIO_HPS							AH21				
			VCCIO_HPS							AF21				
			VCCIO_HPS							AD21				
			VCCIO_PIO_2A							CB39				
			VCCIO_PIO_2A							CB37				
			VCCIO_PIO_2A							BY39				
			VCCIO_PIO_2A							BY37				
			VCCIO_PIO_2B							CB35				
			VCCIO_PIO_2B							CB33				
			VCCIO_PIO_2B							BY35				
			VCCIO_PIO_2B							BY33				
			VCCIO_PIO_2C							CB31				
			VCCIO_PIO_2C							CB29				
			VCCIO_PIO_2C							BY31				
			VCCIO_PIO_2C							BY29				
			VCCIO_PIO_2D							CB27				
			VCCIO_PIO_2D							CB25				
			VCCIO_PIO_2D							BY27				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCIO_P10_2D											
			VCCIO_P10_3A											
			VCCIO_P10_3A							AD39				
			VCCIO_P10_3A							AD37				
			VCCIO_P10_3A							AB39				
			VCCIO_P10_3A							AB37				
			VCCIO_P10_3B							AD35				
			VCCIO_P10_3B							AD33				
			VCCIO_P10_3B							AB35				
			VCCIO_P10_3B							AB33				
			VCCIO_P10_3C							AD31				
			VCCIO_P10_3C							AD29				
			VCCIO_P10_3C							AB31				
			VCCIO_P10_3C							AB29				
			VCCIO_P10_3D							AD27				
			VCCIO_P10_3D							AD25				
			VCCIO_P10_3D							AB27				
			VCCIO_P10_3D							AB25				
			VCCIO_P10_SDM							BV43				
			VCCIO_SDM							CB41				
			VCCIO_SDM							BV41				
2A		VREFB2AN0	VREFB2AN0							CC38				
2B		VREFB2BN0	VREFB2BN0							CC36				
2C		VREFB2CN0	VREFB2CN0							CC26				
2D		VREFB2DN0	VREFB2DN0							CC24				
3A		VREFB3AN0	VREFB3AN0							AA38				
3B		VREFB3BN0	VREFB3BN0							AA36				
3C		VREFB3CN0	VREFB3CN0							AA26				
3D		VREFB3DN0	VREFB3DN0							AA24				
			VREFM_ADC							CA62				
			VREFP_ADC							CC62				
			NC							W62				
			NC							V61				
			NC							U62				
			NC							T61				
			NC							P61				
			NC							N62				
			NC							M61				
			NC							L62				
			NC							J62				
			NC							G62				
			NC							CM61				
			NC							CM1				
			NC							CK61				
			NC							CK1				
			NC							CH1				
			NC							CF1				
			NC							CC8				
			NC							CC50				
			NC							CC22				
			NC							CC20				
			NC							CC2				
			NC							CC18				
			NC							CC16				
			NC							CC14				
			NC							CB7				
			NC							CB51				
			NC							CB21				
			NC							CB19				
			NC							CB17				
			NC							CB15				
			NC							CB13				
			NC							CB1				
			NC							CA56				
			NC							CA50				
			NC							CA18				
			NC							CA16				
			NC							CA14				
			NC							BV57				
			NC							BV21				
			NC							BV17				
			NC							BV15				
			NC							BV19				
			NC							BV17				
			NC							BV15				
			NC							BV13				
			NC							BU60				
			NC							BU16				
			NC							BU14				
			NC							BT59				
			NC							AD43				
			NC							AD17				
			NC							AD15				
			NC							AC62				
			NC							AC56				
			NC							AC46				
			NC							AC44				
			NC							AC42				
			NC							AC18				
			NC							AC16				
			NC							AC14				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			NC							AB61				
			NC							AB55				
			NC							AB47				
			NC							AB45				
			NC							AB43				
			NC							AB19				
			NC							AB17				
			NC							AB15				
			NC							AB13				
			NC							AB11				
			NC							AA62				
			NC							AA56				
			NC							AA52				
			NC							AA48				
			NC							AA46				
			NC							AA44				
			NC							AA42				
			NC							AA20				
			NC							AA18				
			NC							AA16				
			NC							AA14				
			NC							AA12				
			NC							AA10				
			VCCRCORE							BV37				
			VCCRCORE							BT37				
			VCCRCORE							BR28				
			VCCRCORE							BR26				
			VCCRCORE							BR24				
			VCCRCORE							AI28				
			VCCRCORE							AJ26				
			VCCRCORE							AJ24				
			VCCRCORE							AH37				
			VCCRCORE							AF37				
			VCCRTPLL_GXER1							BG16				
			VCCRTPLL_GXER1							BE16				
			VCCRT_GXER1							BP15				
			VCCRT_GXER1							BN14				
			VCCRT_GXER1							BM15				
			VCCRT_GXER1							BL14				
			VCCRT_GXER1							BK15				
			VCCRT_GXER1							BL14				
			VCCRT_GXER1							BG14				
			VCCRT_GXER1							BE14				
			VCCRT_GXER1							BC14				
			VCCRT_GXER1							BB15				
			VCCRT_GXER1							BA14				
			VCCRT_GXER1							AV15				
			VCCRT_GXER1							AW14				
			VCCRT_GXER1							AV15				
			VCCRT_GXPL1							BD49				
			VCCRT_GXPL1							BC48				
			VCCRT_GXPL1							BB49				
			VCCRT_GXPL1							AV49				
			VCCRT_GXPL1							AV49				
			VCCRT_GXPL1							AT49				
			VCCRT_GXPL1							AN48				
			VCCRT_GXPL1							AP49				
			VCCRT_GXPL1							AN48				
			VCCRT_GXPL1							AM49				
			IO_AUX_BREF10_P							BM43				
			IO_AUX_BREF20							AN20				
			VCCADC							CB43				
			VCCADC							BY43				
			VCCCLK_GXER1							AR18				
			VCCCLK_GXPL1							BH47				
			VCCCLK_GXPL1							BH45				
			VCCFUSEWR_SDM							CB49				
			VCCFUSEWR_SDM							BV49				
			VCCFUSE_GXP							BR44				
			VCCCH							BF43				
			VCCCH							BD43				
			VCCCH							AV43				
			VCCCH							AV43				
			VCCCH							BL20				
			VCCCH							BJ20				
			VCCCH							BE20				
			VCCCH							BC20				
			VCCCH_GXER1							BP17				
			VCCCH_GXER1							BM17				
			VCCCH_GXER1							BK17				
			VCCCH_GXER1							BB17				
			VCCCH_GXER1							AV17				
			VCCCH_GXPL1							BE46				
			VCCCH_GXPL1							BC46				
			VCCCH_GXPL1							AV47				
			VCCCH_GXPL1							AW46				
			VCCCH_GXPL1							AV47				
			VCCCH_GXPL1							AL46				
			VCCCH_GXPL1							AN46				
			VCCCH_GXPL1							BT43				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	GT support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCLSENSE							B825				
			VCCL_HPS							AK23				
			VCCL_HPS							AH25				
			VCCL_HPS							AH23				
			VCCL_HPS							AF25				
			VCCL_HPS							AF23				
			VCCL_SDM							BW41				
			VCCL_SDM							BV39				
			VCCL_SDM							BT41				
			VCCL_SDM							BT39				
			VCCP							BV35				
			VCCP							BV33				
			VCCP							BV31				
			VCCP							BV29				
			VCCP							BV27				
			VCCP							BT35				
			VCCP							BT33				
			VCCP							BT31				
			VCCP							BT29				
			VCCP							BT27				
			VCCP							AH35				
			VCCP							AH33				
			VCCP							AH31				
			VCCP							AH29				
			VCCP							AH27				
			VCCP							AF35				
			VCCP							AF33				
			VCCP							AF31				
			VCCP							AF29				
			VCCP							AF27				
			VCCPLL_DG_HPS							AD23				
			VCCPLL_DG_HPS							AB23				
			VCCPLL_DG_SDM							CB45				
			VCCPLL_DG_SDM							BW45				
			VCCPLL_HPS							AC22				
			VCCPLL_HPS							AA22				
			VCCPLL_SDM							CB47				
			VCCPLL_SDM							BW47				
			VCC_HSSI_GXER1							BT19				
			VCC_HSSI_GXER1							BP20				
			VCC_HSSI_GXER1							BP19				
			VCC_HSSI_GXER1							BK19				
			VCC_HSSI_GXER1							BH19				
			VCC_HSSI_GXER1							BD19				
			VCC_HSSI_GXER1							BB19				
			VCC_HSSI_GXER1							AY19				
			VCC_HSSI_GXER1							AW20				
			VCC_HSSI_GXER1							AL20				
			VCC_HSSI_GXPL1							BH43				
			VCC_HSSI_GXPL1							BE44				
			VCC_HSSI_GXPL1							BC44				
			VCC_HSSI_GXPL1							BA44				
			VCC_HSSI_GXPL1							AW44				
			VCC_HSSI_GXPL1							AU44				
			VCC_HSSI_GXPL1							AR44				
			U10_P_ID_RESREF_0							BH49				

Date	Version	Changes
June 2020	2020.06.29	Initial release.

(1) For more information about pin definition and pin connection guidelines, refer to the [Intel® Agilex™ Device Family Pin Connection Guidelines](#)