

Revision	Description	Date	Approved

The optional components connected to pins 1, 4, & 5 can be as small as 0402, and are only used to adjust the compensation of the control loop when necessary:

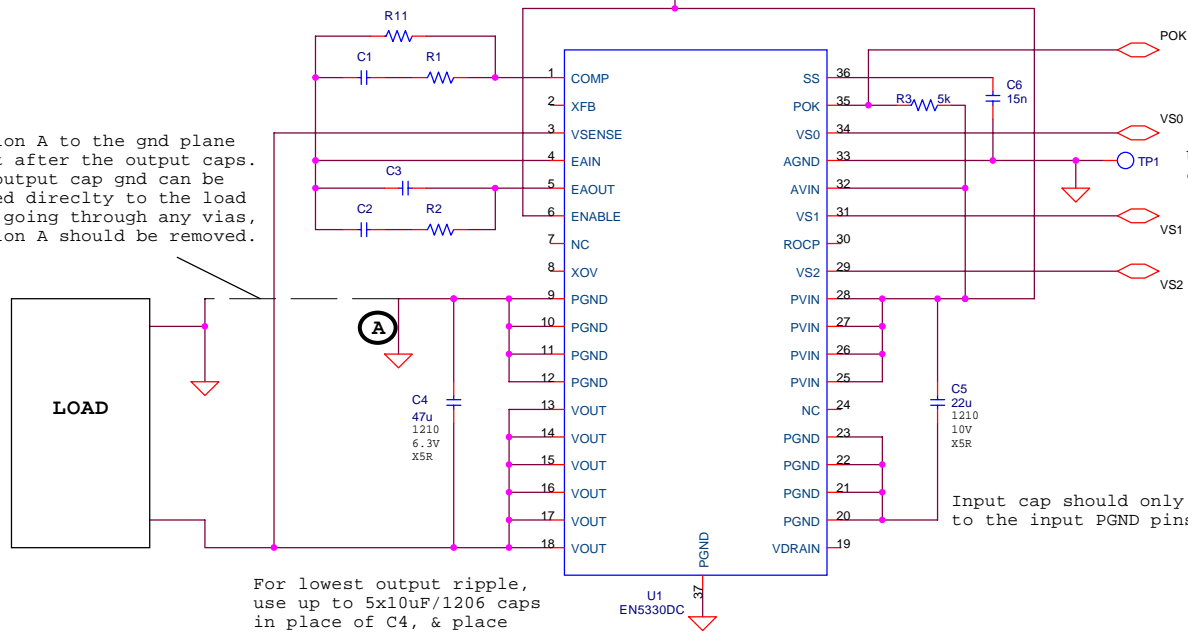
- R11 increases the gain of the loop
- R1 & C1 increase the loop bandwidth
- R2 & C2 decrease the loop bandwidth
- C3 compensates for excess parasitic inductance in the output filter.

In most applications that need compensation adjustment only one or two of the above components are needed.

The POK pull up resistor R3 is optional, necessary only when using the POK signal.

PVIN = 2.5 - 5.5 VDC

Connection A to the gnd plane is right after the output caps. If the output cap gnd can be connected directly to the load without going through any vias, connection A should be removed.



For lowest output ripple, use up to 5x10uF/1206 caps in place of C4, & place smaller decoupling caps right at the load.

Pin 37 is the thermal PGND pad at the package center. Connecting it to the quiet GND plane will make a single connection point between PGND and AGND at the device.

Use through-hole test point TP1 to connect AGND pin to the GND plane.

Input cap should only go to the input PGND pins.

Voltage Programming Connections				
Voltage Selector				Output Voltage
vs2	vs1	vs0		vout
0	0	0		3.3V
0	0	1		2.5V
0	1	0		1.8V
0	1	1		1.5V
1	0	0		1.25V
1	0	1		1.2V
1	1	0		0.8V
1	1	1		<Default> External Divider*

0 = Connect to GNDA

1 = Default connection inside the device

\* Not shown. Refer to the datasheet.



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Title			
EN5330 Recommended Application Ckt with Compensation Adj			
Size	Drawing Number	Drawn By	Rev
Date	January 4, 2006	Sheet	1 of 1