

Revision	Description	Date	Approved

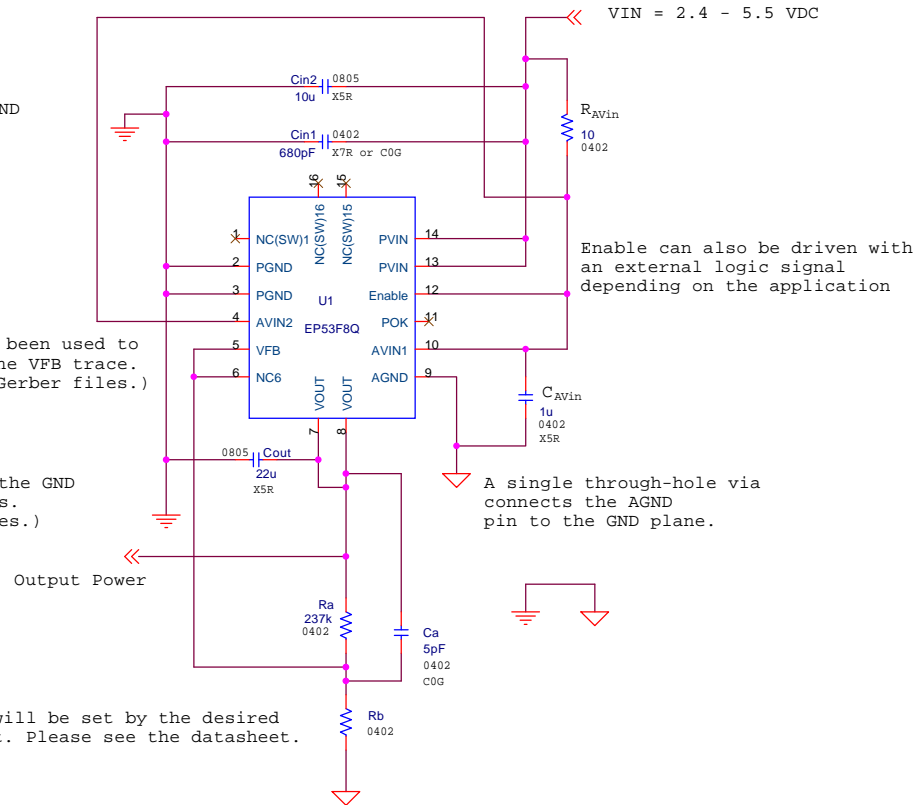
Note: This schematic only shows the critical components and traces for minimum footprint with **ENABLE** tied to AVin. Other **ENABLE** configurations, & **POK** signal need to be connected and routed according to the customer application.

Connect the input caps to the GND plane through multiple vias.
(Please see the Gerber files.)


NC pin 6 has been used to help route the VFB trace.
(Please see Gerber files.)

Connect the output cap to the GND plane through multiple vias.
(Please see the Gerber files.)

Rb will be set by the desired Vout. Please see the datasheet.



Schematic 03114
Gerbers 03115

			CONFIDENTIAL		
Title EP53F8QI Critical Components Circuit for Minimum Footprint					
Size	Drawing Number 03114			Drawn By C. Romano	Rev
Date	February 20, 2009			Sheet	of