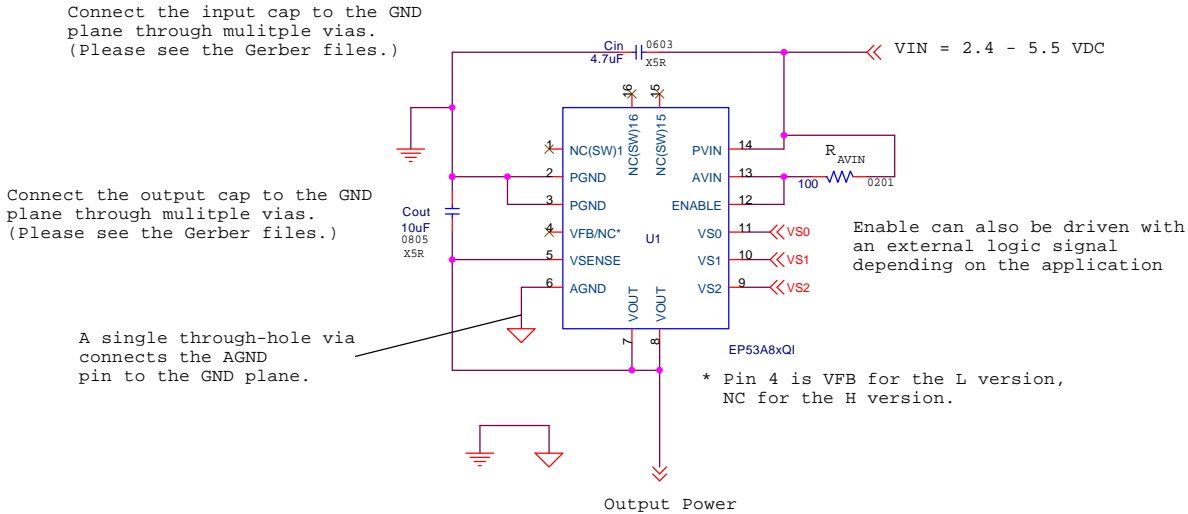


Note: This schematic only shows the critical components and traces for minimum footprint with **ENABLE** tied to AVin. Alternate **ENABLE** configurations, & other low-power pins need to be connected and routed according to the customer application.



Voltage Programming Connections		
Voltage Selector VS2 VS1 VS0	EP53A8LQI VOUT	EP53A8HQI VOUT
0 0 0	1.50V	3.3V
0 0 1	1.45V	3.0V
0 1 0	1.20V	2.9V
0 1 1	1.15V	2.6V
1 0 0	1.10V	2.5V
1 0 1	1.05V	2.2V
1 1 0	0.80V	2.1V
1 1 1	External Divider*	1.8V

0 = Connect to the GND plane.  
1 = Connect to Vin.  
\* Not shown. Refer to the datasheet.

Schematic 04591  
Gerbers 04592

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Title EP53A8xQI Critical Components Circuit for Minimum Footprint				
Size	Drawing Number	04591	Drawn By	Rev
			C. Romano	
Date	June 16, 2010			Sheet of
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