

Intel® Embedded University Program

Recommended Pre-work

Create an Intel® Embedded Design Center (Intel® EDC) Account:

You will need your NDA account number to register for a privileged account if you wish to access confidential collateral. Otherwise you can register for a basic user account.

1. Go to <http://edc.intel.com> and click on "Register" in the upper right corner to create an account. Then choose "Privileged Registration" or "Basic Registration." Fill out the information. The EDC will email a password to you.

Free Online x86 Architecture Training From Mindshare:

Abstract: This is your opportunity to take the Intro to Intel® 32/64-bit x86 Architecture course at no cost. The approximately 3-hour module is divided into two parts and provides an overview of the x86 instruction set, register set and operating modes such as real, protected and 64-bit modes. The modules also introduce paging, interrupt handling and Intel virtualization technology.

1. Login to the Intel Embedded Design Center.
2. Go to this link to view the two courses. Note: this training does not require Intel EDC NDA account privileges.
https://www-ssl.intel.com/p/s/en_US/embedded/designcenter/training/mindshare

Part 1 - Click on the link titled "[Intro to Intel® 32/64-bit x86 Architecture Part 1 of 2.](#)" The course will start playing immediately.

Part 2 – Click on the link titled "[Intro to Intel® 32/64-bit x86 Architecture Part 2 of 2.](#)" You will be redirected to the Mindshare course Web site. Register for a Mindshare account and use the "Offering ID" of [IntelEDC0119](#) to view this course free of charge. Mindshare will send an email to you with directions to access this course.

Introduction to Intel® Architecture: The Basics (Intel® Embedded Design Center White Paper)

The goal of this paper is to describe the basic operation and function to the platform for the ingredients used in three classes of Intel® architecture platforms: Intel® Atom™ processor, Intel® Core™ 2 Duo processor, and the Intel® Core™ i7 processor.

<http://download.intel.com/design/intarch/papers/321087.pdf?iid=1042>